AN5560

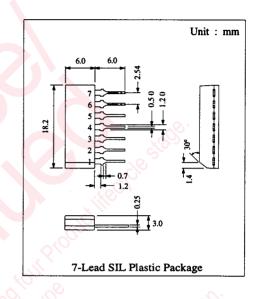
TV 50Hz and 60Hz Detection Circuit

Description

The AN5560 is an integrated circuit designed for vertical synchronizing signals 50Hz/60Hz detection of TV Signal.

Features

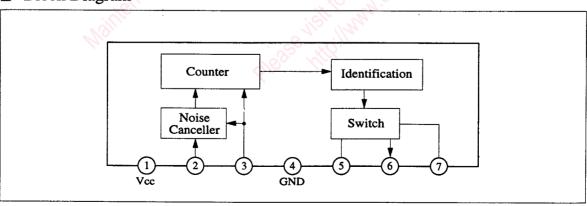
- AN5560 detects whether Vertical Synchronous signal's frequency is 50Hz or 60Hz by taking in Horizontal and Vertical Synchronous signals
- Detected frequency is indicated High and Low DC Voltage
- Manual switch is included to change detected output DC levels



■ Pin Descriptions

Pin No.	Pin Name
1	Vcc
2	Vertical Pulse Input
3	Horizontal Pulse Input
4	GND
5	Manual Switch
6	50/60Hz Detected Output
7	Output Hold

■ Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

Item Supply Voltage		Symbol	Rating 14.4		Unit
		Vcc			
		V ₂₋₄	V ₁₋₄ + 0.4	0	v
Voltage		V _{3, 5-4}	V ₁₋₄	0	V
		V ₆₋₄	24	0	V
Current		I ₂	5	-0.8	mA
		I ₆	20		mA
Power Dissipat	ion (Ta = 70 °C)	P_{D}	340 r		mW
Temperature	Operating Ambient Temperature	Topr	-20 ~ +70		°C
	Storage Temperature	Tstg -55 ~ +150		-150	°C

■ Electrical Characteristics (Vcc=12V, Ta=25°C)

Item	Symbol	Test Cct.	Condition	min.	typ.	max.	Unit
Total Current	Itotal	1		7.5	10.0	12.5	mA
50/60 Detection Output Auto 50	V _{Auto 50}	2	SW1=2, SW2=1, SW3=2	11.9		12.0	V
50/60 Detection Output Auto 60	V _{Auto} 60	2	SW1=2, SW2=2, SW3=2	Þ		0.3	v
50/60 Detection Output Manual 50	V _{manual} 50	2	SW1=2, SW3=3	11.9		12.0	V
50/60 Detection Output Manual 60	V _{manual 60}	2	SW1=2, SW3=1			0.3	V
Horizontal Frequency Input Range	f _H	2	10),	15.6		16.0	kHz
Vert. Freq. Input Range 50Hz	f _{V 50}	2	9/12 6/11:	fн/346	fн/312	fн/284	Hz
Vert. Freq. Input Range 60Hz	f _{V 60}	2	10M, CO.	fн/282	fн/260	fn/243	Hz
Horizontal Pulse Input Level	Ufh	2	UfH Pin 3 open	2	×C	5	V
Vertical Pulse Input Level	Ufv	2	Ufv Ufv	0	S. Illi	Vcc-3.0	v
Horizontal Pulse Width	t _{w(fH)}	2	y la la silly the	4	, %	12	μs
Vertical Pulse Width	$t_{w(fV)}$	2	Les ille our est	150	10.	500	μs

Note: Operating supply voltage range: Vcc (opr) = 9.6V ~14.4V

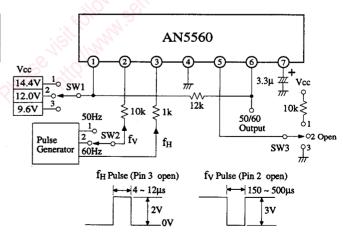
Test Circuit 1 (Icc)

AN5560

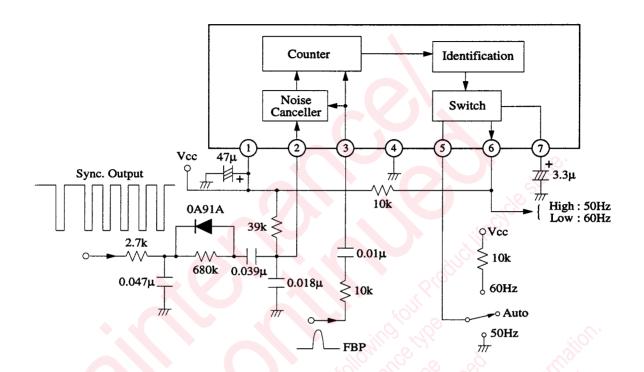
1 2 3 4 5 6 7

Itotal A 820k 10k # 12k

Test Circuit 2 (v_{Auto} , v_{manual} , f_H , f_V , v_{fH} , v_{fV} , $t_{W(fH)}$)



■ Application Circuit



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