□ MN1871681

Туре		MN1871681				
ROM (×8-Bit)		16 K 320 0.5 μs at 2/3 frequency dividing (at 4.5 V to 5.5 V, 12 MHz) • RESET • External 0 • External 1 • Timer 0 • Timer 1 • Timer 2 • I²C • Remote Control • MOSD				
RAM (×8-Bit)						
Minimum Instruction Executi	on Time					
Interrupts						
Timer Counter		Timer Counter 0 : 8-Bit × 1 Clock Source . 1/1, 1/4, 1/16, 1/64 of System Clock Interrupt Source Overflow of Timer Counter 0				
		Timer Counter 1 : 8-Bit × 1 Clock Source 1/2, 1/16, 1/64, 1/256, 1/512 of System Clock Interrupt Source Overflow of Timer Counter 1				
		Time Base Counter Clock Source 1/4096 of System Clock Interrupt Source 1/1, 1/2, 1/4, 1/8 of Timer Counter 2				
		Watchdog Timer				
Serial Interface		I ² C × 1 (Two bus line system)				
I/0 Pins I/0	20	Common use 12 • General use 8				
Input	1	Common use 1				
A/D Inputs		5-Bit × 10ch (without S/H)				
PWM		14-Bit × 1ch, 8-Bit × 5ch (All PWM are 5 V , not connectable to 12 V systems)				
Special Ports		Hsync detection, Remote Control Reception				
CRTC		Single OSD built-in (Menu OSD 12×18 dots 256 letters)				
Notes		Remote Control Data Detection Circuit built-in				
Package		SDIP042-P-0600				
Electrical Characteristics						

A/D Converter Characteristics

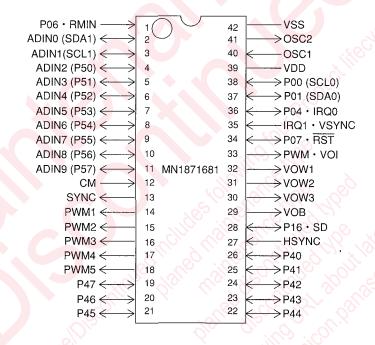
Parameter	Symbol	Condition	min	Limit typ	max	Unit
A/D Conversion Time	TAD	rosc = 12 MHz	9			μs
Analog Input Voltage	VAD		VSS		VDD	٧

(Ta = -20 °C to +70 °C, VDD = 5.0 V, VSS = 0 V)

Support Tool

In-Circuit Emulator	PX-ICE1870 / 80+PX-PRB1876476			
EPROM built-in Type	Туре	MN18P73284		
	ROM (× 8-Bit)	32 K		
	RAM (× 8-Bit)	640		
	Minimum Instruction Execution Time	0 5 μs (at 4 5 V to 5 5 V, 12 MHz)		
	Package	SDIP042-P-0600		
	1			

Pin Assignment



SDIP042-P-0600

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