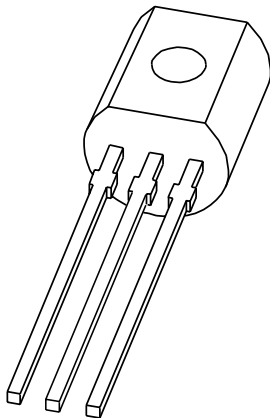


DATA SHEET



BF483; BF485; BF487 NPN high-voltage transistors

Product specification
Supersedes data of 1996 Dec 09

1999 Apr 12

NPN high-voltage transistors

BF483; BF485; BF487

FEATURES

- Low feedback capacitance.

APPLICATIONS

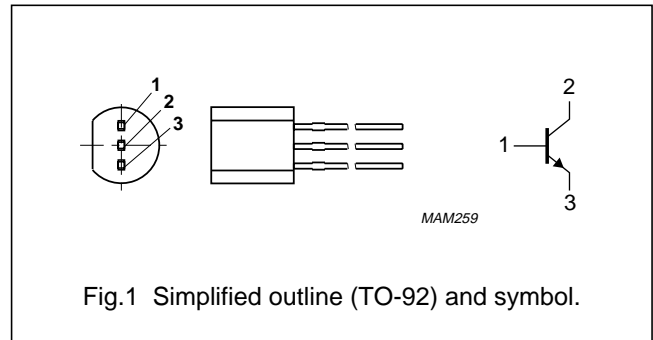
- Intended for use in video output stages in black-and-white and in colour television receivers.

DESCRIPTION

NPN transistors in a TO-92 plastic package.
PNP complement: BF488

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BF483		–	300	V
	BF485		–	350	V
	BF487		–	400	V
V _{CEO}	collector-emitter voltage	open base			
	BF483		–	250	V
	BF485		–	300	V
	BF487		–	350	V
V _{EBO}	emitter-base voltage	open collector	–	5	V
I _C	collector current (DC)		–	100	mA
I _{CM}	peak collector current		–	200	mA
I _{BM}	peak base current		–	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	–	830	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on a printed-circuit board.

NPN high-voltage transistors

BF483; BF485; BF487

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	150	K/W

Note

1. Transistor mounted on a printed-circuit board.

CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = 300\text{ V}$	–	20	nA
		$I_E = 0; V_{CB} = 250\text{ V}; T_j = 150\text{ °C}$	–	20	μA
I_{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 5\text{ V}$	–	100	nA
h_{FE}	DC current gain	$I_C = 25\text{ mA}; V_{CE} = 20\text{ V}$	50	–	
		$I_C = 40\text{ mA}; V_{CE} = 20\text{ V}$	20	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 30\text{ mA}; I_B = 5\text{ mA}$	–	600	mV
C_{re}	feedback capacitance	$I_C = I_c = 0; V_{CE} = 30\text{ V}; f = 1\text{ MHz}$	–	1.4	pF
f_T	transition frequency	$I_C = -10\text{ mA}; V_{CE} = 10\text{ V}; f = 100\text{ MHz}$	70	110	MHz

NPN high-voltage transistors

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	c	D	d	E	e	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT54		TO-92	SC-43		97-02-28

NPN high-voltage transistors

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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NPN high-voltage transistors

BF483; BF485; BF487

NOTES

NPN high-voltage transistors

BF483; BF485; BF487

NOTES

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