



Product Datasheet

SDK-EVAL-E2G-R2

Enfora Enabler IIG Evaluation/Carrier Board

Product Overview

Full blown RoHS compliant development/evaluation board for use with the Enfora Enabler II Family Modules (**with SIM holder**) GSM0116-01, GSM0108-01, EDG0108-01 or MLG0208-01.

Powered USB or serial interfaces - selectable by jumper settings
Complete with headers for audio, comms, GPIO and DAC/ADC for connection and testing. Includes speaker and microphone jacks for audio testing.
Can be used as a carrier card for small build designs or schematic available for larger designs. Ideal for evaluation of all the Enfora Enabler II modules or a low cost prototype option.

Specifications

Size: 58 x 102 mm
Power: 3.6V DC (*please see Enfora Integrator Guide*)
SUPPLIED VIA USB OR SERIAL INTERFACES

Connections:

J9 - 10 pin serial comms header (see page 2 for description)
or

Powered Mini-USB connector (cable supplied)

Headers (details see page 2):

Audio (microphone, speakers and gain control)

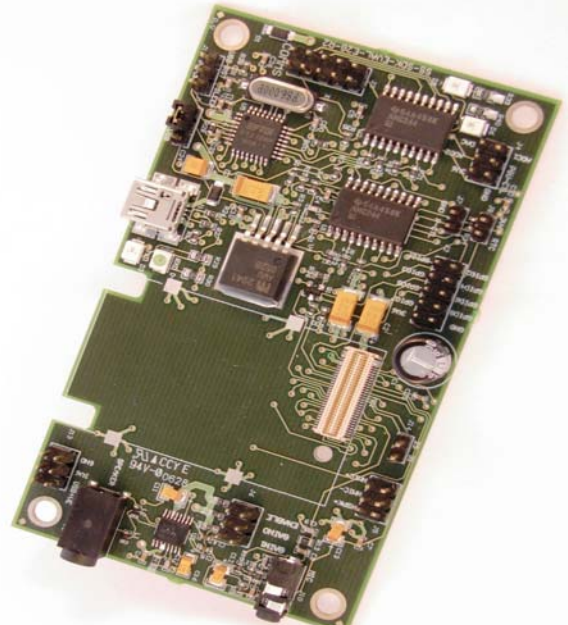
TTL Serial

i/o

RTC

Power Control

USB or TTL comms control



Above shows populated board without Enabler IIG Module in position

Header Description - see page 2

Block Diagram - see page 3

- USB drivers supplied for PC connection using mini USB connector and supplied cable.
- Additional drivers available on-line for older Windows, CE.Net and non Microsoft Operating Systems at: <http://www.ftdichip.com/drivers/VCP.htm>
- Double cut-out for each of antenna connections - 2 antenna needed for MLG0208-01
- Supplied with J1 and J7 in place for USB connections (J1 should be moved for TTL communications)
- Enabler module and pin-header connectors NOT supplied.
- 2.5 mm Microphone Jack socket fitted
- 3.5 mm Speaker Jack socket fitted
- DO NOT CONNECT J14 - RESERVED PINS FOR FACTORY USE ONLY

Part Numbers

SDK-EVAL-E2G-R2 EVALUATION BOARD
SDK-EVAL-MLG-R2 EVALUATION BOARD & ACCESSORIES FOR THE MLG0208-01 (NOV 2006)

| | |
|--------------|-----------------|
| Datasheet | SDK-EVAL-E2G-R2 |
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Simple Solutions

(because design's complicated enough)

SS-SDK-EVAL-E2G-R2 Header Descriptions

| | |
|-----|---|
| USB | 1 |
| 3V6 | 2 |
| TTL | 3 |

J1 - USB/TTL

| | | | |
|--------|---|----|--------|
| GPIO 1 | 1 | 2 | GPIO 2 |
| GPIO 3 | 3 | 4 | GPIO 4 |
| GPIO 5 | 5 | 6 | GPIO 6 |
| GPIO 7 | 7 | 8 | GPIO 8 |
| 3V6 | 9 | 10 | GND |

J2 - GPIO

| | | | |
|------|---|----|-------|
| +5V | 1 | 2 | GND |
| TXD | 3 | 4 | RXD |
| DTR | 5 | 6 | DCD |
| RTS | 7 | 8 | CTS |
| RING | 9 | 10 | RESET |

J3 - Comms

| | | | |
|------|---|---|---------|
| DAC | 1 | 2 | ADC1 |
| ADC2 | 3 | 4 | PSU-CTL |
| 3V6 | 5 | 6 | GND |

J4 - DAC

| | | |
|-------|---|-----|
| HMIC+ | 1 | GND |
| HMIC- | 2 | GND |
| HSPK+ | 3 | GND |

Jumper 5 - Audio

| | | | |
|---------|---|---|-----|
| ENABLE | 1 | 2 | GND |
| GAIN-S0 | 3 | 4 | GND |
| GAIN-S1 | 5 | 6 | GND |

J6 - GAIN

| | |
|---------|---|
| PWR_CTL | 1 |
| GND | 2 |

J7 - PWR_CTL

| | |
|------|---|
| GND | 1 |
| VBAK | 2 |

J8 - RTC

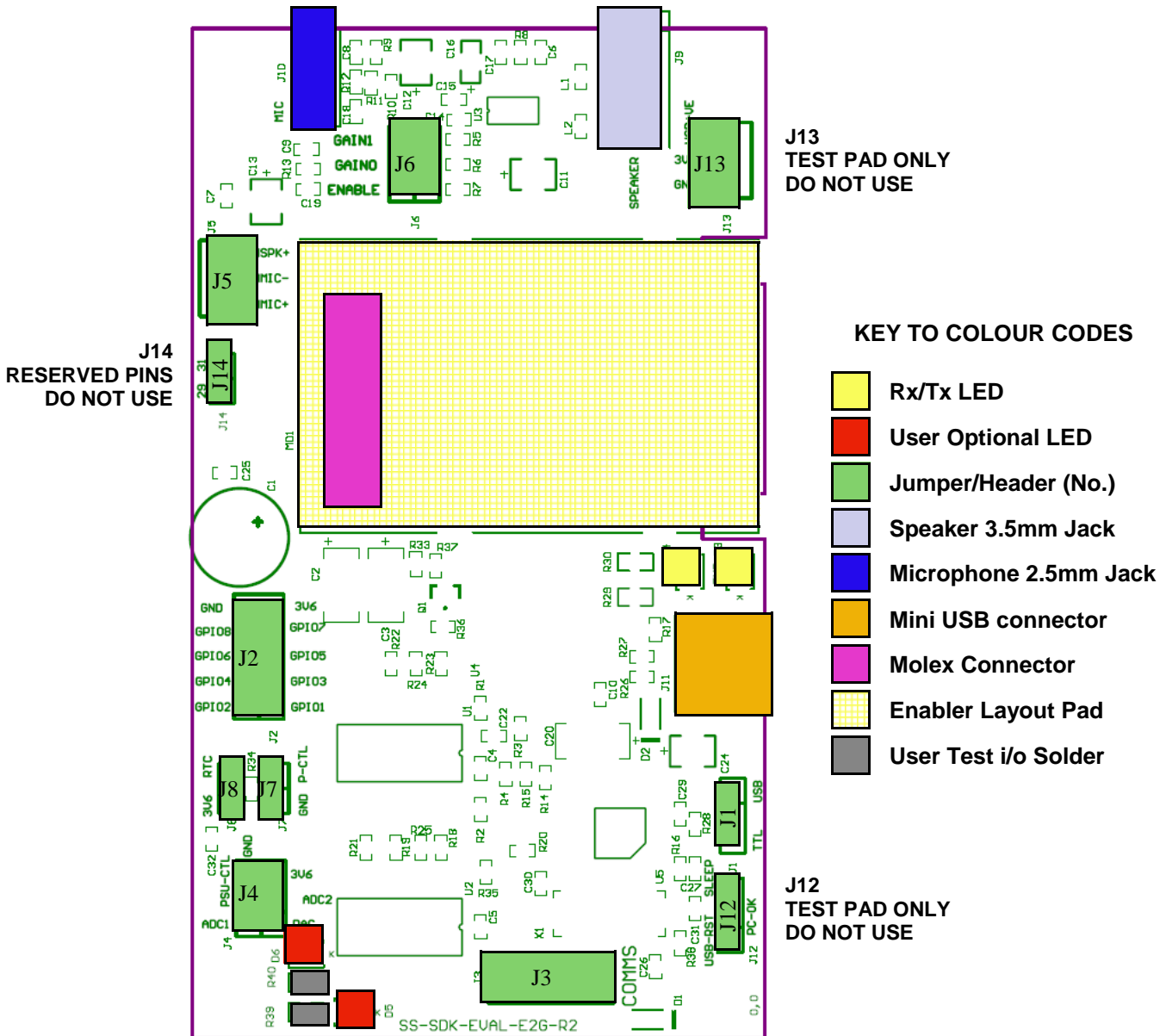
| | |
|---------|---|
| USB_RST | 1 |
| PC_OK | 2 |
| SLEEP | 3 |

J12 - DO NOT USE

| | | | |
|---------|---|---|---------|
| USB +VE | 1 | 2 | USB +VE |
| 3V6 | 3 | 4 | 3V6 |
| GND | 5 | 6 | GND |

J13 - DO NOT USE

SDK-EVAL-E2G-R2 Board Layout



Important notes (please read first):

1. When using USB for comms J1 must be in USB+3V6 position, for TTL comms J1 must be in TTL+3V6 position.
2. Install the driver software for the USB com port before connecting to PC.
3. J7 (PWR_CTL) must be fitted unless external control used.
4. Serial Comms as per Enfora Integrator Guide (115,200 8/N/1 Hardware control)
5. LED's marked in RED above are for use via test pads 1 & 2 (marked in GREY) are designed for customer use with i/o and will need externally connecting via soldered wire to selected i/o pin.
6. Header J14 is for factory reserved PINS 29 and 31 - DO NOT USE

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