

**AUTOMOTIVE MOSFET**

**IRF2807ZPbF**

**IRF2807ZSPbF**

**IRF2807ZLPbF**

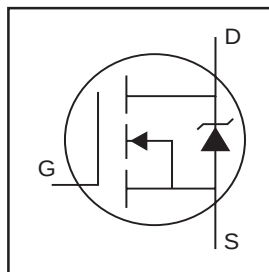
HEXFET® Power MOSFET

**Features**

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free

**Description**

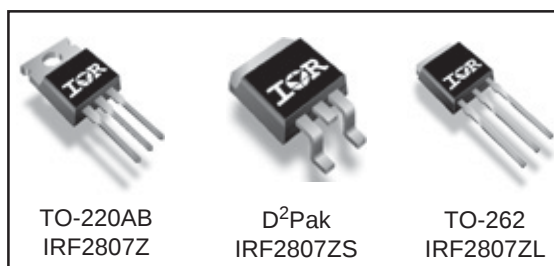
Specifically designed for Automotive applications, this HEXFET® Power MOSFET utilizes the latest processing techniques to achieve extremely low on-resistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.



$$V_{DS} = 75V$$

$$R_{DS(on)} = 9.4m\Omega$$

$$I_D = 75A$$



**Absolute Maximum Ratings**

|                             | Parameter                                                  | Max.                   | Units |
|-----------------------------|------------------------------------------------------------|------------------------|-------|
| $I_D$ @ $T_C = 25^\circ C$  | Continuous Drain Current, $V_{GS}$ @ 10V (Silicon Limited) | 89                     | A     |
| $I_D$ @ $T_C = 100^\circ C$ | Continuous Drain Current, $V_{GS}$ @ 10V (See Fig. 9)      | 63                     |       |
| $I_D$ @ $T_C = 25^\circ C$  | Continuous Drain Current, $V_{GS}$ @ 10V (Package Limited) | 75                     |       |
| $I_{DM}$                    | Pulsed Drain Current ①                                     | 350                    |       |
| $P_D$ @ $T_C = 25^\circ C$  | Maximum Power Dissipation                                  | 170                    | W     |
|                             | Linear Derating Factor                                     | 1.1                    | W/°C  |
| $V_{GS}$                    | Gate-to-Source Voltage                                     | ± 20                   | V     |
| $E_{AS}$                    | Single Pulse Avalanche Energy (Thermally Limited) ②        | 160                    | mJ    |
| $E_{AS}$ (tested)           | Single Pulse Avalanche Energy Tested Value ②               | 200                    |       |
| $I_{AR}$                    | Avalanche Current ③                                        | See Fig.12a,12b,15,16  | A     |
| $E_{AR}$                    | Repetitive Avalanche Energy ④                              |                        | mJ    |
| $T_J$<br>$T_{STG}$          | Operating Junction and<br>Storage Temperature Range        | -55 to + 175           | °C    |
|                             | Soldering Temperature, for 10 seconds                      | 300 (1.6mm from case ) |       |
|                             | Mounting torque, 6-32 or M3 screw                          | 10 lbf•in (1.1N•m)     |       |

**Thermal Resistance**

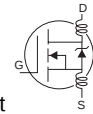
|                 | Parameter                                      | Typ. | Max. | Units |
|-----------------|------------------------------------------------|------|------|-------|
| $R_{\theta JC}$ | Junction-to-Case                               | —    | 0.90 | °C/W  |
| $R_{\theta CS}$ | Case-to-Sink, Flat, Greased Surface            | 0.50 | —    |       |
| $R_{\theta JA}$ | Junction-to-Ambient                            | —    | 62   |       |
| $R_{\theta JA}$ | Junction-to-Ambient (PCB Mount, steady state)⑤ | —    | 40   |       |

HEXFET® is a registered trademark of International Rectifier.

www.irf.com

## Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

|                              | Parameter                            | Min. | Typ.  | Max. | Units              | Conditions                                                                  |
|------------------------------|--------------------------------------|------|-------|------|--------------------|-----------------------------------------------------------------------------|
| $V_{(BR)DSS}$                | Drain-to-Source Breakdown Voltage    | 75   | —     | —    | V                  | $V_{GS} = 0V, I_D = 250\mu A$                                               |
| $\Delta BV_{DSS}/\Delta T_J$ | Breakdown Voltage Temp. Coefficient  | —    | 0.073 | —    | $V/^\circ\text{C}$ | Reference to $25^\circ\text{C}, I_D = 1\text{mA}$                           |
| $R_{DS(on)}$                 | Static Drain-to-Source On-Resistance | —    | 7.5   | 9.4  | $m\Omega$          | $V_{GS} = 10V, I_D = 53A$ ④                                                 |
| $V_{GS(th)}$                 | Gate Threshold Voltage               | 2.0  | —     | 4.0  | V                  | $V_{DS} = V_{GS}, I_D = 250\mu A$                                           |
| $g_{fs}$                     | Forward Transconductance             | 67   | —     | —    | S                  | $V_{DS} = 25V, I_D = 53A$                                                   |
| $I_{DSS}$                    | Drain-to-Source Leakage Current      | —    | —     | 20   | $\mu A$            | $V_{DS} = 75V, V_{GS} = 0V$                                                 |
|                              |                                      | —    | —     | 250  |                    | $V_{DS} = 75V, V_{GS} = 0V, T_J = 125^\circ\text{C}$                        |
| $I_{GSS}$                    | Gate-to-Source Forward Leakage       | —    | —     | 200  | nA                 | $V_{GS} = 20V$                                                              |
|                              | Gate-to-Source Reverse Leakage       | —    | —     | -200 |                    | $V_{GS} = -20V$                                                             |
| $Q_g$                        | Total Gate Charge                    | —    | 71    | 110  | nC                 | $I_D = 53A$                                                                 |
| $Q_{gs}$                     | Gate-to-Source Charge                | —    | 19    | 29   |                    | $V_{DS} = 60V$                                                              |
| $Q_{gd}$                     | Gate-to-Drain ("Miller") Charge      | —    | 28    | 42   |                    | $V_{GS} = 10V$ ④                                                            |
| $t_{d(on)}$                  | Turn-On Delay Time                   | —    | 18    | —    | ns                 | $V_{DD} = 38V$                                                              |
| $t_r$                        | Rise Time                            | —    | 79    | —    |                    | $I_D = 53A$                                                                 |
| $t_{d(off)}$                 | Turn-Off Delay Time                  | —    | 40    | —    |                    | $R_G = 6.2\Omega$                                                           |
| $t_f$                        | Fall Time                            | —    | 45    | —    |                    | $V_{GS} = 10V$ ④                                                            |
| $L_D$                        | Internal Drain Inductance            | —    | 4.5   | —    | nH                 | Between lead,<br>6mm (0.25in.)<br>from package<br>and center of die contact |
| $L_S$                        | Internal Source Inductance           | —    | 7.5   | —    |                    |                                                                             |
| $C_{iss}$                    | Input Capacitance                    | —    | 3270  | —    | pF                 | $V_{GS} = 0V$                                                               |
| $C_{oss}$                    | Output Capacitance                   | —    | 420   | —    |                    | $V_{DS} = 25V$                                                              |
| $C_{rss}$                    | Reverse Transfer Capacitance         | —    | 240   | —    |                    | $f = 1.0\text{MHz}$ , See Fig. 5                                            |
| $C_{oss}$                    | Output Capacitance                   | —    | 1590  | —    |                    | $V_{GS} = 0V, V_{DS} = 1.0V, f = 1.0\text{MHz}$                             |
| $C_{oss}$                    | Output Capacitance                   | —    | 280   | —    |                    | $V_{GS} = 0V, V_{DS} = 60V, f = 1.0\text{MHz}$                              |
| $C_{oss \text{ eff.}}$       | Effective Output Capacitance         | —    | 440   | —    |                    | $V_{GS} = 0V, V_{DS} = 0V \text{ to } 60V$                                  |

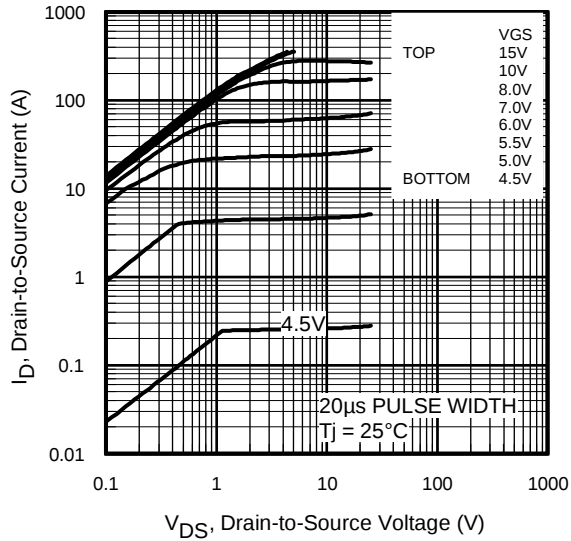


## Diode Characteristics

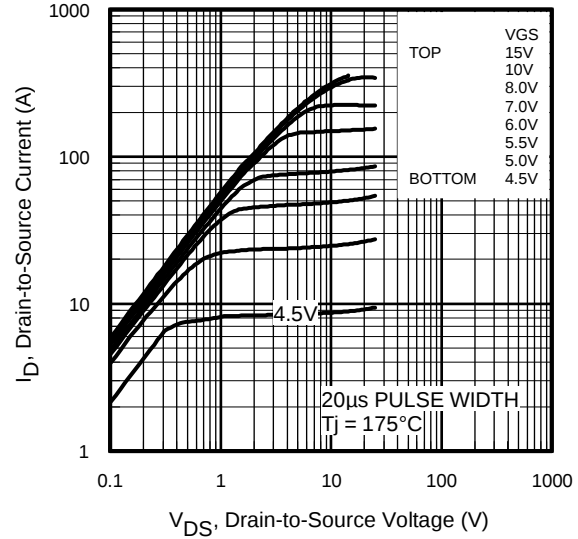
|          | Parameter                                 | Min.                                                                      | Typ. | Max. | Units | Conditions                                                              |
|----------|-------------------------------------------|---------------------------------------------------------------------------|------|------|-------|-------------------------------------------------------------------------|
| $I_S$    | Continuous Source Current<br>(Body Diode) | —                                                                         | —    | 89   | A     | MOSFET symbol<br>showing the<br>integral reverse<br>p-n junction diode. |
| $I_{SM}$ | Pulsed Source Current<br>(Body Diode) ①   | —                                                                         | —    | 350  |       |                                                                         |
| $V_{SD}$ | Diode Forward Voltage                     | —                                                                         | —    | 1.3  | V     | $T_J = 25^\circ\text{C}, I_S = 53A, V_{GS} = 0V$ ④                      |
| $t_{rr}$ | Reverse Recovery Time                     | —                                                                         | 46   | 69   | ns    | $T_J = 25^\circ\text{C}, I_F = 53A, V_{DD} = 25V$                       |
| $Q_{rr}$ | Reverse Recovery Charge                   | —                                                                         | 80   | 120  | nC    | $di/dt = 100A/\mu s$ ④                                                  |
| $t_{on}$ | Forward Turn-On Time                      | Intrinsic turn-on time is negligible (turn-on is dominated by $L_S+L_D$ ) |      |      |       |                                                                         |

### Notes:

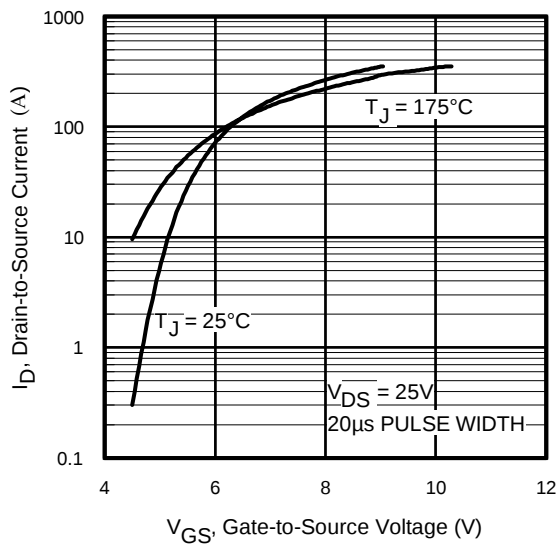
- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11).
- ② Limited by  $T_{Jmax}$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.12\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 53A$ ,  $V_{GS} = 10V$ . Part not recommended for use above this value.
- ③  $I_{SD} \leq 53A$ ,  $di/dt \leq 420A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 175^\circ\text{C}$ .
- ④ Pulse width  $\leq 1.0\text{ms}$ ; duty cycle  $\leq 2\%$ .
- ⑤  $C_{oss \text{ eff.}}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .
- ⑥ Limited by  $T_{Jmax}$ , see Fig.12a, 12b, 15, 16 for typical repetitive avalanche performance.
- ⑦ This value determined from sample failure population. 100% tested to this value in production.
- ⑧ This is applied to D<sup>2</sup>Pak, when mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.



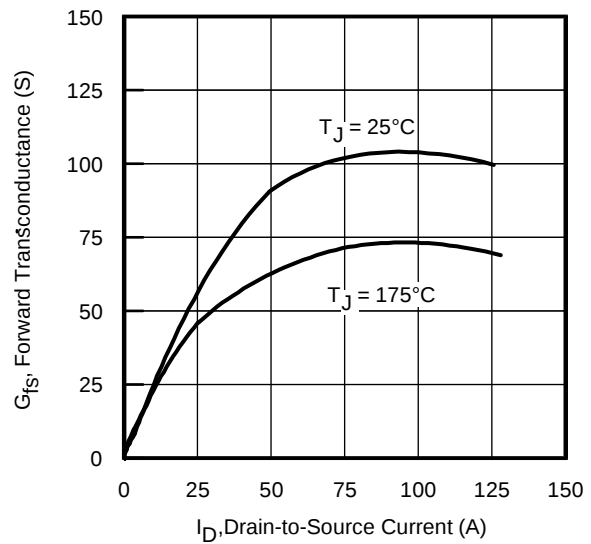
**Fig 1.** Typical Output Characteristics



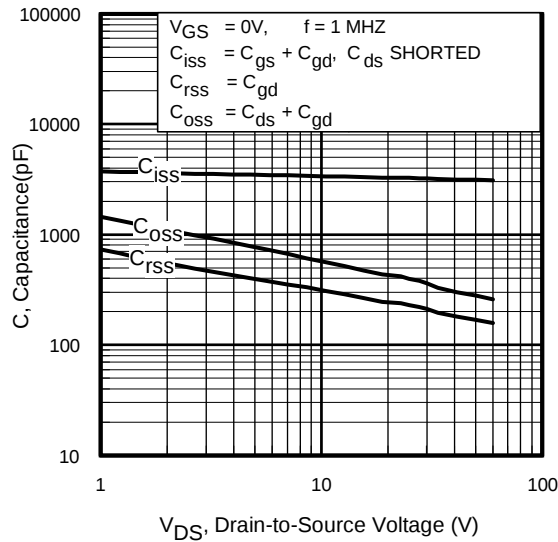
**Fig 2.** Typical Output Characteristics



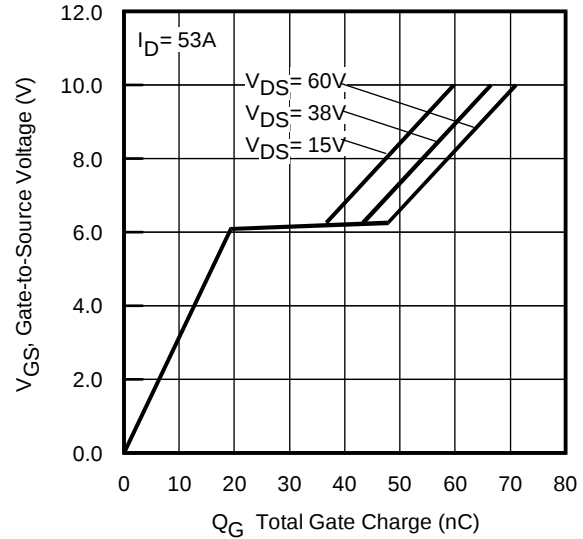
**Fig 3.** Typical Transfer Characteristics



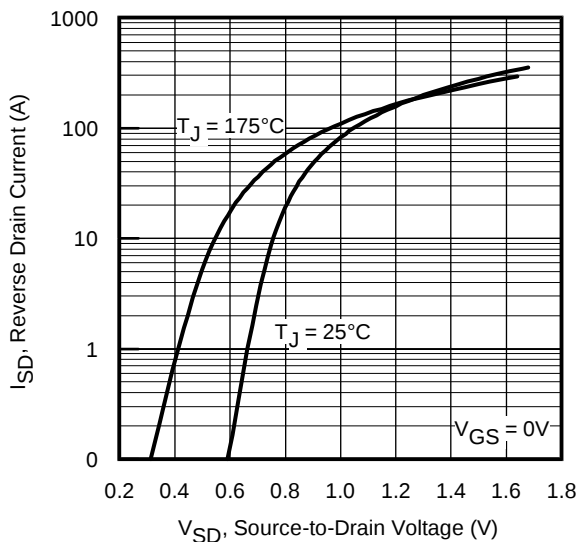
**Fig 4.** Typical Forward Transconductance vs. Drain Current



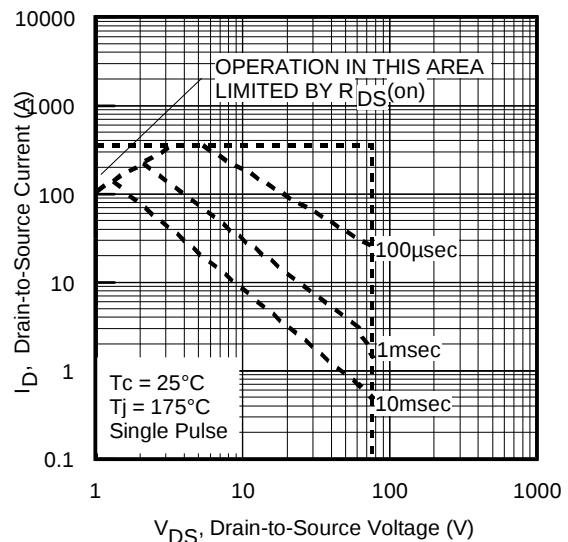
**Fig 5.** Typical Capacitance vs. Drain-to-Source Voltage



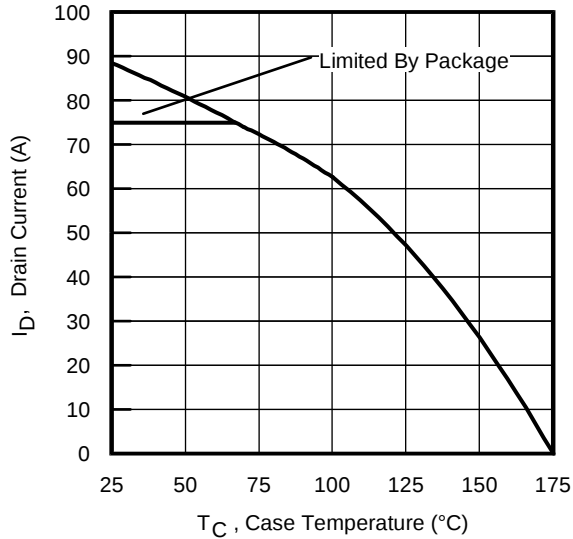
**Fig 6.** Typical Gate Charge vs. Gate-to-Source Voltage



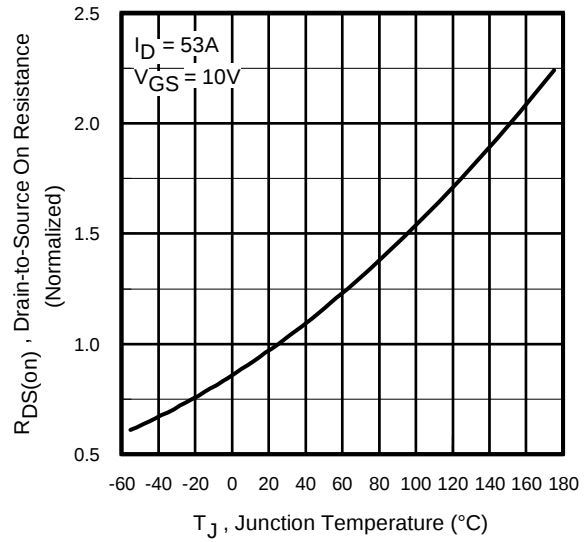
**Fig 7.** Typical Source-Drain Diode Forward Voltage



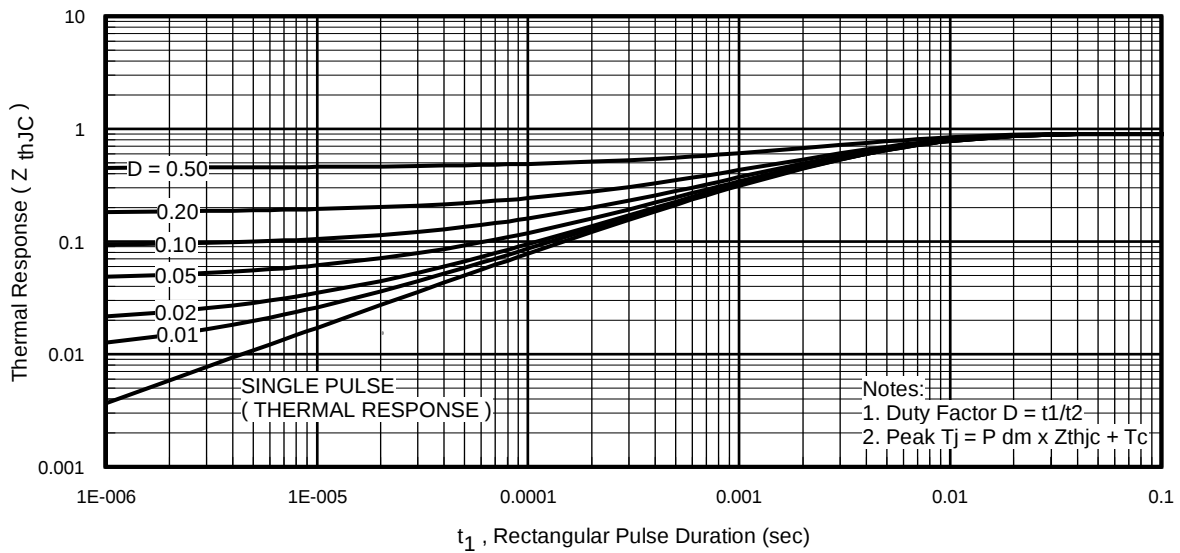
**Fig 8.** Maximum Safe Operating Area



**Fig 9.** Maximum Drain Current vs. Case Temperature

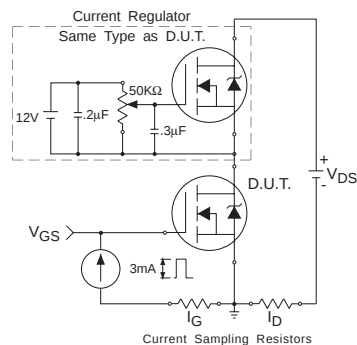
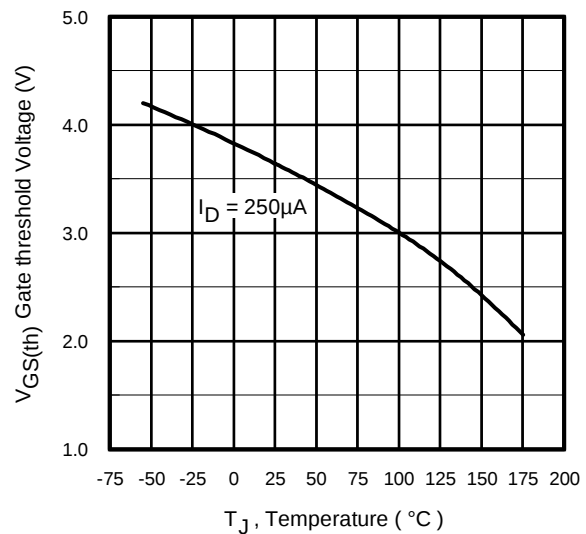
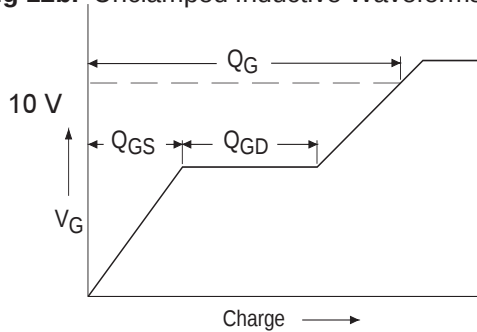
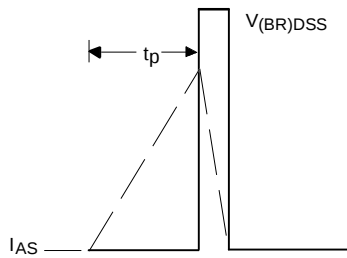
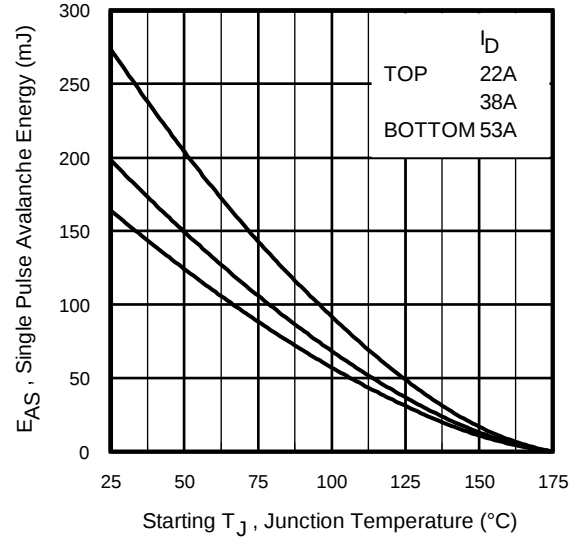
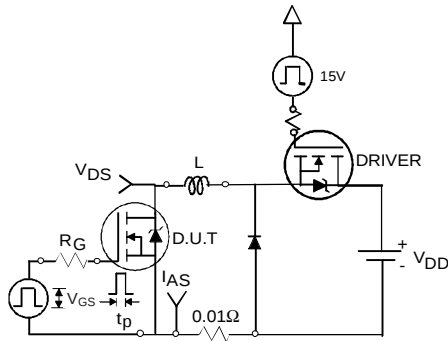


**Fig 10.** Normalized On-Resistance vs. Temperature



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

International  
**IOR** Rectifier



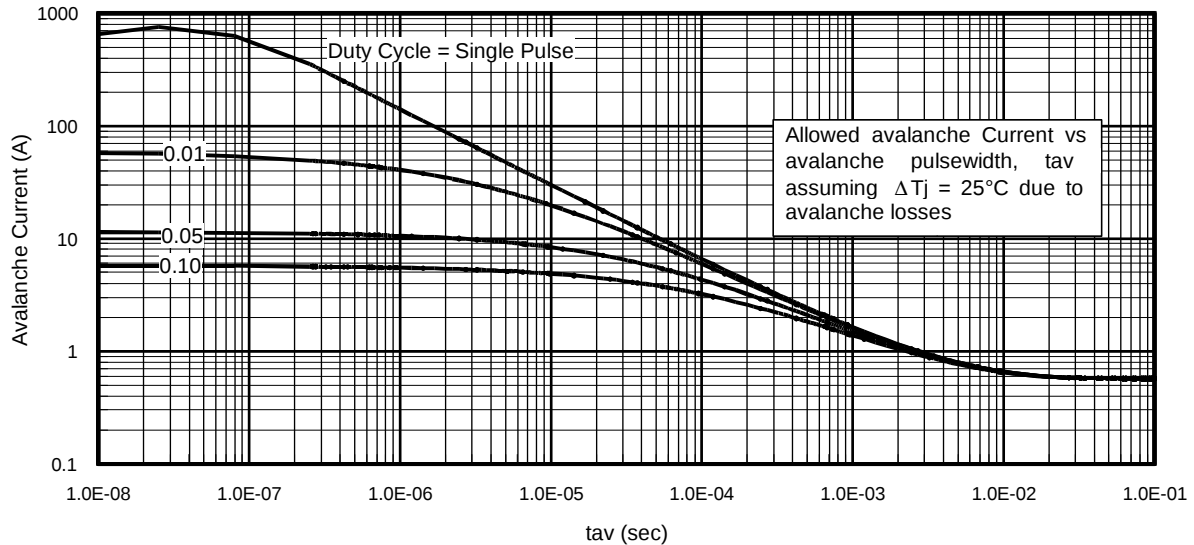


Fig 15. Typical Avalanche Current vs. Pulsewidth

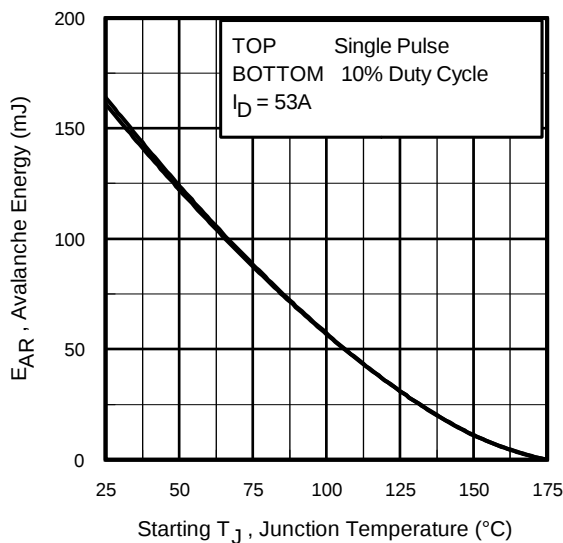


Fig 16. Maximum Avalanche Energy vs. Temperature

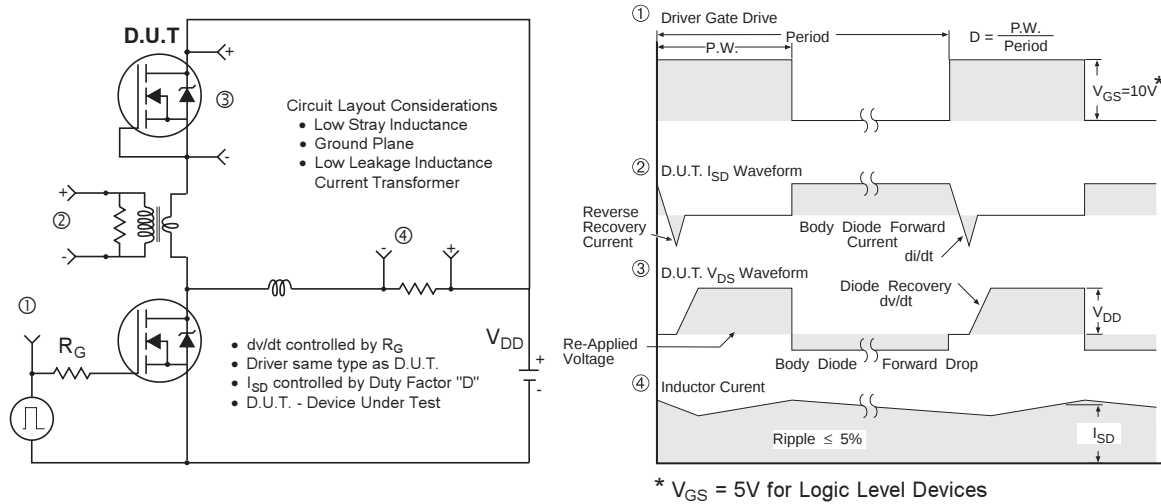
**Notes on Repetitive Avalanche Curves , Figures 15, 16:**  
(For further info, see AN-1005 at [www.irf.com](http://www.irf.com))

1. Avalanche failures assumption:  
Purely a thermal phenomenon and failure occurs at a temperature far in excess of  $T_{jmax}$ . This is validated for every part type.
2. Safe operation in Avalanche is allowed as long as  $T_{jmax}$  is not exceeded.
3. Equation below based on circuit and waveforms shown in Figures 12a, 12b.
4.  $P_{D(ave)}$  = Average power dissipation per single avalanche pulse.
5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
6.  $I_{av}$  = Allowable avalanche current.
7.  $\Delta T$  = Allowable rise in junction temperature, not to exceed  $T_{jmax}$  (assumed as 25°C in Figure 15, 16).  
 $t_{av}$  = Average time in avalanche.  
 $D$  = Duty cycle in avalanche =  $t_{av} \cdot f$   
 $Z_{thJC}(D, t_{av})$  = Transient thermal resistance, see figure 11)

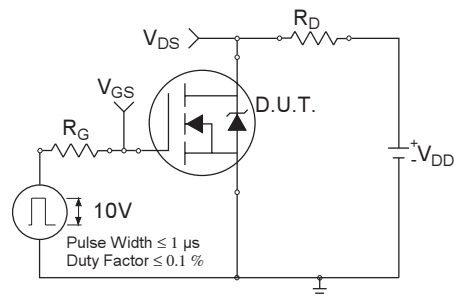
$$P_{D(ave)} = 1/2 (1.3 \cdot BV \cdot I_{av}) = \Delta T / Z_{thJC}$$

$$I_{av} = 2\Delta T / [1.3 \cdot BV \cdot Z_{th}]$$

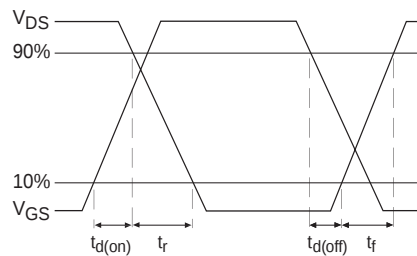
$$E_{AS(AR)} = P_{D(ave)} \cdot t_{av}$$



**Fig 17. Peak Diode Recovery  $dv/dt$  Test Circuit for N-Channel HEXFET® Power MOSFETs**



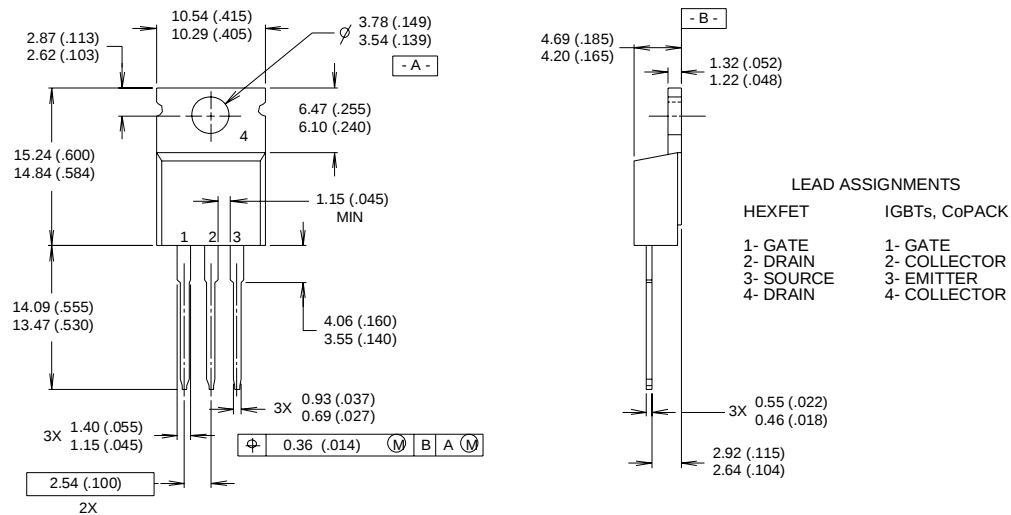
**Fig 18a. Switching Time Test Circuit**



**Fig 18b. Switching Time Waveforms**

## TO-220AB Package Outline

Dimensions are shown in millimeters (inches)



### NOTES:

- 1 DIMENSIONING & TOLERANCING PER ANSI Y14.5M, 1982.
- 2 CONTROLLING DIMENSION : INCH

- 3 OUTLINE CONFORMS TO JEDEC OUTLINE TO-220AB.
- 4 HEATSINK & LEAD MEASUREMENTS DO NOT INCLUDE BURRS.

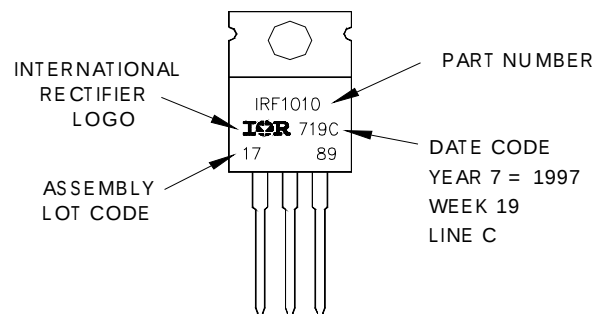
## TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010

LOT CODE 1789

ASSEMBLED ON WW 19, 1997  
IN THE ASSEMBLY LINE "C"

**Note:** "P" in assembly line  
position indicates "Lead-Free"

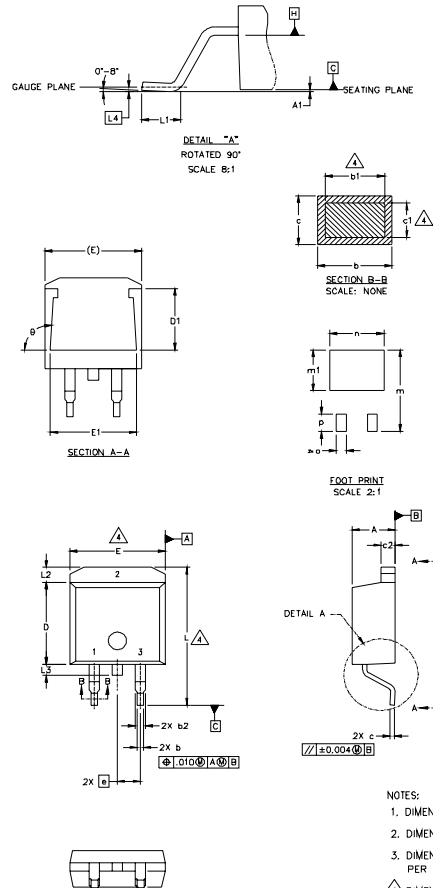


# IRF2807Z/S/LPbF

International  
**IR** Rectifier

## D<sup>2</sup>Pak Package Outline

Dimensions are shown in millimeters (inches)



| SYMBOL | DIMENSIONS  |       |        |      | NOTES |
|--------|-------------|-------|--------|------|-------|
|        | MILLIMETERS |       | INCHES |      |       |
|        | MIN.        | MAX.  | MIN.   | MAX. |       |
| A      | 4.06        | 4.83  | .160   | .190 | 4     |
| A1     |             | 0.127 |        | .005 |       |
| b      | 0.51        | 0.99  | .020   | .039 |       |
| b1     | 0.51        | 0.89  | .020   | .035 |       |
| b2     | 1.14        | 1.40  | .045   | .055 | 4     |
| c      | 0.43        | 0.63  | .017   | .025 |       |
| c1     | 0.38        | 0.74  | .015   | .029 |       |
| c2     | 1.14        | 1.40  | .045   | .055 |       |
| D      | 8.51        | 9.65  | .335   | .380 | 3     |
| D1     | 5.33        |       | .210   |      | 3     |
| E      | 9.65        | 10.67 | .380   | .420 |       |
| E1     | 6.22        |       | .245   |      |       |
| e      | 2.54        | BSC   | .100   | BSC  |       |
| L      | 14.61       | 15.88 | .575   | .625 |       |
| L1     | 1.78        | 2.79  | .070   | .110 |       |
| L2     |             | 1.65  |        | .065 |       |
| L3     | 1.27        | 1.78  | .050   | .070 |       |
| L4     | 0.25        | BSC   | .010   | BSC  |       |
| m      | 17.78       |       | .700   |      |       |
| m1     | 8.89        |       | .350   |      |       |
| n      | 11.43       |       | .450   |      |       |
| o      | 2.08        |       | .082   |      |       |
| p      | 3.81        |       | .150   |      |       |
| θ      | 90°         | 93°   | 90°    | 93°  |       |

### LEAD ASSIGNMENTS

| HEXFET     | IGBTs, CoPACK | DIODES      |
|------------|---------------|-------------|
| 1.- GATE   | 1.- GATE      | 1.- ANODE * |
| 2.- DRAIN  | 2.- COLLECTOR | 2.- CATHODE |
| 3.- SOURCE | 3.- EMITTER   | 3.- ANODE   |

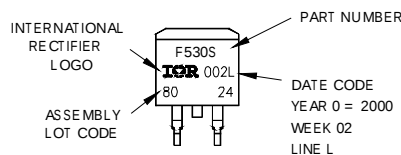
\* PART DEPENDENT.

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
  2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
  3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH; MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
  4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
  5. CONTROLLING DIMENSION: INCH.

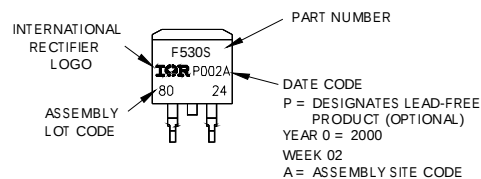
## D<sup>2</sup>Pak Part Marking Information

EXAMPLE: THIS IS AN IRF530S WITH  
LOT CODE 8024  
ASSEMBLED ON WW 02, 2000  
IN THE ASSEMBLY LINE "L"

Note: "P" in assembly line  
position indicates "Lead-Free"

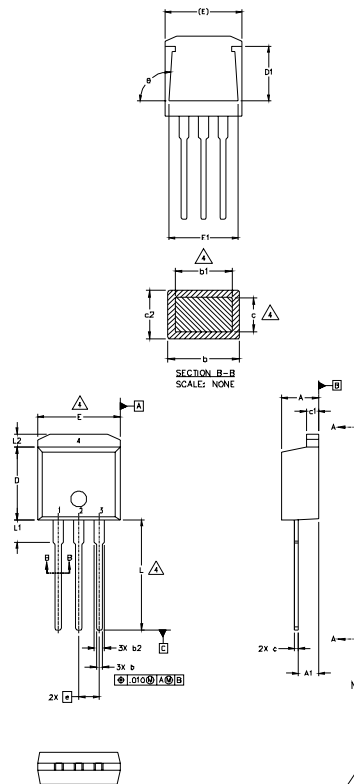


**OR**



## TO-262 Package Outline

Dimensions are shown in millimeters (inches)



| SYMBOL | DIMENSIONS  |       |          |      | NOTES |
|--------|-------------|-------|----------|------|-------|
|        | MILLIMETERS |       | INCHES   |      |       |
|        | MIN.        | MAX.  | MIN.     | MAX. |       |
| A      | 4.06        | 4.83  | .160     | .190 | 4     |
| A1     | 2.03        | 2.92  | .080     | .115 |       |
| b      | 0.51        | 0.99  | .020     | .039 |       |
| b1     | 0.51        | 0.89  | .020     | .035 |       |
| b2     | 1.14        | 1.40  | .045     | .055 | 4     |
| c      | 0.38        | 0.63  | .015     | .025 |       |
| c1     | 1.14        | 1.40  | .045     | .055 |       |
| c2     | 0.43        | .063  | .017     | .029 |       |
| D      | 8.51        | 9.65  | .335     | .380 | 3     |
| D1     | 5.33        |       | .210     |      |       |
| E      | 9.65        | 10.67 | .380     | .420 | 3     |
| E1     | 6.22        |       | .245     |      |       |
| e      | 2.54 BSC    |       | .100 BSC |      |       |
| L      | 13.46       | 14.09 | .530     | .555 |       |
| L1     | 3.56        | 3.71  | .140     | .146 |       |
| L2     |             | 1.65  |          | .065 |       |

### LEAD ASSIGNMENTS

#### HEXFET

- 1.- GATE
- 2.- DRAIN
- 3.- SOURCE
- 4.- DRAIN

#### IGBT

- 1 - GATE
- 2 - COLLECTOR
- 3 - EMITTER

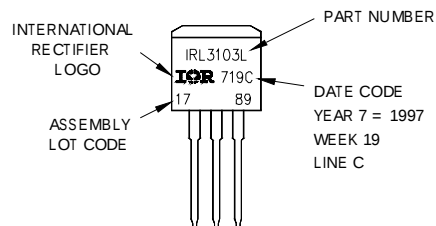
#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.

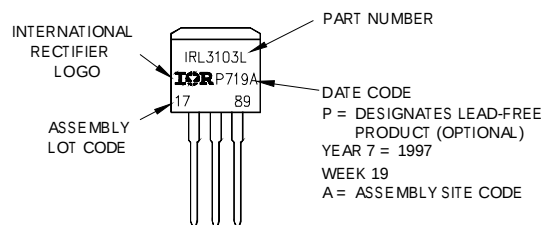
## TO-262 Part Marking Information

EXAMPLE: THIS IS AN IRL3103L  
LOT CODE 1789  
ASSEMBLED ON WW 19, 1997  
IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position indicates "Lead-Free"



OR

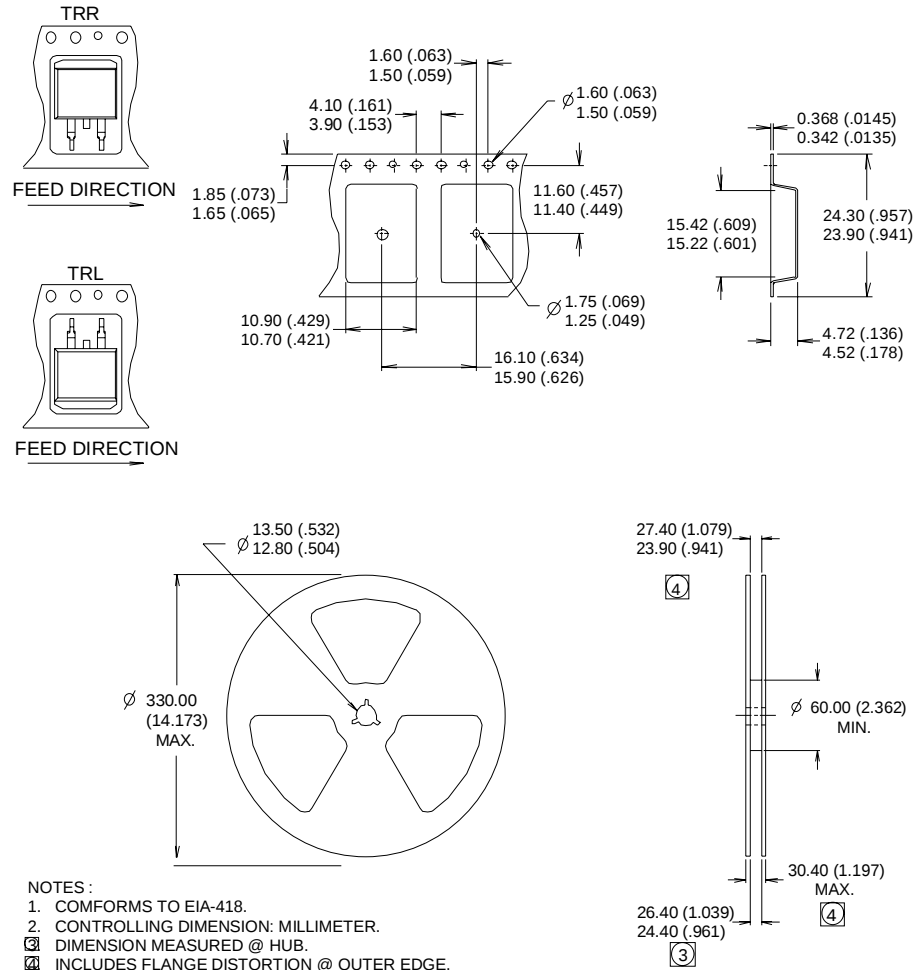


# IRF2807Z/S/LPbF

International  
**IR** Rectifier

## D<sup>2</sup>Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)



**TO-220AB package is not recommended for Surface Mount Application.**

Data and specifications subject to change without notice.  
 This product has been designed and qualified for the Automotive [Q101] market.  
 Qualification Standards can be found on IR's Web site.

International  
**IR** Rectifier

**IR WORLD HEADQUARTERS:** 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105  
 TAC Fax: (310) 252-7903

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