

LVDS CLOCK OSCILLATORS

SMD-version 2,5V 3,3V

model	KXO-V65
frequency range	19,440 ~ 700,0 MHz
frequency stability at -20° ~ +70° C at -40° ~ +85° C	± 50 ppm ± 100 ppm
operating temperature	standard -20° ~ +70° C available -40° ~ +85° C (=KXO-V65T)
storage temperature	-55° ~ +125° C
symmetry	50% ±5% at ½ Vcc level
rise & fall time (Tr) max.	600ps typ 850ps max. (20% - 80% of waveform)
rise & fall time (Tf) max.	600ps typ 850ps max. (80% - 20% of waveform)
LVDS offset output voltage	1,125V ~ 1,375V
"0" level max.	0,9V ~ 1,1V
"1" level min.	1,43V ~ 1,6V
input voltage V _{DD}	standard + 3,3V ± 5% available +2,5V ±5%
input current max.	50 mA
output load	100 Ohm
start up time max.	10 ms
tristate function	yes
enable/disable delay time	200 ns max.
phase jitter (12kHz - 20MHz)	RMS: 1ps typ. 3ps max.
contents of reel	1000 pcs.
RoHS	since production start
part no.	12.93600 ~ 12.93799

