International **IOR** Rectifier

Data Sheet No. PD10054-B

Series PVI-N

Photovoltaic Isolator Single and Dual Channel 5-10 Volt Output

General Description

The PVI Series Photovoltaic Isolator generates an electrically isolated DC voltage upon receipt of a DC input signal. It is capable of directly driving gates of power MOSFETs or IGBTs. It utilizes a monolithic integrated circuit photovoltaic generator of novel construction as its output. The output is controlled by radiation from a GaAIAs light emitting diode (LED), which is optically isolated from the photovoltaic generator.

The PVI Series is ideally suited for applications requiring high-current and/or high-voltage switching with optical isolation between the low-level driving circuitry and high-energy or high-voltage load circuits. It can be used for directly driving gates of power MOSFETs. The dual-channel device allows its outputs to drive independent discrete power MOSFETs, or be connected in parallel or in series to provide higher current drive for power MOSFETs or higher voltage drive for IGBTs. The PVI Series Photovoltaic isolators employ fast turn-off circuitry.

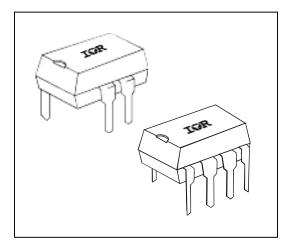
These PVI Series Photovoltaic Isolators are packaged in 8-pin, molded DIP packages and available with either thru-hole or surface-mount ("gull-wing") leads, in plastic shipping tubes.

Applications

- Load Distribution
- Industrial Controls
- Current-to-Voltage Conversion
- Custom Solid-State Relay

Features

- Isolated Voltage Source
- Monolithic Construction
- Up to 8µA Output
- Single or Dual Output
- Solid-State Reliability



Part Identification

PVI1050N PVI5050N PVI5080N

thru-hole

PVI1050NS PVI5050NS PVI5080NS surface-mount (gull-wing)

Electrical Specifications (-40°C \leq T_A \leq +85°C unless otherwise specified)

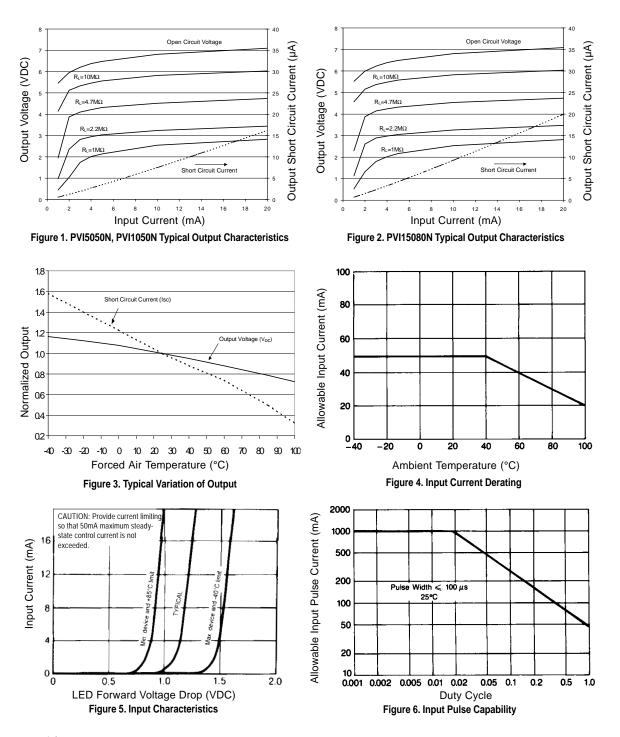
INPUT CHARACTERISTICS	PVI Series	Units
Input Current Range (see figure 4)	2.0 to 50	mA (DC)
Maximum Forward Voltage Drop @ 10mA, 25°C (see figure 5)	1.4	V (DC)
Maximum Reverse Voltage	7.0	V(DC)
Maximum Reverse Current @ -7.0V (DC), 25°C	100	μA(DC)
Maximum Pulsed Input Current @ 25°C (see figure 6)	1.0	A(peak)
OUTPUT CHARACTERISTICS	PVI Series	Units
Maximum Forward Voltage @ 10µA	8.0 per channel	V _(DC)
Maxiumum Reverse Current @ -10VDC	10	µA(DC)

COUPLED CHARACTERISTICS		PVI5050N	PVI5080N	PVI1050N	Units
Minimum Open Circuit Voltage @ ILED = 10mA, 25°C, RL = >10M Ω (see figures 1 to 2)		5.0		5.0/channel 10 series	V (DC)
Minimum Short Circuit Current @ ILED = 10mA, 25°C (see	e figures 1 to 2)	5.0	8.0	5.0 /channel 10 parallel	μΑ (DC)
Maximum Capacitance (Input/Output)		1.0 2		2.0	pF
Maximum Ton Time @ ILED=10mA, CLOAD=10pF (See Figure7)					
	$RL>20M\Omega$		300		μS
	RL=10MΩ		160		μS
	RL=4.7MΩ		90		μS
Maximum Toff Time @ ILED=10mA, CLOAD=10pF (See Figure7)			220		μS

GENERAL CHARACTERISTICS	PVI5050N/5080N	PVI1050N	Units
Min. Dielectric Strength, Input-Output	4000	2500	V _{RMS}
Min. Dielectric Strength, Output-to-Output	12	V _{DC}	
Min. Insulation Resistance, Input-to-Output @T _A =+25°C, 50%RH, 100V _{DC}	10	Ω	
Max. Pin Soldering Temperature (10 seconds max.)	+260		°C
Ambient Temperature Range: Operating	-40 to +85		°C
Storage	-40 to +	°C	

International Rectifier does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this International Rectifier product in such applications assume all risks of such use and indemnify International Rectifier against all damages resulting from such use.

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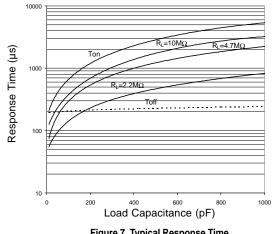
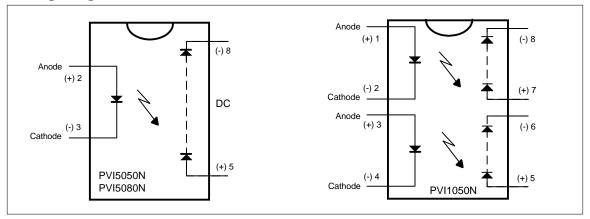


Figure 7. Typical Response Time

Wiring Diagram



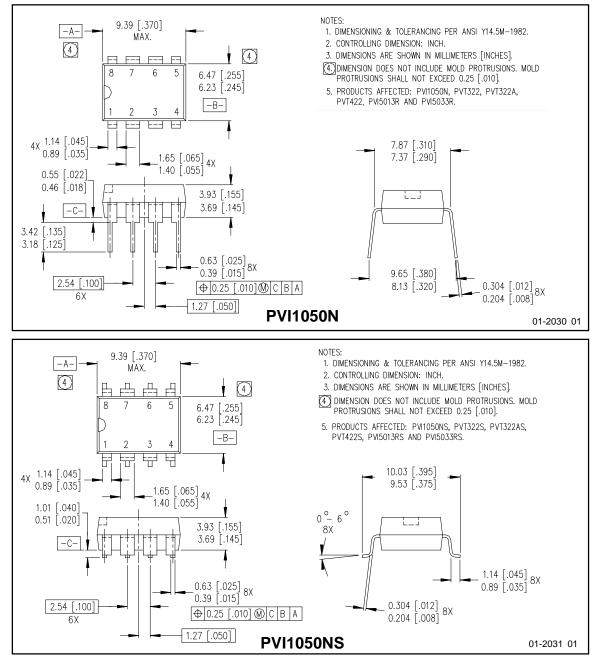
Application Note:

The outputs of the PVI1050N (pins 5-6 and 7-8) may be placed in series connection to produce a 10-volt output with a 5µA minimum short circuit current. Alternatively, the two ouptut of the PVI1050 may be connected in parallel to produce a 5.0-volt ouput with a 10µA minimum short circuit current.

The two outputs of the PVI1050N may be applied separately with a maximum 1200VDC between the outputs. Input-to-output isolation to either output is 2500V (RMS).

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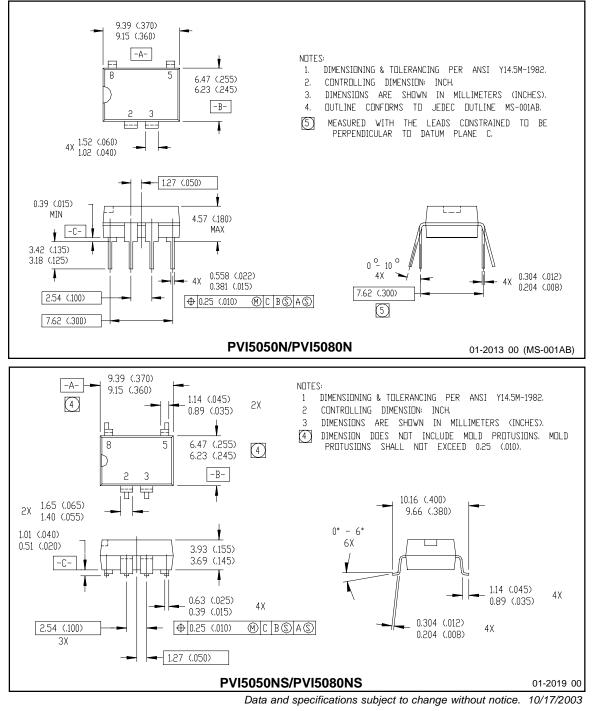
Case Outlines



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Case Outlines



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