



STB120N4F6 STD120N4F6, STP120N4F6

N-channel 40 V, 4 mΩ , 80 A, DPAK, D²PAK, TO-220
STripFET™ VI DeepGATE™ Power MOSFET

Features

Type	V _{DSS}	R _{DS(on)} max.	I _D
STB120N4F6	40 V	4 mΩ	80 A ⁽¹⁾
STD120N4F6	40 V	4 mΩ	80 A ⁽¹⁾
STP120N4F6	40 V	4.3 mΩ	80 A ⁽¹⁾

1. Current limited by package

- Standard threshold drive
- 100% avalanche tested

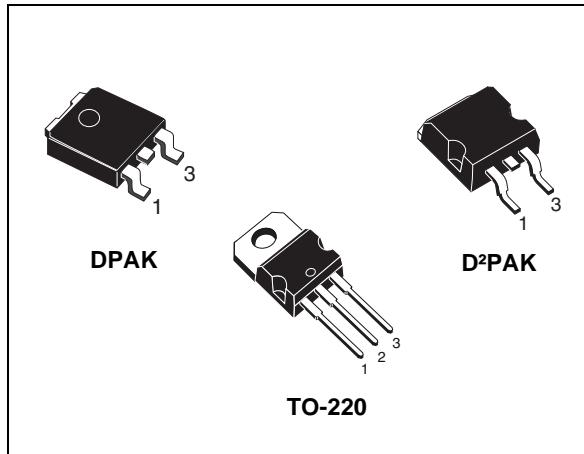


Figure 1. Internal schematic diagram

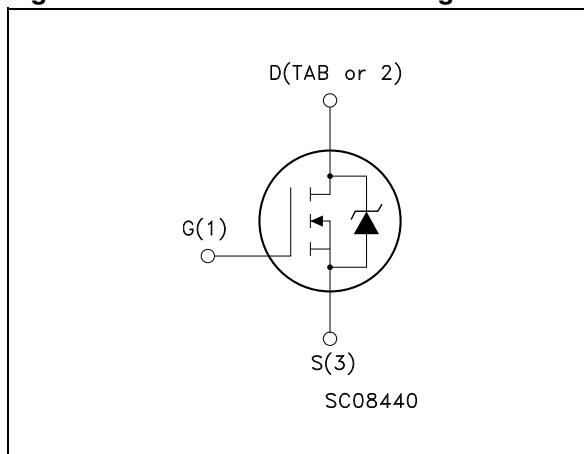


Table 1. Device summary

Order codes	Marking	Package	Packaging
STB120N4F6	120N4F6	D ² PAK	Tape and reel
STD120N4F6	120N4F6	DPAK	Tape and reel
STP120N4F6	120N4F6	TO-220	Tube

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-source voltage ($V_{GS} = 0$)	40	V
V_{GS}	Gate-source voltage	± 20	V
$I_D^{(1)}$	Drain current (continuous) at $T_C = 25^\circ\text{C}$	80	A
$I_D^{(1)}$	Drain current (continuous) at $T_C = 100^\circ\text{C}$	80	A
$I_{DM}^{(2)}$	Drain current (pulsed)	320	A
P_{TOT}	Total dissipation at $T_C = 25^\circ\text{C}$	110	W
T_{stg}	Storage temperature	-55 to 175	$^\circ\text{C}$
T_j	Operating junction temperature		

- 1. Current limited by package
- 2. Pulse width limited by safe operating area

Table 3. Thermal resistance

Symbol	Parameter	Value			Unit
		DPAK	D ² PAK	TO-220	
$R_{thj-case}$	Thermal resistance junction-case max	1.36			$^\circ\text{C/W}$
$R_{thj-pcb}$	Thermal resistance junction-pcb max ⁽¹⁾	50	35		$^\circ\text{C/W}$
$R_{thj-amb}$	Thermal resistance junction-amb max	62.5			$^\circ\text{C/W}$

- 1. When mounted on 1 inch² 2 oz. Cu board.

Table 4. Thermal resistance

Symbol	Parameter	Value	Unit
$I_{AR}^{(1)}$	Avalanche current, repetitive or non-repetitive	40	A
$E_{AS}^{(2)}$	Single pulse avalanche energy	394	mJ

- 1. Pulse width limited by T_j max
- 2. Starting $T_j = 25^\circ\text{C}$, $I_D = 40\text{ A}$, $V_{DD} = 25\text{ V}$

2 Electrical characteristics

($T_{CASE} = 25^\circ\text{C}$ unless otherwise specified)

Table 5. Static

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown Voltage	$I_D = 250 \mu\text{A}, V_{GS}=0$	40			V
I_{DSS}	Zero gate voltage drain current ($V_{GS} = 0$)	$V_{DS} = 20 \text{ V}$ $V_{DS} = 20 \text{ V}, T_c = 125^\circ\text{C}$			1 10	μA μA
I_{GSS}	Gate body leakage current ($V_{DS} = 0$)	$V_{GS} = \pm 20 \text{ V}$			± 100	nA
$V_{GS(\text{th})}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	2		4	V
$R_{DS(\text{on})}$	Static drain-source on resistance	$V_{GS} = 10 \text{ V}, I_D = 40 \text{ A}$ For TO-220		3.8	4.3	$\text{m}\Omega$
		$V_{GS} = 10 \text{ V}, I_D = 40 \text{ A}$ For DPAK/D ² PAK		3.5	4.0	$\text{m}\Omega$

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min	Typ.	Max.	Unit
C_{iss}	Input capacitance			3850		pF
C_{oss}	Output capacitance	$V_{DS} = 25 \text{ V}, f=1 \text{ MHz}$,	-	650	-	pF
C_{rss}	Reverse transfer capacitance	$V_{GS} = 0 \text{ V}$		350		pF
Q_g	Total gate charge	$V_{DD} = 20 \text{ V}, I_D = 80 \text{ A}$	-	65		nC
Q_{gs}	Gate-source charge	$V_{GS} = 10 \text{ V}$	-	20	-	nC
Q_{gd}	Gate-drain charge	(see Figure 14)		16		nC
R_G	Intrinsic gate resistance	$f = 1 \text{ MHz open drain}$	-	1.5	-	Ω

Table 7. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 20 \text{ V}$, $I_D = 40 \text{ A}$, $R_G = 4.7 \Omega$, $V_{GS} = 10 \text{ V}$ (see Figure 15)	-	20	-	ns
t_r	Rise time		70			ns
$t_{d(off)}$	Turn-off delay time		-	40	-	ns
t_f	Fall time		20			ns

Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain current		-		80	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				320	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 40 \text{ A}$, $V_{GS} = 0$	-		1.1	V
t_{rr}	Reverse recovery time	$I_{SD} = 80 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$, $V_{DD} = 30 \text{ V}$ (see Figure 17)	-	40		ns
Q_{rr}	Reverse recovery charge			56		nC
I_{RRM}	Reverse recovery current			2.8		A

1. Pulse width limited by safe operating area
2. Pulsed: pulse duration = 300 μs , duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

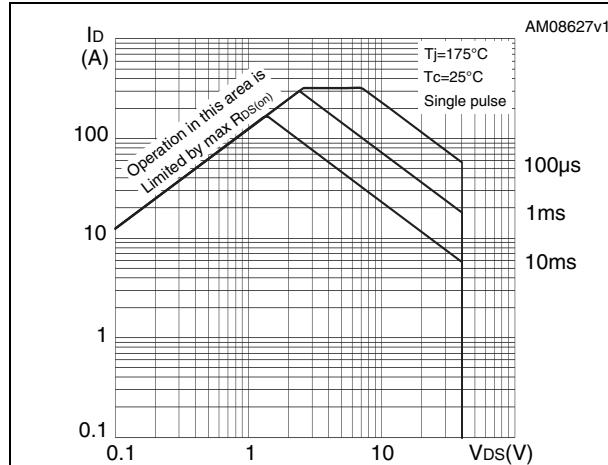


Figure 3. Thermal impedance

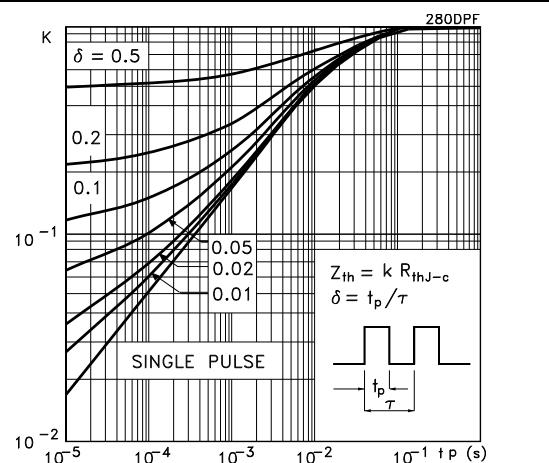


Figure 4. Output characteristics

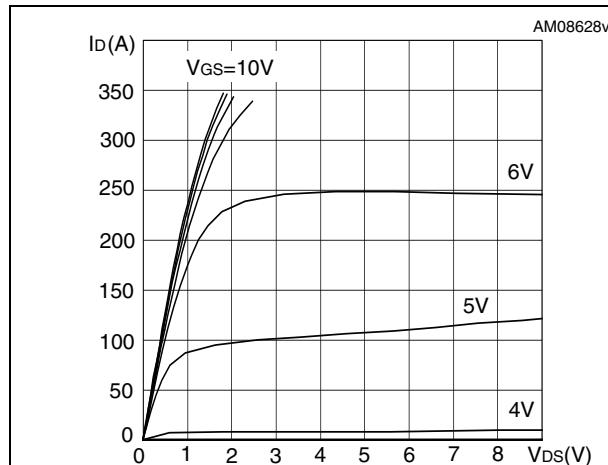


Figure 5. Transfer characteristics

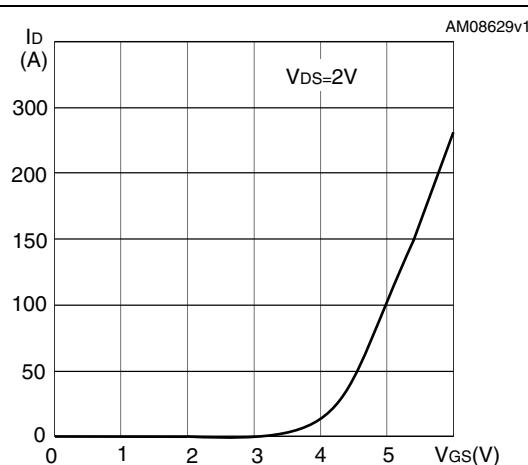
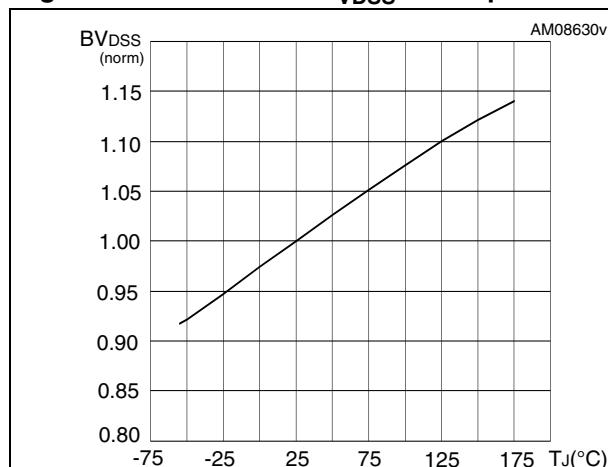
Figure 6. Normalized B_{VDSS} vs temperature

Figure 7. Static drain-source on resistance

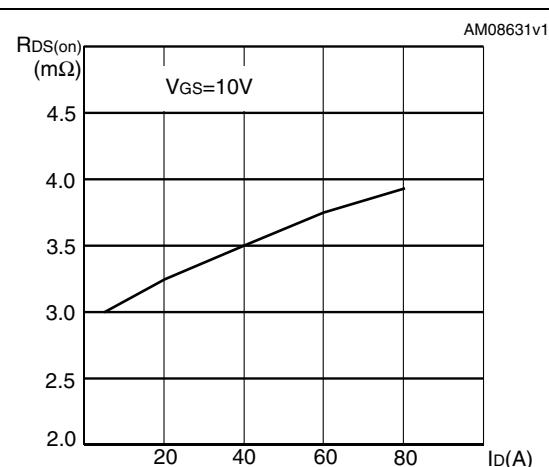
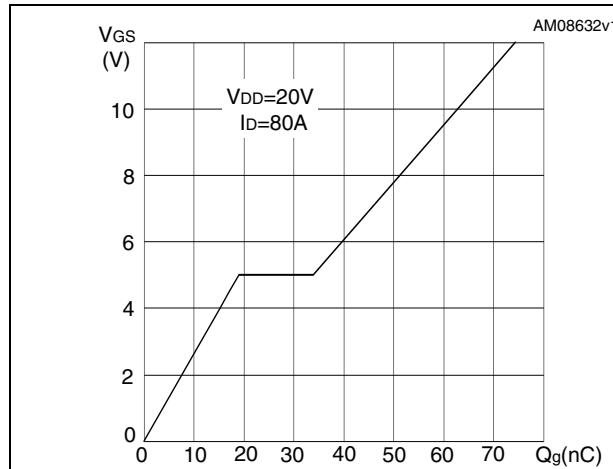
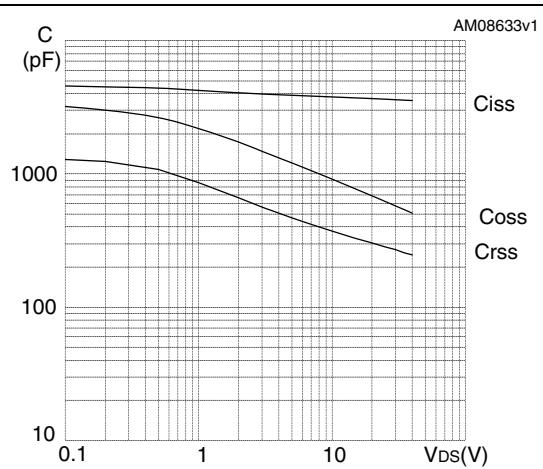
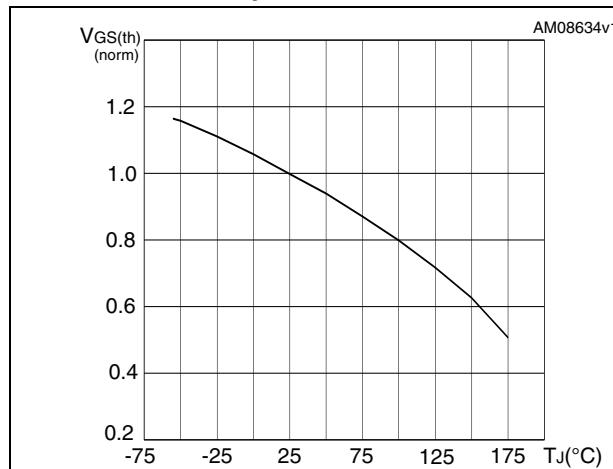
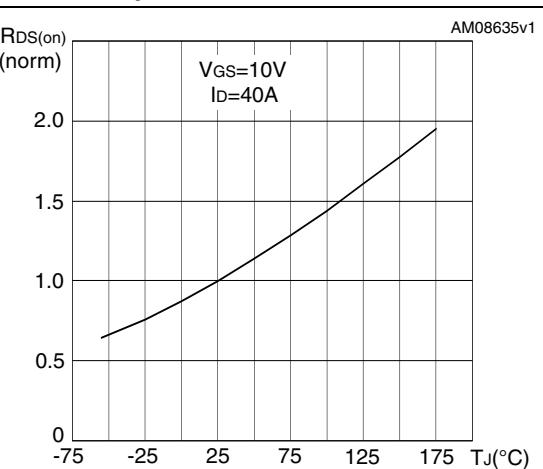
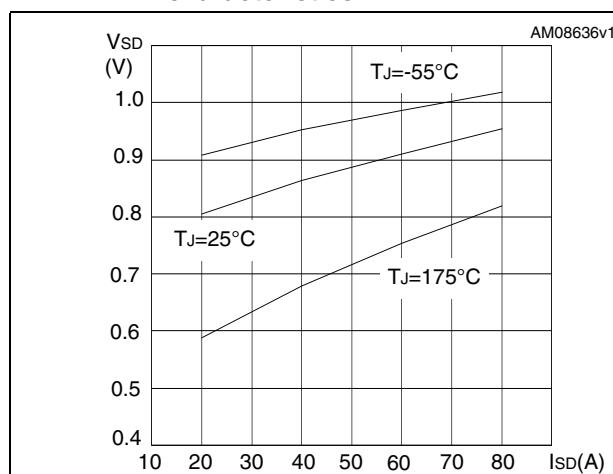


Figure 8. Gate charge vs gate-source voltage**Figure 9. Capacitance variations****Figure 10. Normalized gate threshold voltage vs temperature****Figure 11. Normalized on resistance vs temperature****Figure 12. Source-drain diode forward characteristics**

3 Test circuits

Figure 13. Switching times test circuit for resistive load

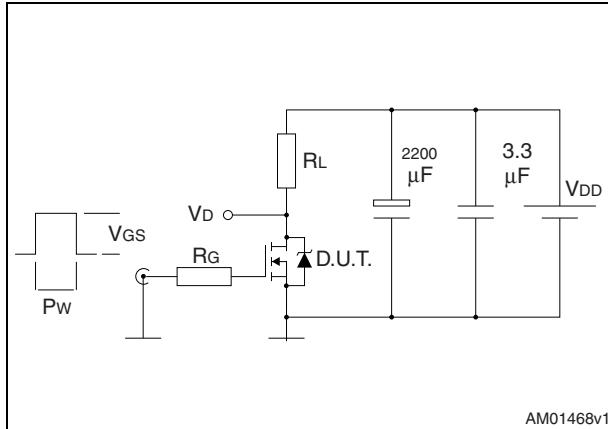


Figure 14. Gate charge test circuit

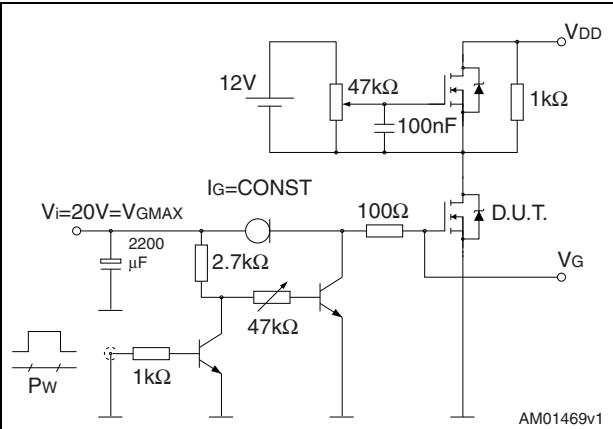


Figure 15. Test circuit for inductive load switching and diode recovery times

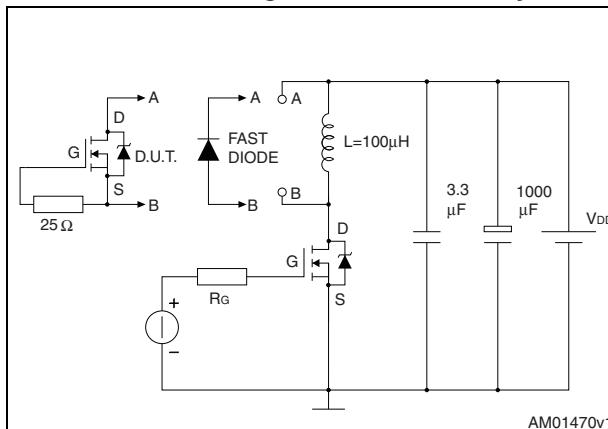


Figure 16. Unclamped Inductive load test circuit

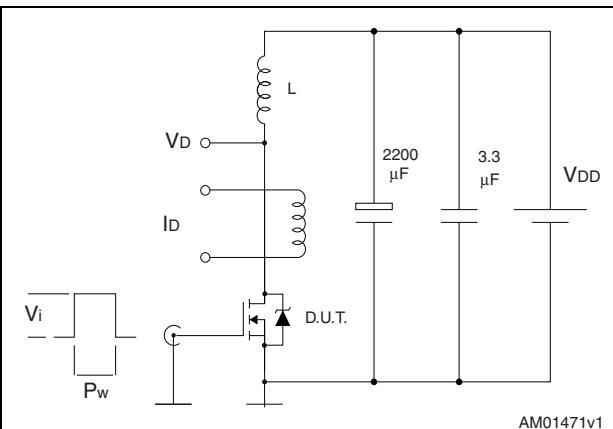


Figure 17. Unclamped inductive waveform

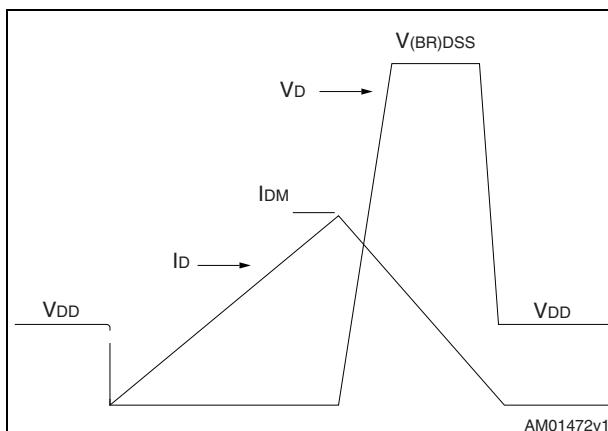


Figure 18. Switching time waveform

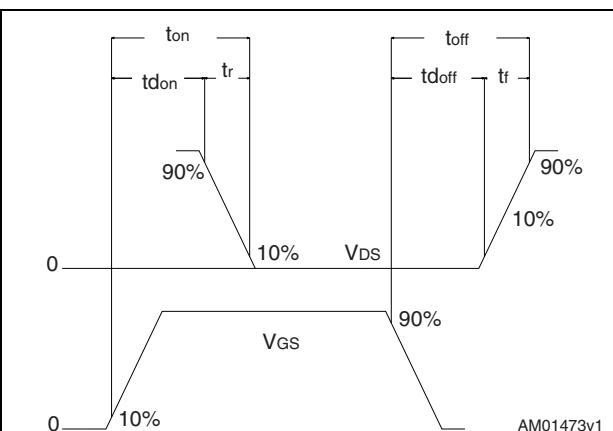
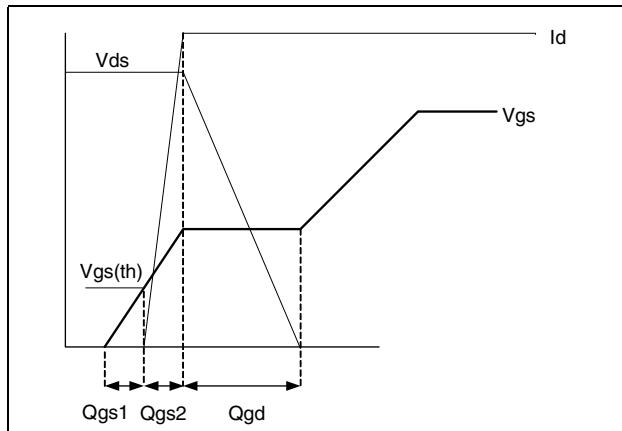


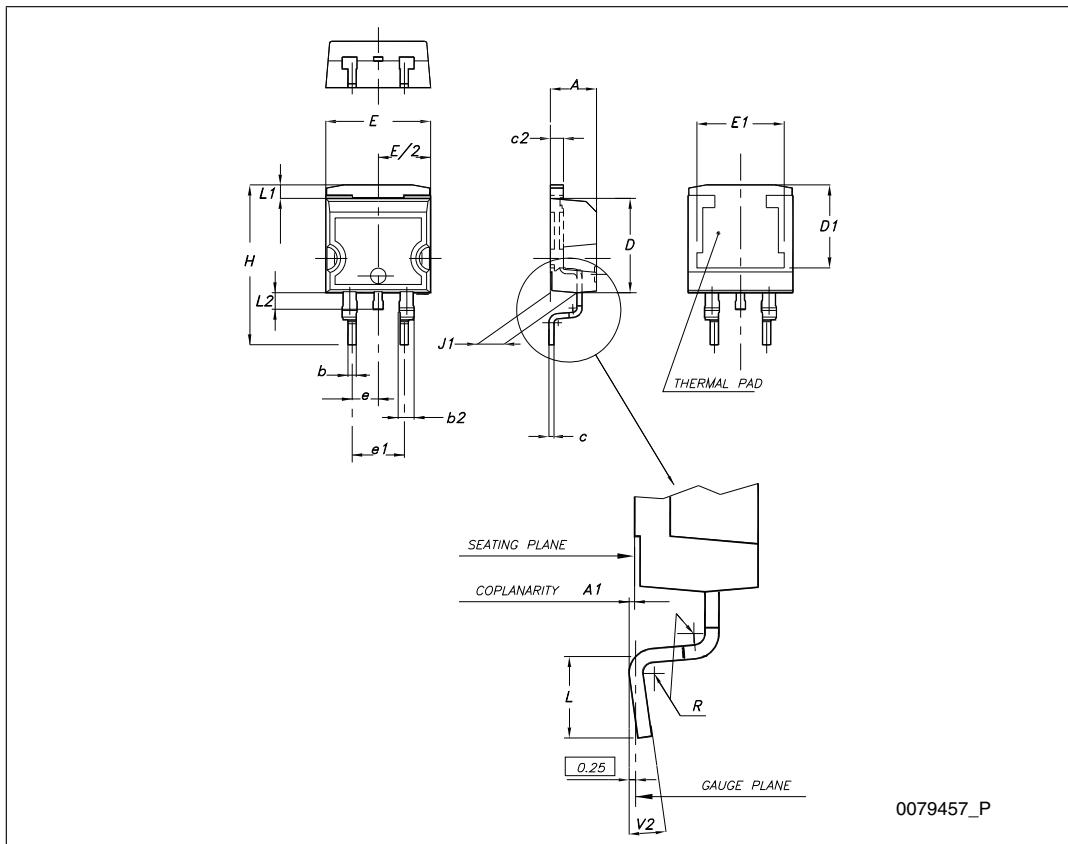
Figure 19. Gate charge waveform

4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and products status are available at: www.st.com.
ECOPACK is an ST trademark.

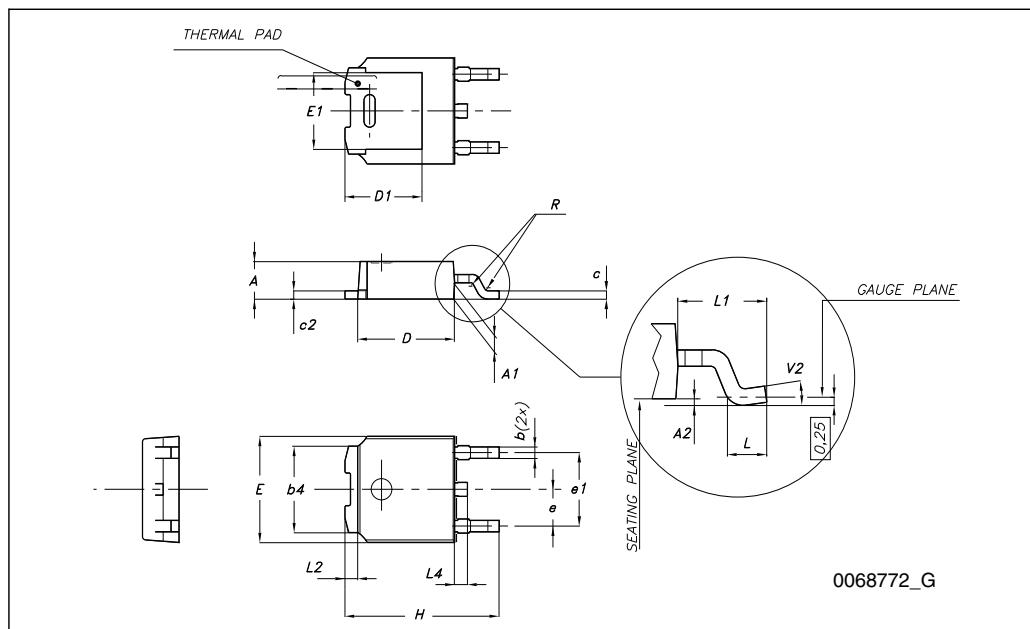
D²PAK (TO-263) mechanical data

Dim.	mm.		
	Min.	Typ.	Max.
A	4.40		4.60
A1	0.03		0.23
b	0.70		0.93
b2	1.14		1.70
c	0.45		0.60
c2	1.23		1.36
D	8.95		9.35
D1	7.50		
E	10		10.40
E1	8.50		
e		2.54	
e1	4.88		5.28
H	15		15.85
J1	2.49		2.69
L	2.29		2.79
L1	1.27		1.40
L2	1.30		1.75
R		0.4	
V2	0°		8°



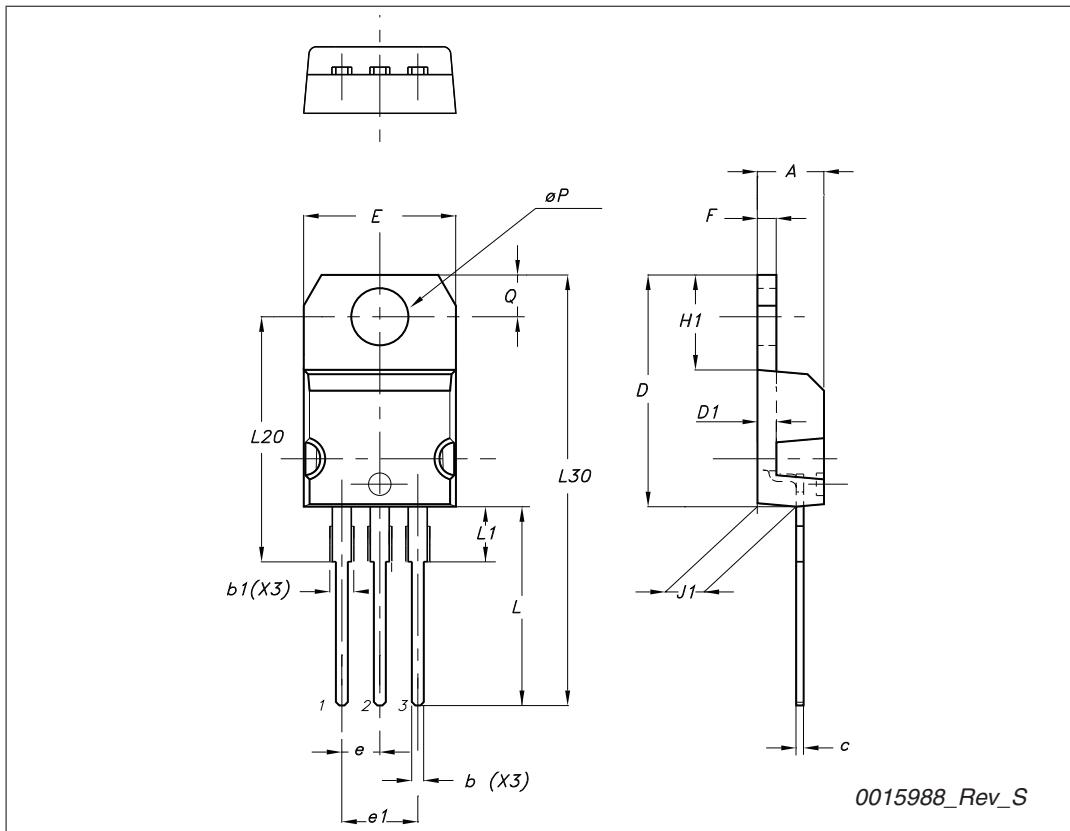
TO-252 (DPAK) mechanical data

DIM.	mm.		
	min.	typ	max.
A	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
c	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
E	6.40		6.60
E1		4.70	
e		2.28	
e1	4.40		4.60
H	9.35		10.10
L	1		
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0 °		8 °



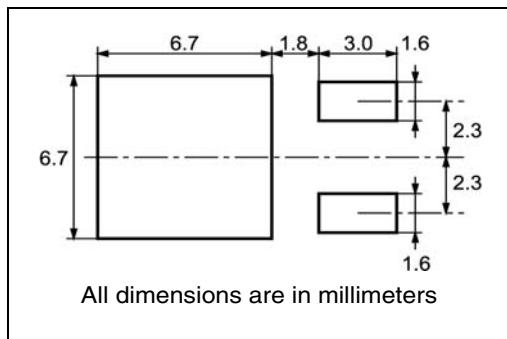
TO-220 type A mechanical data

Dim	mm		
	Min	Typ	Max
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
c	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
$\emptyset P$	3.75		3.85
Q	2.65		2.95



5 Packaging mechanical data

DPAK FOOTPRINT



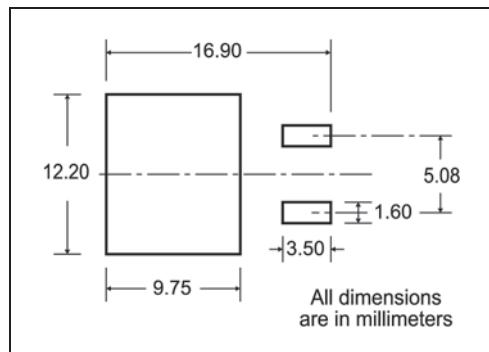
TAPE AND REEL SHIPMENT

REEL MECHANICAL DATA				
DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A			330	12.992
B	1.5		0.059	
C	12.8	13.2	0.504	0.520
D	20.2		0.795	
G	16.4	18.4	0.645	0.724
N	50		1.968	
T		22.4		0.881

BASE QTY	BULK QTY
2500	2500

TAPE MECHANICAL DATA

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A ₀	6.8	7	0.267	0.275
B ₀	10.4	10.6	0.409	0.417
B ₁		12.1		0.476
D	1.5	1.6	0.059	0.063
D ₁	1.5		0.059	
E	1.65	1.85	0.065	0.073
F	7.4	7.6	0.291	0.299
K ₀	2.55	2.75	0.100	0.108
P ₀	3.9	4.1	0.153	0.161
P ₁	7.9	8.1	0.311	0.319
P ₂	1.9	2.1	0.075	0.082
R	40		1.574	
W	15.7	16.3	0.618	0.641

D²PAK FOOTPRINT

TAPE AND REEL SHIPMENT

REEL MECHANICAL DATA				
DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A		330		12.992
B	1.5		0.059	
C	12.8	13.2	0.504	0.520
D	20.2		0795	
G	24.4	26.4	0.960	1.039
N	100		3.937	
T		30.4		1.197
BASE QTY		BULK QTY		
1000		1000		

TAPE MECHANICAL DATA				
DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A ₀	10.5	10.7	0.413	0.421
B ₀	15.7	15.9	0.618	0.626
D	1.5	1.6	0.059	0.063
D ₁	1.59	1.61	0.062	0.063
E	1.65	1.85	0.065	0.073
F	11.4	11.6	0.449	0.456
K ₀	4.8	5.0	0.189	0.197
P ₀	3.9	4.1	0.153	0.161
P ₁	11.9	12.1	0.468	0.476
P ₂	1.9	2.1	0.075	0.082
R	50		1.574	
T	0.25	0.35	0.0098	0.0137
W	23.7	24.3	0.933	0.956

6 Revision history

Table 9. Document revision history

Date	Revision	Changes
09-Feb-2010	1	First release
29-Oct-2010	2	Document status promoted from preliminary data to datasheet.
11-Nov-2010	3	Corrected $R_{DS(on)}$ value in Table 5: Static .

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