AN7178

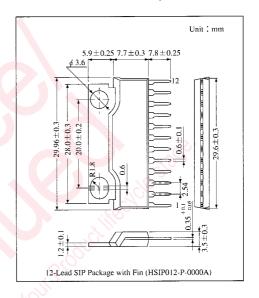
Dual 5.7W Audio Power Amplifier Circuit

Overview

The AN7178 is an integrated circuit designed for audio with low distortion, low noise and low dissipation of 5.7W (13.2V, 4Ω) output. Stereo operation is enabled due to incorporating two amplifiers. 12-pin SIL package enables compact and high integrated set. Thermal protection, short protection and excessive current protection circuits are built in.

■ Features

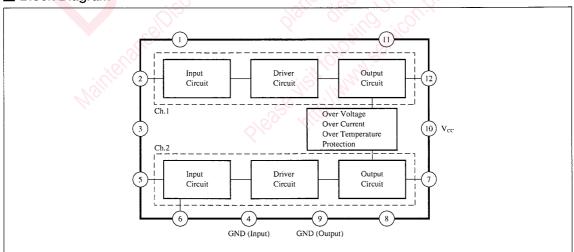
- · Highly stable operation
- Low distortion
- · Low quiescent current
- · Low noise
- Low shock noise from power ON/OFF operation
- · Built-in muting circuit
- Fewer external components
- Incorporating protection circuits



■ Pin Descriptions

Pin No.	Pin Name	Pin No.	Pin Name	
1	NFB Ch.1	7	Output Ch.2	
2	Input Ch.1	8	Bootstrap Ch.2	
3	Ripple Filter	9	GND (Output)	
4	GND (Input)	10	V _{CC}	
5	Input Ch.2	11	Bootstrap Ch.1	
6	NFB Ch.2	12	Output Ch.1	

■ Block Diagram

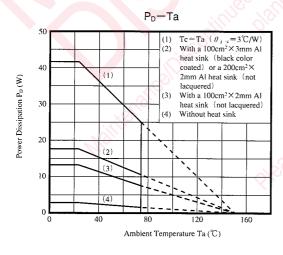


Absolute Maximum Ratings ($Ta = 25^{\circ}$ C)

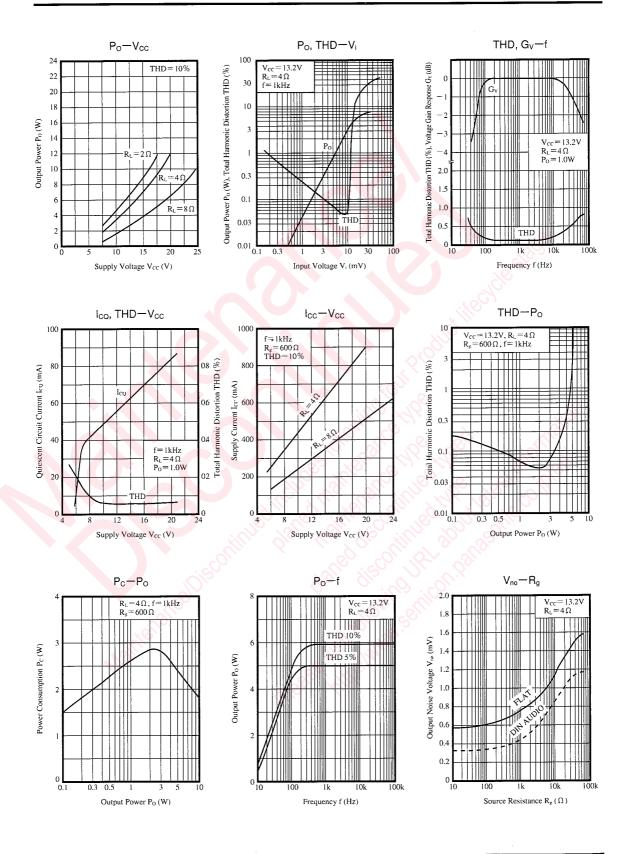
Parameter	Symbol	Rating	Unit	
Supply Voltage	V _{CC}	18	V	
Supply Current	I _{CC}	4.0	A	
Power Dissipation	P _D	41.7	W	
Operating Ambient Temperature	Торг	-30~+75	°C	
Storage Temperature	T_{stg}	$-55 \sim +150$	°C	

■ Electrical Characteristics ($V_{CC}=12V$, $R_C=3\Omega$, f=1kHz, $Ta=25^{\circ}C$)

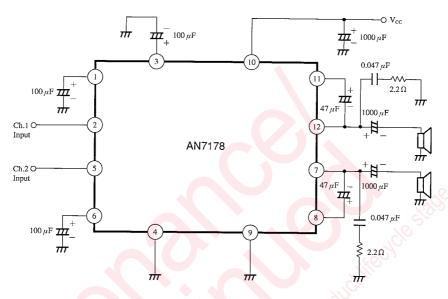
Parameter	Symbol	Condition	min.	typ.	max.	Unit	
Quiescent Circuit Current	I_{CQ}	I_{CQ} $V_i = 0 \text{mV}$		55	100	mA	
Voltage Gain	Gv	$P_0 = 0.5W$	52	54		dB	
	THD	$P_0 = 0.5W$		0.1	0.5		
Total Harmonic Distortion		$P_0 = 0.5W, f = 100Hz$		0.1		%	
		$P_0 = 0.5W, f = 10kHz$		0.2			
		THD=10%	5.0	5.7		W	
Maximum Output	Po	THD= 10% , $R_L=2\Omega$	4	8.9			
		THD= 10% , $R_L = 8\Omega$	<u> </u>	3.1			
Output Naire Valence	V	$R_g = 10k \Omega$, 1000pF, $f = 15Hz \sim 30kHz$, 12dB/OCT	_	0.8	1.5	mV	
Output Noise Voltage	V _{no}	R _g =10kΩ, 1000pF, Without Filter		1.1			
Channel Balance	СВ	P ₀ =0.5W	—	0	1.0	dB	
Channel Separation	CS	Po=0.5W	40	50	0	dB	
Ripple Rejection Ratio	RR	P_0 = 0.5W, R_g = 10k Ω , V_{ripple} = 280m Vrms, f_{ripple} = 12Hz Sine wave	35	41	HOITH	dB	
Offset Voltage	V _{O (offset)}	$V_i = 0 \text{mV}$	_	0	200	mV	



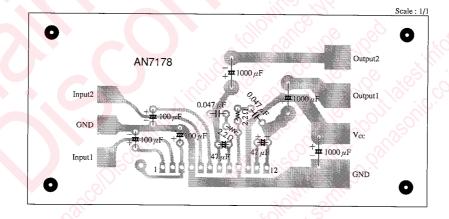




■ Application Circuit



■ Printed Circuit Board Layout





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