



High-Current Switching Applications

Applications

 DC-DC converters, motor drivers, relay drivers, lamp drivers.

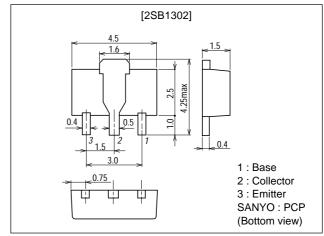
Features

- · Adoption of FBET, MBIT processes.
- · Low collector-to-emitter saturation voltage.
- · Large current capacity.
- · Fast switching speed.
- · Ultrasmall size making it easy to provide highdensity, small-sized hybrid ICs.

Package Dimensions

unit:mm

2038A



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		-25	V
Collector-to-Emitter Voltage	VCEO		-20	V
Emitter-to-Base Voltage	V _{EBO}		-5	V
Collector Current	IC		-5	Α
Collector Current (Pulse)	ICP		-8	Α
Collector Dissipation	PC	Mounted on ceramic board (250mm²×0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector Cutoff Current	I _{CBO}	V _{CB} =-20V, I _E =0			-500	nA
Emitter Cutoff Current	I _{EBO}	V _{EB} =-4V, I _C =0			-500	nA
DC Current Gain	h _{FE} 1	V _{CE} =-2V, I _C =-500mA	100*		400*	
	h _{FE} 2	V _{CE} =-2V, I _C =-4A	60			
Gain-Bandwidth Product	fT	V _{CE} =-5V, I _C =-200mA		320		MHz
Output Capacitance	C _{ob}	V _{CB} =-10V, f=1MHz		60		pF

*: The 2SB1302 is classified by 500mA h_{FE} as follows:

 Rank
 R
 S
 T

 hFE
 100 to 200
 140 to 280
 200 to 400

Marking: BJ h_{FE} rank: R, S, T

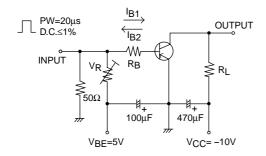
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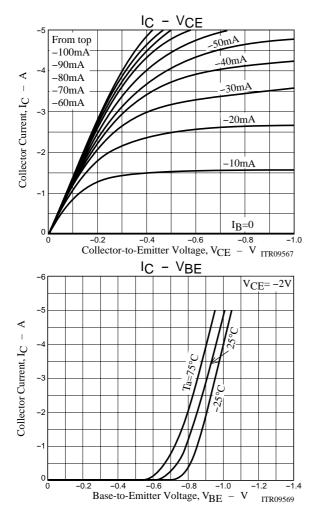
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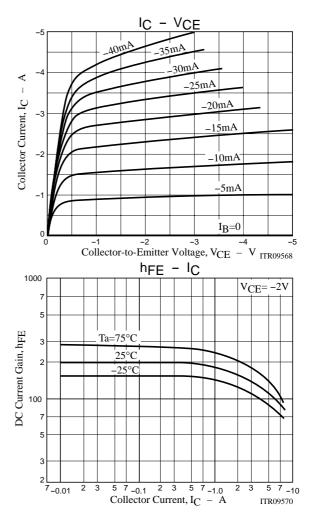
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	01111
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =-3A, I _B =-60mA		-250	-500	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =-3A, I _B =-60mA		-1.0	-1.3	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	$I_{C}=-10\mu A, I_{E}=0$	-25			٧
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =-1mA, R _{BE} =∞	-20			٧
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-10μA, I _C =0	-5			V
Turn-ON Time	ton	See specified test circuit.		40		ns
Storage Time	t _{stg}	See specified test circuit.		200		ns
Fall Time	t _f	See specified test circuit.		10		ns

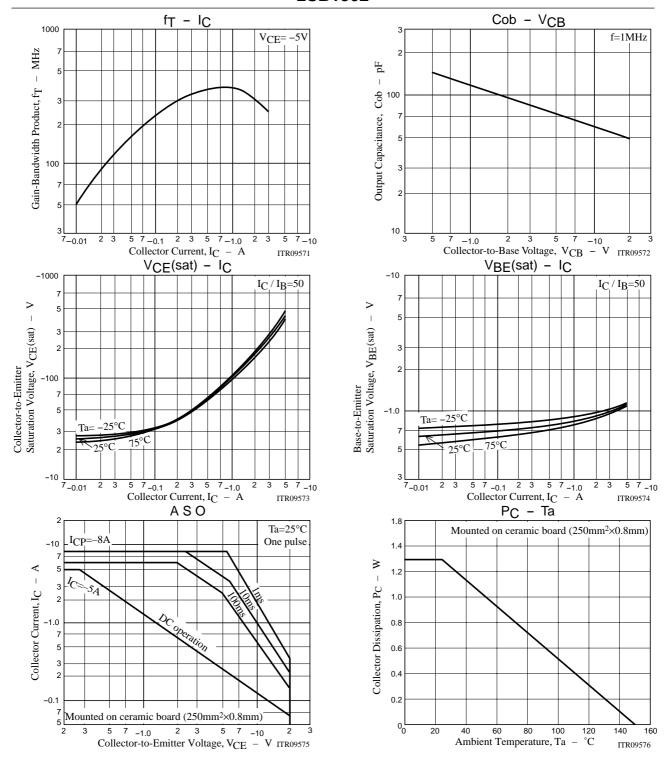
Switching Time Test Circuit



 $I_{C}=10I_{B1}=-10I_{B2}=-2A$







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