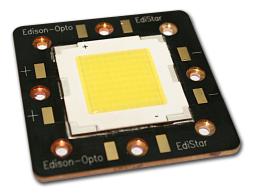
EdiStar Series



EdiStar series is the brightness LEDs in the world by Edison Opto. EdiStar series emitters are designed to satisfy more and more Solid-State lighting High Power LED applications for brilliant world such as general lighting, street light and projector light engine. EdiStar series emitters are designed by particular package for High Power LED. 50W and 100W EdiStar

series white has typical 4,000 and 7,000 lumens at 2,400mA and 3,000mA. Unlike the fluorescent sources, EdiStar series contains no mercury and has more energy efficient than other incandescent light source.

Features

- Outstanding thermal performance
- LED lighting engine
- Ultra high power LED



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Product Nomenclature

The following table describes the available color, power, and lens type. For more information on luminous flux and color, please refer to the Bin Group document.

< Table 1 EdiStar series nomenclature >

$\frac{E N}{x_1} \frac{E}{x_2} \frac{W}{x_3}$	$- \frac{0.5}{x_4} - \frac{0.7}{x_5} \frac{0.5}{x_5}$	$\frac{07}{x_6} - \frac{A}{x_7}$	
X1	X2	X	(3
LED Item	Туре	Emitte	r Color
Code Type Code	е Туре	Code	Туре
EN EdiStar E	Emitter	W	White
P	Emitter + Driver	н	Neutral White
S	Emitter + Cu Star	Х	Warm White
С	Emitter + Cu Star + Drive	er	
X4	X5	X	(6
Power	Circuit Series		Parallel
Code Type Code	е Туре	Code	Туре
0 5 50W 1~10	1~10 Series	1~10	1~10 Parallel
10 100W			
X7	X8	>	(9
Material	Phosphor		strate
Code Type Code	FIIOSOHOI		
Code Type Code		Code	Туре

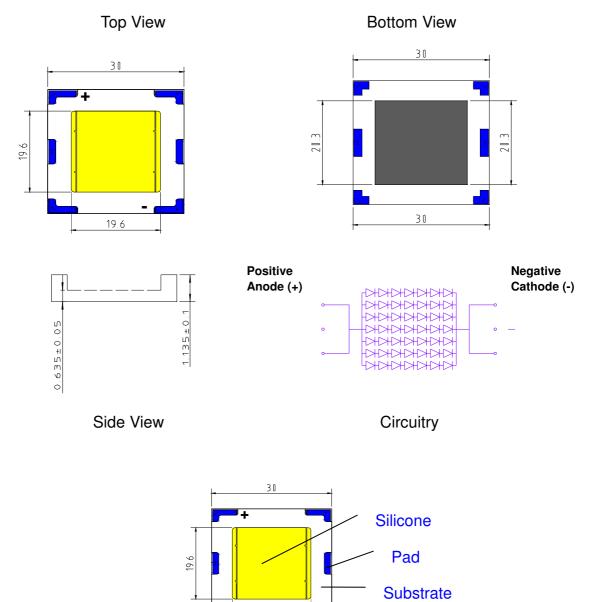
Environmental Compliance

EdiStar series is compliant to the Restriction of Hazardous Substances Directive or RoHS. The restricted materials including lead, mercury cadmium hexavalent chromium, polybrominated biphenyls (PBB) and polybrominated diphenyl ether (PBDE) are not used in EdiStar series to provide an environmentally friendly product to the customers.



LED Package Dimensions and Polarity

ENEW-05-0707-EB-1



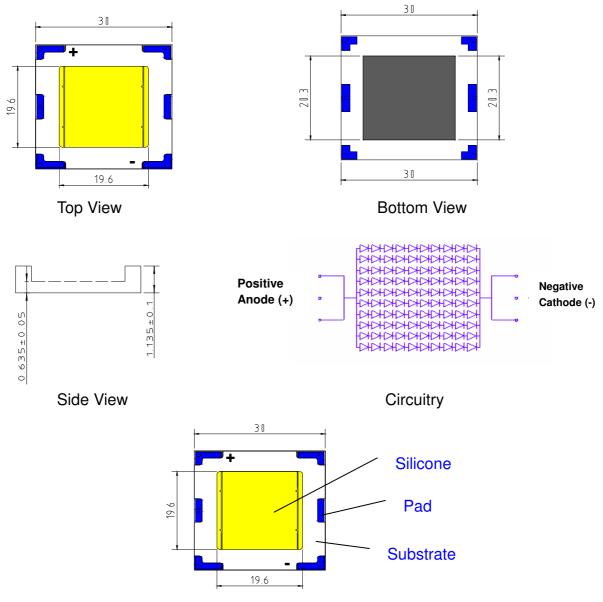
< Figure 1 EdiStar 50W series dimensions >

19.6

- 1. All dimensions are in mm.
- 2. The tolerance is $\pm 0.35\ \text{mm}$
- 3. It is strongly recommended that the temperature of substrate dose not exceed $55^\circ\!\mathrm{C}$.



ENEW-10-1010-EB-1

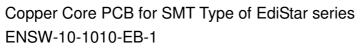


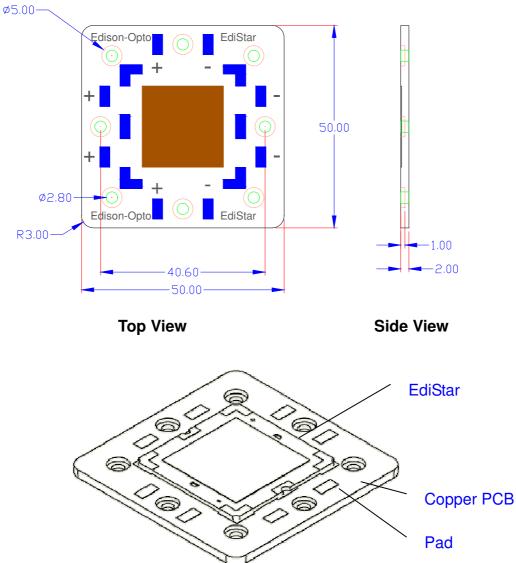
< Figure 2 EdiStar 100W series dimensions >

- 1. All dimensions are in mm.
- 2. The tolerance is ± 0.35 mm
- 3. It is strongly recommended that the temperature of substrate dose not exceed 55° C.



LED Package with Star Dimension and Polarity





<Figure 3 EdiStar series with copper PCB dimensions>

- 1. All dimensions are in mm.
- 2. The tolerance is ± 0.35 mm
- 3. It is strongly recommended that the temperature of substrate does not exceed 55° C.



Absolute Maximum Ratings

The following table describes characteristics of EdiStar series.

< Table 2 Absolute maximum ratings for EdiStar 50W and 100W series >

Parameter	Rating(50W)	Unit	Symbol
DC Forward Current	2,400	mA	IF
Peak pulse current;(tp \leq 100µs, Duty cycle=0.25)	5,000	mA	
Reverse Voltage	35	V	V _R
Forward Voltage	35	V	VF
Junction Temperature	125	°C	Tj
Substrate Temperature	100	°C	
Operating Temperature	-30 ~ +60	°C	
Storage Temperature	-40 ~ +60	V	
ESD Sensitivity	500	V	
Manual Soldering Time at 400 $^\circ\!\mathrm{C}$ (Max.)	5	Sec.	

Parameter	Rating(100W)	Unit	Symbol
DC Forward Current	3,000	mA	١ _F
Peak pulse current;(tp \leq 100µs, Duty cycle=0.25)	5,000	mA	
Reverse Voltage	35	V	VR
Forward Voltage	35	V	VF
Junction Temperature	125	°C	Tj
Substrate Temperature	100	°C	
Operating Temperature	-30 ~ +60	°C	
Storage Temperature	-40 ~ +60	V	
ESD Sensitivity	500	V	
Manual Soldering Time at 400 $^\circ\!\mathrm{C}$ (Max.)	5	Sec.	

- 1. Proper current rating must be observed to maintain junction temperature below the maximum at all time.
- 2. LEDs are not designed to be driven in reverse bias.
- 3. tp: Pulse width time



The following table describes thermal resistance of EdiStar series.

< Table 3 Temperature Coefficient of Forward Voltage & Thermal Resistance Junction to Case Characteristics at T_J=25 $^\circ\!C$ for EdiStar series >

Part Name	Color	∆V	_F /∆T	R) _{Ј-В}
		Тур.	Unit	Тур.	Unit
ENEW-05-0707-EB-1	Cool White	-2	mV/℃	0.75	°C /W
ENEH-05-0707- EE-1	Neutral White	-2	mV/℃	0.75	°C /W
ENEX-05-0707- EE-1	Warm White	-2	mV/℃	0.75	°C /W
ENEW-10-1010-EB-1	Cool White	-2	mV/℃	0.75	°C /W
ENEH-10-1010- EE-1	Neutral White	-2	mV/℃	0.75	°C /W
ENEX-10-1010- EE-1	Warm White	-2	mV/℃	0.75	°C/W

Luminous Flux Characteristics

The following table describes flux of EdiStar series emitters.

< Table 4 Luminous flux characteristics at I_F=2,400mA/3,000mA and T_J=25 $^\circ\!\!C$ for EdiStar

series >

Part Name	Color		Unit		
Fait Name	Color	Min.	Тур.	Max.	Unit
ENEW-05-0707-EB-1	Cool White		4,000		lm
ENEH-05-0707-EE-1	Neutral White		3,200		lm
ENEX-05-0707-EE-1	Warm White		2,800		lm
ENEW-10-1010-EB-1	Cool White		7,000		lm
ENEH-10-1010-EE-1	Neutral White		5,600		lm
ENEX-10-1010-EE-1	Warm White		4,900		lm

Note:

Flux is measured with an accuracy of \pm 10%.



Forward Voltage Characteristics

The following table describes forward voltage of EdiStar series.

< Table 5 Forward voltage characteristics at I_F=2,400mA/3,000mA and T_J=25 $^\circ\!\!\mathbb{C}$ for EdiStar

		series >	•		
Part Name	Color	Min.	V _F Typ.	Max.	Unit
ENEW-05-0707-EB-1	Cool White	22.0	24.5	27.5	V
ENEH-05-0707-EE-1	Neutral White	22.0	24.5	27.5	V
ENEX-05-0707-EE-1	Warm White	22.0	24.5	27.5	V
ENEW-10-1010-EB-1	Cool White	30.0	33.0	36.0	V
ENEH-10-1010-EE-1	Neutral White	30.0	33.0	36.0	V
ENEX-10-1010-EE-1	Warm White	30.0	33.0	36.0	V

Note:

Forward Voltage is measured with an accuracy of $\pm 0.1V$



JEDEC Information

JEDEC is used to determine what classification level should be used for initial reliability qualification. Once identified, the LEDs can be properly packaged, stored and handled to avoid subsequent thermal and mechanical damage during the assembly solder attachment and/or repair operation. The present moisture sensitivity standard contains six levels, the lower the level, the longer the devices floor life. EdiStar series is certified at level 4. This means EdiStar series has a floor life of 72 hours before EdiStar series emitters need to re-baked.

	Floor Life		Floor Life Soak Re		equirements	
Level	Time	Conditions	Star	ndard	Accelerated	d Environment
Time		Conultions	Time (hours)	Conditions	Time (hours)	Conditions
4	72hours	≦30℃ / 60% RH	96 +5/-0	30℃ / 60% RH	20 +0.5/-0	60℃ / 60% RH

< Table 6 JEDEC characteristics at $T_{\rm J}{=}25\,^\circ\!\!{\rm C}$ for EdiStar series >

	Fla	o # 1 :fo	Soak Requirements				
Level	FIO	or Life	Stan	dard	Accelerated Equivalent		
	Time	Condition	Time(hours)	Condition	Time(hours)	Condition	
1	Unlimited	<i>≦</i> 30 ℃ /85% RH	168 +5/-0	85 ℃/85% RH			
2	1 year	<i>≦</i> 30 ℃/60% RH	168 +5/-0	85 ℃/60% RH			
2a	4 weeks	<i>≦</i> 30 ℃/60% RH	696 ¹ +5/-0	30 ℃/60% RH	120 +1/-0	60 ℃/60% RH	
3	168 hours	<i>≦</i> 30 ℃/60% RH	192 ¹ +5/-0	30 ℃/60% RH	40 +5/-0	60 ℃/60% RH	
4	72 hours	<i>≦</i> 30 ℃/60% RH	96 ¹ +5/-0	30 ℃/60% RH	20 +5/-0	60 ℃/60% RH	
5	48 hours	<i>≦</i> 30 ℃/60% RH	72 ¹ +5/-0	30 ℃/60% RH	15 +5/-0	60 ℃/60% RH	
5a	24 hours	<i>≦</i> 30 ℃/60% RH	48 ¹ +5/-0	30 ℃/60% RH	10 +5/-0	60 ℃/60% RH	
6	Time on tabel (TOL)	<i>≦</i> 30 ℃/60% RH	TOL	30 ℃/60% RH			

Note:

The standard soak time includes a default value of 24 hours for semiconductor manufacturer's exposure time (MET) between bake and bag, and includes the maximum time allowed out of the bag at the distributor's facility.



Reliability Items and Failure Measures

Reliability test

The following table describes operating life, mechanical, and environmental tests performed on EdiStar series package.

< Table 7 Reliability Items and Conditions >

Stress Test	Stress Conditions	Stress Duration	Failure Criteria
Room Temperature Operating Life	25°C, $I_F = I_F$ Max DC current	1000 hours	Note 1
High Temperature High Humidity Storage Life	85℃ / 85%RH	1000 hours	Note 1
Non-Operating Temperature Cycle	-40 $^\circ\!\mathrm{C}/100^\circ\!\mathrm{C}$,30 min dwell /<5min transfer	200 cycles	Note 1
High Temperature Storage Life	85 ℃	1000 hours	Note 1
Low Temperature Storage Life	- 40 °C	1000 hours	Note 1
Non-Operating Thermal Shock	-40 / 125 $^\circ\!\!\mathbb{C}$, 15 min dwell /<10 sec transfer	300 cycles	Note 1

Notes:

1. A failure is a LED that is not fully lit, or less than 70% of its initial luminous flux.

2. All results of the above stress tests are 0 failures.

Failure Types

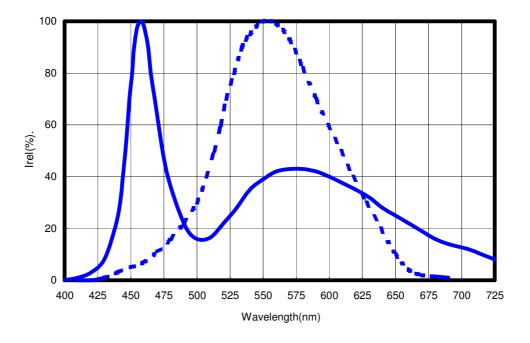
Catastrophic failures are failures that result in the LED emitting no light or very little light at normal current levels. Catastrophic failures are not expected for EdiStar series emitters that are handled and operated within the limits specified in EdiStar series documentation. Please refer to Absolute Maximum Ratings for more information on design limits.

Parametric failures are failures that cause key characteristics to shift outside of acceptable bounds. The most common parametric failure, for a high-power LED, is permanent light output degradation over operating life. Most other light sources experience catastrophic failure at the end of their useful life, providing a clear indication that the light source must be replaced. For instance, the filament of an incandescent light bulb breaks and the bulb ceases to create light. In contrast, high-power LEDs generally do not experience catastrophic failure but simply become too dim to be useful in the intended application. Further discussion of this matter can be found in the Long-Term Lumen Maintenance Testing section of this document. Another parametric failure common to white LEDs is a large and permanent shift in the exact color of white light output, called the white point or color point. A shift in white point may not be detectable in one LED by itself, but would be obvious in a



side-by-side comparison of multiple LEDs. Since each lighting installation commonly uses many high-power LEDs, white point stability is a point of concern for lighting designers. Typically, white high-power LEDs, created by combining blue LEDs with yellow (and sometimes red) phosphor, will shift towards blue over operational life. This shift can be accelerated by high temperatures and high drive currents. For example, a cool white (e.g., 6500K CCT) LED with a white point failure will typically appear light blue instead of white. In some high-power LEDs, this failure mode can occur after just 1,000 hours of operational life.

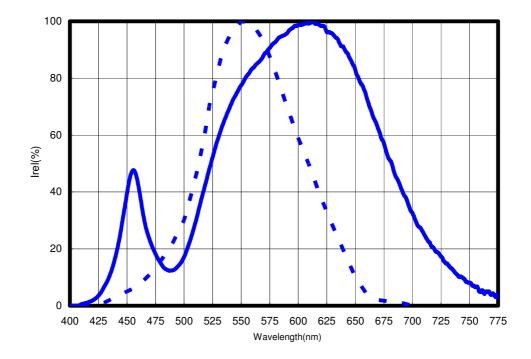
Just as with fluorescent light sources, all white high-power LEDs will experience shifts in white point over their operating lives. It is possible for the design of the phosphor and packaging systems to minimize these shifts and contain the shifts to be less than what is detectable by the human eye. As with catastrophic failures, parametric failures can be minimized by adhering to limits specified in EdiStar series documentation.



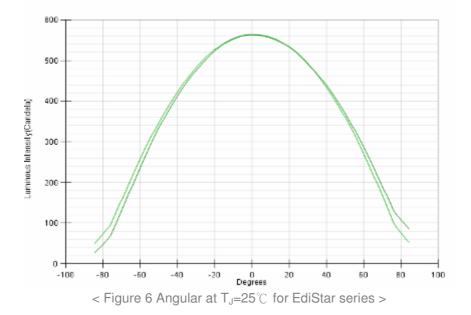
Color Spectrum and Radiation Pattern

< Figure 4 Cool White Color spectrum at $T_J = 25^{\circ}$ C.for EdiStar series >





< Figure 5 Neutral White \sim Warm White Color spectrum at T_J =25 $^\circ\!{\rm C}$.for EdiStar series >





Emission Angle Characteristics

< Table 8 Emission angle characteristics at Tj=25°C for EdiStar series>

Part Name	Color	20½(Typ.) Lambertian	Unit
ENEW-05-0707-EB-1	Cool White	120	Deg.
ENEH-05-0707-EE-1	Neutral White	120	Deg.
ENEX-05-0707-EE-1	Warm White	120	Deg.
ENEW-10-1010-EB-1	Cool White	120	Deg.
ENEH-10-1010-EE-1	Neutral White	120	Deg.
ENEX-10-1010-EE-1	Warm White	120	Deg.

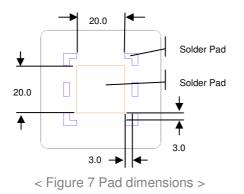
Correlated Color Temperature Characteristics TJ=25°C

< Table 9 Correlated Color Temperature Characteristics at $T_J\!=\!25^\circ\!\mathbb{C}$ for EdiStar series >

Part Name	Color	ССТ		Unit
i art Name	00101	Min.	Max.	Ont
ENEW-05-0707-EB-1	Cool White	5,000	10,000	К
ENEH-05-0707-EE-1	Neutral White	3,800	5,000	К
ENEX-05-0707-EE-1	Warm White	2,670	3,800	К
ENEW-10-1010-EB-1	Cool White	5,000	10,000	К
ENEH-10-1010-EE-1	Neutral White	3,800	5,000	К
ENEX-10-1010-EE-1	Warm White	2,670	3,800	К

Product Soldering Instructions

The central circle pad at the bottom face of the package provides the main path for heat dissipation from the LED to the heatsink (heatsink contact).



- 1. All dimensions are measured in mm.
- 2. MCPCB material with a thermal conductivity greater than 3.0 W/mK.
- 3. Please avoid touching the EdiStar center area during assembly processes .This may cause pollution or scratch on the EdiStar.



The choice of solder and the application method will dictate the specific amount of solder. For most consistent results, an automated dispensing system or a solder stencil printer is recommended.

Positive results will be used solder thickness that results in 50µm. The lamp can be placed on the PCB simultaneously with any other required SMD devices and reflow completed in a single step. Automated pick-and-place tools are recommended.

The central slug at the bottom face of the package provides the main path for heat dissipation from the LED to the heat sink (heat sink contact). A key feature of EdiStar series emitters are an electrically neutral heat path that is separate from the LED's electrical contacts. This electrically isolated thermal pad makes EdiStar series emitters perfect for use with either FR4 circuit boards with thermal via or with metal-core printed circuit boards (MCPCB).

Recommend Solder Steps

To prevent mechanical failure of LEDs in the soldering process, a carefully controlled pre-heat and post-cooling sequence is necessary. The heating rate in an IR furnace depends on the absorption coefficients of the material surfaces and on the ratio of the component's mass to its irradiated surface. The temperature of parts in an IR furnace, with a mixture of radiation and convection, cannot be determined in advance. Temperature measurement may be performed by measuring the temperature of a specific component while it is being transported through the furnace. Influencing parameters on the internal temperature of the component are as follows:

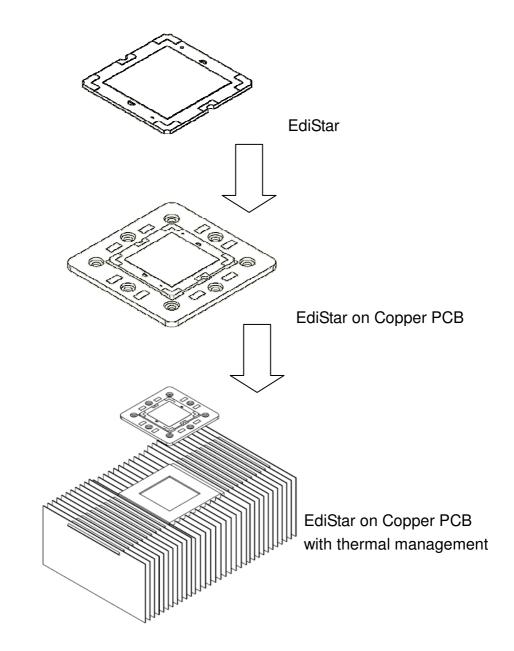
- Time and power
- Mass of the component (for EdiStar series emitters on MCPCB this is very important)
- Size of the component
- Size of the printed circuit board
- Absorption coefficient of the surfaces and MCPCB
- Packing density

Peak temperatures can vary greatly across the PC board during IR processes. The variables that contribute to this wide temperature range include the furnace type and the size, mass and relative location of the components on the board. Profiles must be carefully tested to determine the hottest and coolest points on the board. The hottest and coolest points should fall within the recommended temperatures. The profile of the reflow system should be based on design needs, the selected solder system and the solder-paste manufacturer's recommended reflow profile.



Product Thermal Application Information

Thermal grease should be evenly speeded with a thickness <100um. When assembling on Copper PCB and heatsink carrier.



< Figure 8 EdiStar series heatsink application >

Note:

EdiStar series emitter will generate ultra high thermal power, therefore its need a great-design heatsink to dissipate heat.



Suggested Adhesive for Selection(such as thermal grease)

· Ease of use

Non-solvent, One-part

Fast tack free

3 minutes at 25°C

No corrosion

Alcohol type of room temperature vulcanization (RTV)

Low volatility

Low weight loss of silicone volatiles

Adhesion

Excellent adhesion to most materials without use of a primer

Dielectric properties

Cured rubber exhibits good dielectric properties

- Excellent thermal stability and cold resistance

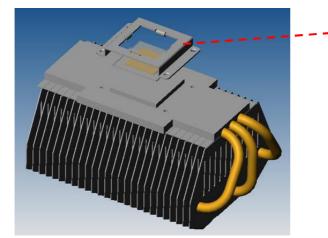
Cured rubber provides wide service temperature range

< Table 10 Specification for adhesive properties >

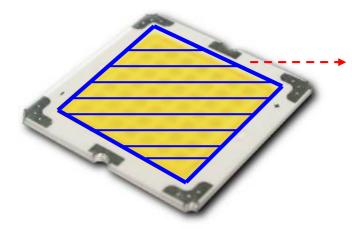
Specification	Suggested Properties
Take-free time	3~10 minutes
Specific gravity	< 3 g/cm ²
Thermal conductivity	> 2.5 W/mK
Rth in using	< 1.8 °C/W
Volume resistance	> 1x10 ¹⁴
Lap shear adhesion strength	$> 200 \text{ N/ cm}^2$
Tensile strength	> 4 Mpa



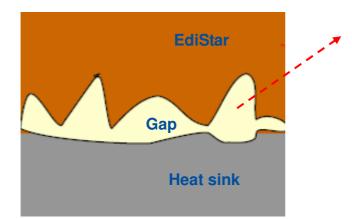
Recommended heat sink and attachment



The function of cover (clip) is used to fix and force EdiStar and heatsink to prevent the small gaps generate between the bottom surface of EdiStar and the top surface of heat sink.



The forced area is the edge of the EdiStar (outside the shaded emitting area). The applied force on the EdiStar should not exceed 3kgf/cm².



Without the applied force on EdiStar, the gap between EdiStar and heat sink will be filled with air. The thermal path would be affected such that the thermal resistance between the medium will increase.

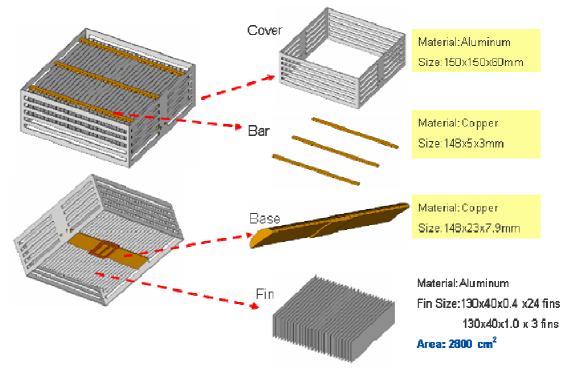
< Figure 9 Heatsink and attachment >



Example for Thermal Management

Example 1

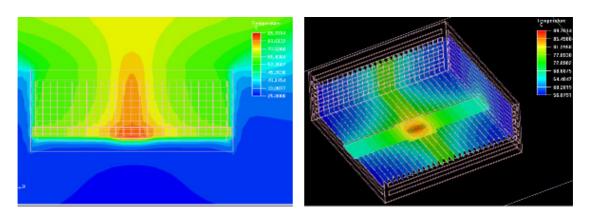
Mechanical Design



< Figure 10 Thermal heatsink design example 1 >

	Tmin (℃)	Tmax (°C)	Tavg (℃)
Chip	85.9	90.5	88.36
Slug	81.4	89.7	84.5
Base	65.8	88.1	73.8
Fin	56.1	84.8	66

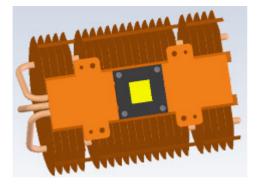
Thermal simulation data



< Figure 11 50W Thermal simulation temperature example 1 >



Example 2 Mechanical Design



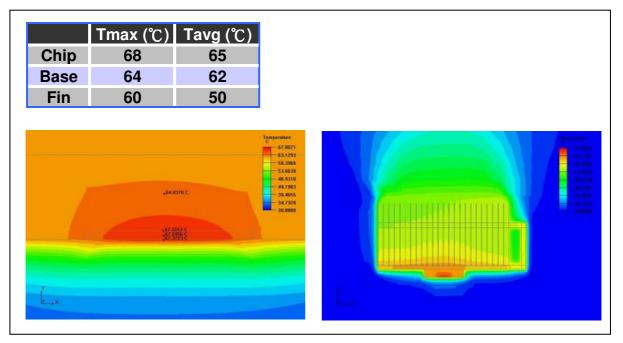
Dimension: 170x130x100 mm Surface area: 4,800cm²

Use copper plate, heat pipe and fin for the thermal module.

It can be more efficient to dissipate the LED generated heat

< Figure 12 Thermal heatsink design example 2>

Thermal simulation data

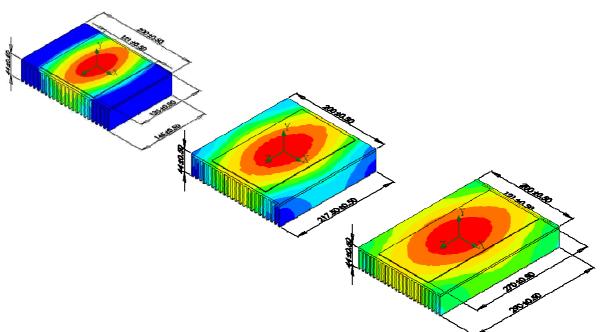


< Figure 13 50W Thermal simulation temperature example 2>



Example 3 Design using extruded heatsink

Aluminum alloy 6063 is used for the thermal simulation. It is the common material used for extrusion heatsink. The specifications and parameters serve as design reference and are conditioned in the most ideally free convection environment. Result would vary if the extruded heatsink is placed in an enclosed environment under different ambient temperature.



Thermal Management for Extruded Aluminum under free convection

(T_A=25℃; T_{HS}=60℃) 65,000 60,000 55.000 50.000 45,000 (cm2) 40,000 Surface Area 35,000 30,000 25,000 20,000 15,000 10,000 5,000 0 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75 80 85 90 95 100 105 110 Operating Power (W)



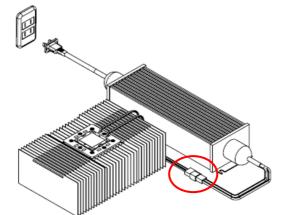


Product Electrical Application Information

Electrical Application

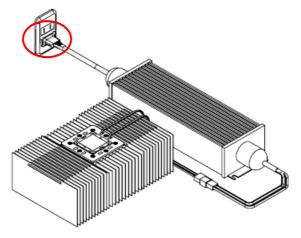
Following graphs and descriptions show how to connect LED or LED module and plug to AC outlet.

Step1: Connect the wires of LED Module to the DC output of the driver.



<Figure 15 LED Module connect to the DC output of the driver>

Step2: Plug the driver to AC outlet.



<Figure 16 Plug the AC output of the driver to AC outlet>

Caution: Never plug the driver to AC outlet before the LED Module is properly connected as this may generate transient voltage damage the LEDs permanently with a short or open circuit.



Recommended driver



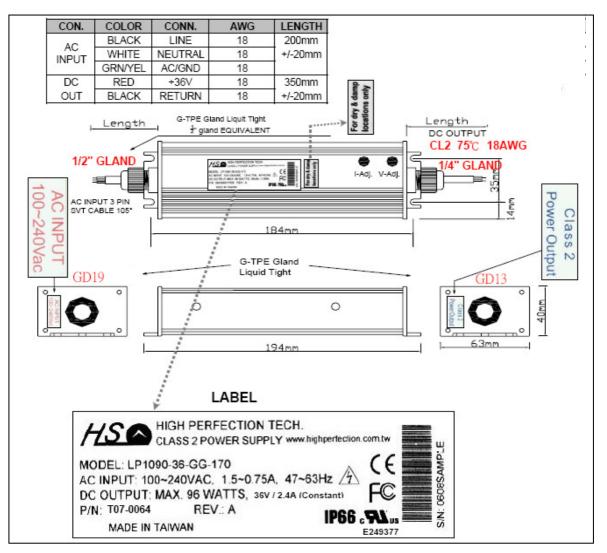
Part No.: EP-C50C-2400 DC Output Forward Voltage: 18~36V Output Current: 2.4A (constant current) Made by High Perfection

Safety standard Compliance UL1310,Class 2 (Recognized) UL48 LVDSafety Certifications E249377EMI Stantards : FCC 47 CFR PART 15 Class A CE EN55022,EN61000-3-2,-3 CE EN61000-4-2,3,4,5,6,8,11CSafety Certifications E249377	SAFETY
INPUT RANGE : 90~263VAC,1.5~0.75A@FULL LOAD FREQUENCY : 47~63Hz POWER FACTOR : >0.92 @115VAC @230VAC TOTAL HARMONIC DISTORTION : < 20% EFFICIENCY : 85% TYP. PEAK INRUSH : <20A@115VAC,40A@230VAC LEAKAGE CURRENT : <0.7mA/230Vac	INPUT
LOAD CAPACITY : 96 Watts DC OUTPUT MIN. MAX. LOAD REG. 36V 0.1Amps 2.66Amps +/-5% CALIBRATION SET : CONSTANT CURRENT MODE - 2.4A 36 ~ 18 V +/- 3% CREST FACTOR : 1.5 MAX.	Ουτρυτ
PROTECTION : OCP,SCP - AUTO RECOVERY COOLING : CONVECTION , ALUMINUM HOUSING OPERATION TEMPERTURE : -30 ~ 70°C TEMP. DE-RATING 1% PER°C FROM 50°C TO 70°C RELATIVE HUMIDITY : 5 ~ 95% STORAGE TEMP. : -40°C ~ 85°C RELIABILITY(MTBF) : >100,000Hours @25°C FULL LOAD (MIL-HDBK-217E) 3 YEARS WARANTY : PART AND LABOR APPLIABLE LOCATION : DRY OR DAMP,WET, IP66 Weight : 600g (Typical)	MISCELLANEOUS

< Figure 17 Recommended driver specifications >



Product Outlines (HS LP1090 series)



< Figure 18 Recommended driver dimensions >



Recommended driver

Part No.: CLG-150-36-A DC Output Forward Voltage: 27~36V Output Current: 4.2A (constant current)

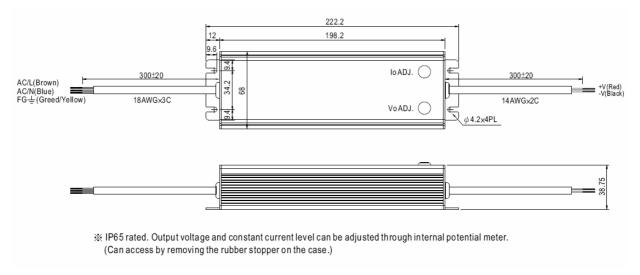


VOLTAGE ADJ. RANGE Note.6 CURRENT ADJ. RANGE	11 A 132 W 150 mVp-p 9 ~ 13 V	15V 11.25 ~ 15V 9.5A 142.5W 150mVp-p 13 ~ 17V vy internal potential 4.75 ~ 9.5A ±2.0%	20V 15 ~ 20V 7.5A 150W 150mVp-p 17 ~ 22V meter or through o 3.75 ~ 7.5A	24V 18~24V 6.3A 151.2W 150mVp-p 22~27V	30V 22.5 ~ 30V 5A 150W 150mVp-p	36V 27~36V 4.2A 151.2W 150mVp-p	48V 36~48V 3.2A 153.6W		
RATED CURRENT RATED POWER RIPPLE & NOISE (max.) Note.2 VOLTAGE ADJ. RANGE Note.6 CURRENT ADJ. RANGE VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	11A 132W 150mVp-p 9~13V Can be adjusted b 5.5~11A ±2.0% ±0.5%	9.5A 142.5W 150mVp-p 13 ~ 17V y internal potential 4.75 ~ 9.5A	7.5A 150W 150mVp-p 17 ~ 22V meter or through of	6.3A 151.2W 150mVp-p	5A 150W 150mVp-p	4.2A 151.2W	3.2A 153.6W		
RATED POWER RIPPLE & NOISE (max.) Note.2 VOLTAGE ADJ. RANGE Note.6 CURRENT ADJ. RANGE VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	132W 150mVp-p 9~13V Can be adjusted b 5.5~11A ±2.0% ±0.5%	142.5W 150mVp-p 13 ~ 17V vy internal potential 4.75 ~ 9.5A	150W 150mVp-p 17 ~ 22V meter or through o	151.2W 150mVp-p	150W 150mVp-p	151.2W	153.6W		
RIPPLE & NOISE (max.) Note.2 VOLTAGE ADJ. RANGE Note.6 CURRENT ADJ. RANGE VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	150mVp-p 9 ~ 13V Can be adjusted b 5.5 ~ 11A ±2.0% ±0.5%	150mVp-p 13 ~ 17V oy internal potential 4.75 ~ 9.5A	150mVp-p 17 ~ 22V meter or through o	150mVp-p	150mVp-p				
VOLTAGE ADJ. RANGE Note.6 CURRENT ADJ. RANGE VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	9 ~ 13V Can be adjusted b 5.5 ~ 11A ±2.0% ±0.5%	13 ~ 17V y internal potential 4.75 ~ 9.5A	17 ~ 22V meter or through o			150mVp-p			
VOLTAGE ADJ. RANGE Note.6 CURRENT ADJ. RANGE VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	9 ~ 13V Can be adjusted b 5.5 ~ 11A ±2.0% ±0.5%	13 ~ 17V y internal potential 4.75 ~ 9.5A	17 ~ 22V meter or through o				200mVp-p		
VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	5.5~11A ±2.0% ±0.5%	4.75~9.5A		-	26~32V	31~41V	40~56V		
VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME	5.5~11A ±2.0% ±0.5%	4.75~9.5A		Can be adjusted by internal potential meter or through output cable					
LINE REGULATION LOAD REGULATION SETUP, RISE TIME	±0.5%	±2.0%	3.75 ~ 7.0A	3.15~6.3A	2.5~5A	2.1~4.2A	1.6~3.2A		
LOAD REGULATION SETUP, RISE TIME			±2.0%	±1.0%	±1.0%	±1.0%	±1.0%		
SETUP, RISE TIME	14 09/	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%		
	±1.0%	±1.0%	±1.0%	±0.5%	±0.5%	±0.5%	±0.5%		
	3000ms. 80ms at	=	/115VAC						
	50ms / 230 VAC	16ms / 115VAC							
,									
FREQUENCY RANGE	47~63Hz	.2. 000100							
		C PE>0.98/1	15VAC at full load	and rated output vo	Itage PE > 0	9 at 75 ~ 10.0% loa	d		
							90%		
	/*	*	30%	30 /6	3076	0376	50 70		
. ,									
LEARAGE CURRENT									
OVER CURRENT (Typ.) Note.4									
SHORT CIRCUIT Note.8					22 201	40.40%	57 ~ 65V		
PROTECTION OVER VOLTAGE		1.4 241				42~48V	5/~050		
			n off o/p voltage, n	s-power on to recov	ver				
OVER TEMPERATURE									
	-30 ~ +55 °C@ full load ; +70 °C@ 60% load								
	20 ~ 95% RH non-condensing								
STORAGE TEMP., HUMIDITY	-40~+80°C, 10~95% RH								
	±0.03%/'C (0~5	0°C)							
VIBRATION	10 ~ 500Hz, 5G 12min./1cycle, period for 72min. each along X, Y, Z axes								
SAFETY STANDARDS Note.9	UL1012; EN61347-1, EN61347-2-13 independent; UL60950-1, TUV EN60950-1 (TBD)								
WITHSTAND VOLTAGE	I/P-O/P:3.75KVAC I/P-FG:1.88KVAC O/P-FG:0.5KVAC								
ISOLATION RESISTANCE	I/P-O/P, I/P-FG, O/P-FG:100M Ohms / 500VDC / 25°C/ 70% RH								
EMI CONDUCTION & RADIATION									
HARMONIC CURRENT	Compliance to EN61000-3-2 Class C (≥75% load); EN61000-3-3								
EMSIMMUNITY	Compliance to EN61000-4-2,3,4,5,6,8,11; ENV50204, EN61547, EN55024, light industry level (surge 4KV), criteria A								
MTBF	303.7Khrsmin.	MIL-HDBK-217F	(25°C)						
DIMENSION	222°68°39mm (L*W*H)								
PACKING	1.0Kg; 12pcs/13K	g/0.49CUFT(CLG-	150-A/B) 1Kg	; 12pcs/13Kg/0.96	CUFT(CLG-150-C)				
 Ripple & noise are measure Tolerance : includes set up Constant current operation i reconfirm special electrical in Derating may be needed un Type A and type C only. Please refer to derating curv. 	d at 20MHz of ba tolerance, line reg region is within 75 equirements for s ider low input volt /e.	ndwidth by using a julation and load re % ~100% rated of ome specific syste	a 12" twisted pair- egulation. utput voltage. This em design.	wire terminated wi is the suitable op	th a 0.1 uf & 47uf p eration region for L	arallel capacitor.	ations, but please		
	OWER FACTOR FFICIENCY (Typ.) AC CURRENT NRUSH CURRENT(max.) LEAKAGE CURRENT OVER CURRENT (Typ.) Note.4 SHORT CIRCUIT Note.8 OVER VOLTAGE OVER VOLTAGE OVER TEMPERATURE VORKING TEMP. Note.7 VORKING TEMP. NOT.7 SOLATION TEMP. TEMP. NOT.7 SOLATION TEMP. T	OWER FACTOR PF ≥0.95/230VA/ 88% EFFICIENCY (Typ.) 88% AC CURRENT 2A / 115VAC NRUSH CURRENT(max.) COLD START 65. LEAKAGE CURRENT <1mA / 240 VAC	OWER FACTOR $PF \ge 0.95/230VAC$ $PF \ge 0.98/1$ EFFICIENCY (Typ.) 88% 88% AC CURRENT 2A/115VAC 1A/230VAC NRUSH CURRENT (max.) COLD START 65A/230VAC EAKAGE CURRENT LEAKAGE CURRENT <1mA/240VAC	OWER FACTOR PF ≥0.95/230VAC PF ≥0.98/115VAC at full load. EFFICIENCY (Typ.) 88% 88% 90% AC CURRENT 2A / 115VAC 1A / 230VAC 90% NRUSH CURRENT (max.) COLD START 65A/230VAC ImA / 240VAC JEAKAGE CURRENT <1mA / 240VAC	NOWER FACTOR PF $\ge 0.95/230VAC$ PF $\ge 0.98/115VAC$ at full load and rated output voltage EFFICIENCY (Typ.) 88% 88% 90% 90% AC CURRENT 2A/115VAC 1A/230VAC 90% NRUSH CURRENT(max.) COLD START 65A/230VAC	OWER FACTOR PF≥0.95/230VAC PF≥0.98/115VAC at full load and rated output voltage PF≥0. EFFICIENCY (Typ.) 88% 88% 90% 90% 90% VC CURRENT 2A/ 115VAC 1A/230VAC 90% 90% 90% VC CURRENT 2A/ 115VAC 1A/230VAC 90% 90% 90% VC CURRENT (Typ.) 88% 88% 90% 90% 90% VC CURRENT (Typ.) 88% 95 ~ 108% 95 ~ 108% Protection type : Constant current limiting, recovers automatically after fault condition is removed VVER CURRENT (Typ.) 95 ~ 108% Protection type : Shut down and latch of olp voltage, re-power on to recover 100°C ±10°C (RTH2) VVER TEMPERATURE Protection type : Shut down o/p voltage, re-power on to recover 100°C ±10°C (10 ~ 95% RH VORKING TEMP, Note, 7 30 ~ +55°C @ full load ; +70°C @ 60% load 20 ~ 95% RH VORKING TEMP, Note, 7 30 ~ +55°C @ full load ; +70°C @ 60% load 20 ~ 95% RH VIEMP COEFFICIENT ±0.03%/°C (0 ~ 50°C) 10 ~ 500H2; 50 12min./1cycle, period for 72min. each along X, Y, Z axes SAFETY STANDARDS Note, 8 UL1012; EN61347-1, EN61347-2-13 independent; UL60950-1, TUV EN60950-1 (TBD) VIP-O/P, I/P-FG,	OWER FACTOR PF ≥0.95/230VAC PF ≥0.98/115VAC at full load and rated output voltage PF ≥0.9 at 75~100% loa FFFICIENCY (Typ.) 88% 90% 90% 90% 90% 89% 89% C CURRENT 2A/115VAC 1A/230VAC 90% 90% 90% 89% 89% C CURRENT (max.) COLD START EXA/230VAC 89% 90% 90% 90% 89% 89% VER CURRENT (Typ.) COLD START EXA/230VAC 95 ~ 108% Protection type : Constant current limiting, recovers automatically after fault condition is removed 95 ~ 48V Y2 ~ 48V		

< Figure 19 Recommended driver specifications >



Product Outlines (CLG-150-36-A)

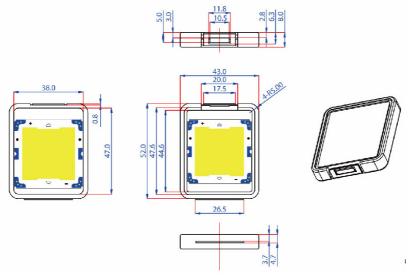


< Figure 20 Recommended driver dimensions >



Product Packaging Information

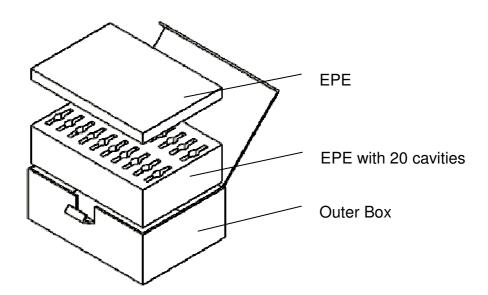
Package Specifications



< Figure 21 PP box dimensions>

Notes:

- 1. All dimensions are in mm.
- 2. Tolerance: ±0.2mm



< Figure 22 Outer box and EPE package>

< Table 11 Package dimensions and quantity >

Item	Quantity	Dimensions(mm)		
PP Box	1 pc	52*43*8		
Outer box	20 PP boxes	240*170*90		