

TV VERTICAL OUTPUT CIRCUIT

The KA2131 is a monolithic integrated circuit designed for the vertical output stage in color television receivers.

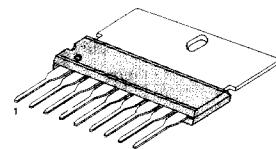
FUNCTIONS

- Driver stage.
- Output stage.
- Flyback generators.
- Pulse shapers.

FEATURES

- Low power consumption, direct deflection coil driving capability
(Flyback voltage is two times as high as the supply voltage is supplied during flyback period only).
- High breakdown voltage: 60V.

9 SIP H/S



BLOCK DIAGRAM

ORDERING INFORMATION

Device	Package	Operating Temperature
KA2131	9 SIP H/S	-20 ~ +70°C

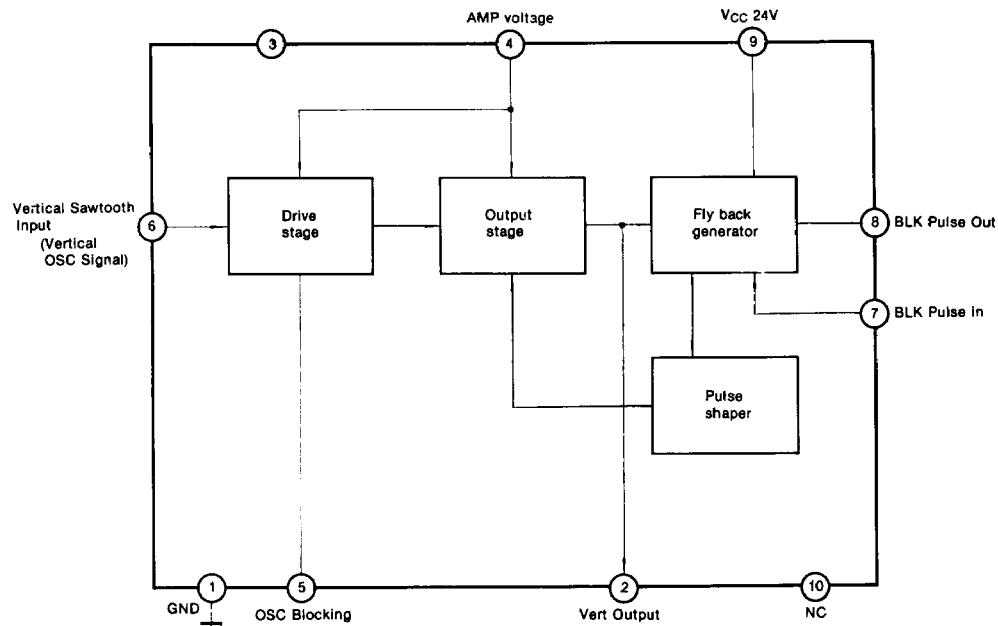
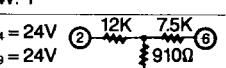


Fig. 1

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	27.6	V
	V ₄	60	V
Circuit Voltage	V ₆	2.5	V
	V ₇	1.3	V
Supply Current	I _{CC}	250	mA
Power Dissipation	P _d	6.66	W
Circuit Current	I ₂	-1000 ~ +1000	mA _{p-p}
	I ₈	-1000 ~ +1000	mA _{p-p}
Operating Temperature	T _{OPR}	-20 ~ +70	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Deflection Current	I _{YP-P}	SW:2	860	930	1000	mA _{p-p}
Deflection Current Linearity	ΔIy (+)	SW: 1	25	—	75	mA _{p-p}
	ΔIy (-)	SW:1	22	—	85	mA _{p-p}
Deflection Current vs. Operating Temperature	ΔIy/T _a	T _a = -20 ~ +70°C	-1.5	—	1.5	%
Center Voltage	V _{MID}	SW: 1	12.1	12.6	13.1	V
Flyback Pulse Amplitude	V(FBP)	SW: 1	47	—	—	V
Flyback Pulse Width	t _{FBP}	SW: 1	850	920	980	μsec
Quiescent Circuit Current	I _Q	V ₄ =24V V ₉ =24V V ₇ =0V 	7	13	22	mA
Output TR Saturation Voltage	V ₄₋₂	V ₄ =V ₉ =24V, pin ₂₋₁ =56Ω V ₆ =0.3V, V ₇ =0V	—	2.7	3.7	V
	V ₂	V ₄ =V ₉ =24V, pin ₂₋₄ =56Ω V ₆ =1.3V, V ₇ =0V	—	0.6	1.0	V
Saturation Voltage	V ₈	V ₉ =24V, R _{pin9-8} =1.2KΩ V ₇ =0V	—	—	0.5	V
Thermal Resistance	R _{th (j-c)}		—	—	12	°C/W

TYPICAL APPLICATION CIRCUIT

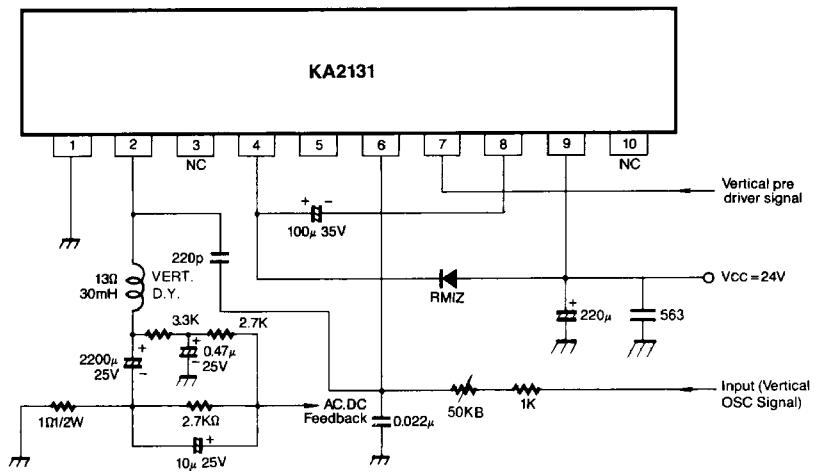


Fig. 2

TEST CIRCUIT

