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	SPECIFICATION	GROUP
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	DEVICE SPECIFICATION F	TOR
	TFT-LCD module	
	TFT-LCD module MODEL No. LQ057Q3DC1	
CUSTOMER'S API	MODEL No. LQ057Q3DC1	

PRESENTED

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RECORDS OF REVISION

MODEL No: LQ057Q3DC12

SPEC No : LA1-05016

	NO.	PAGE	SUMMARY	NOTE
2005.5.30		-		1st Issue
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1. Application

This specification applies to color TFT-LCD module, LQ057Q3DC12.

2. Summary and Features

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT LCD panel, driver ICs, control circuit (PWB), FPC, front and back polarizer, sealed case, backlight unit. Graphics and texts can be displayed on a $320 \times RGB \times 240$ dots panel with 262,144 colors by suitable control supplying from the outside.

It isn't composed DC/AC inverter for a backlight drive.

- · The input of QVGA and VGA signal is possible.
- Wide Viewing Angle technology is adopted. (The most suitable viewing angle is in the 12 o'clock direction.)
- · By adopting an active matrix drive, a picture with high contrast is realized.
- · Reflection due to external light is minimized through the use of a low reflection black matrix.
- · A thin, light and compact module is accomplished through the use of COG mounting technology.
- Through the use of high color purity color filter and TN-normally white mode excelled in color reproducibility, an image with highly natural color reproduction is realized.
- High viewing angle and high brightness (500cd/m2).
- · An inverted video display in the vertical / horizontal directions is possible.
- · This module is adapted to RoHS compliance.

3. Mechanical specifications

table 3-1

Parameter	Specifications	Units
Screen size	14.4 (5.7 inch) diagonal	cm
Active area	115.2 (H) × 86.4 (V)	mm
Display format	320(H)×240(V)	pixels
	(1 pixel = R + G + B dots)	
Dot pitch	0.360 (H) × 0.360 (V)	mm
Pixel configuration	R,G,B Stripe configuration	
Display mode	Normally white	
Outline dimension [Note1-1]	144.0 (W)×104.6 (H)×13.0(D)	mm
Mass	$220\!\pm\!20$	g
Surface treatment (Surface hardness)	Hard coat (3H)	

[Note 1-1]

A connector of backlight is excepted.

A figure of outline dimension is shown in Fig. 1.

4. Input terminal

4-1. TFT-LCD panel driving part

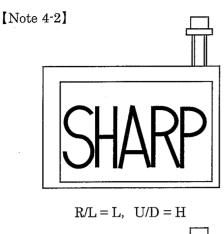
CN1

Used connector: FH12-33S-0.5SH(55) (HIROSE ELECTRIC CO., LTD.)

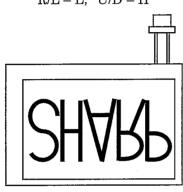
Table 4-1 Functional table of CN1 terminals

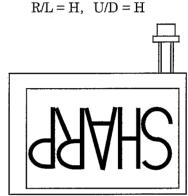
Table 4 Pin No.	Symbol	nctional table of CN1 terminals Description	Polarity
1	GND	ground	
2	CK	Clock signal for sampling each data signal	
3	Hsync	Horizontal synchronous signal	Negative
4	Vsync	Vertical synchronous signal	Negative
5	GND	ground	
6	RO	RED data signal(LSB)	
7	R1	RED data signal	
8	R2	RED data signal	
9	R3	RED data signal	
1 0	R4	RED data signal	
1 1	R5	RED data signal(MSB)	
1 2	GND	ground	
1 3	G0	GREEN data signal(LSB)	
1 4	G1	GREEN data signal	
1 5	G2	GREEN data signal	
1 6	G3	GREEN data signal	
1 7	G4	GREEN data signal	,
1 8	G5	GREEN data signal(MSB)	
1 9	GND	ground	
2 0	В0	BLUE data signal(LSB)	
2 1	B1	BLUE data signal	
2 2	B2	BLUE data signal	
2 3	В3	BLUE data signal	
2 4	B4	BLUE data signal	
2 5	B5	BLUE data signal(MSB)	
2 6	GND	ground	
2 7	ENAB	Signal to settle the horizontal display position [Note4-1]	Positive
2 8	Vcc	+3.3V power supply	
2 9	Vcc	+3.3V power supply	·
3 0	R/L	Selection signal for horizontal scanning direction [Note4-2] ("L": Normally, "H": Right-and-Left reversal)	
3 1	U/D	Selection signal for vertical scanning direction [Note4-2] ("H": Normally, "L": Up-and-Down reversal)	
3 2	V/Q	Selection signal for VGA or QVGA ("H": VGA, "L": QVGA)	
3 3	GND	ground	

[Note 4-1] The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in Fig7. Don't keep ENAB "High" during operation.









R/L = L, U/D = L

R/L = H, U/D = L

4-2. Backlight fluorescent tube driving part

CN2 Used connector: BHR-03VS-1 (JST Co., Ltd.)

Suitable connector: SM02(8.0)B-BHS-1N-TB (JST Co., Ltd.)

Table 4-2 Functional table of CN2 terminals

Terminal No.	symbol	function	Color of FL cable
1	V_{LOW}	input terminal (Low Voltage Side)	White
2	NC	Non connection	
3	V_{HIGH}	input terminal (High Voltage Side)	Red

5. Absolute maximum ratings

Table 5-1

able 5-1					
Parameter	Symbol	Condition	Ratings	Unit	Note
Input voltage	VI	Ta=25℃	$-0.3 \sim +6.0 \text{V}$	V	[Note 5-1]
+3.3V power supply voltage	Vcc	Ta=25℃	$0 \sim +4.0$	V	
Storage temperature	Tstg		$-30 \sim +80$	$^{\circ}$	[Note 5-2,3]
Operating temperature (Panel surface temperature)	Topr1		$-10 \sim +70$	$^{\circ}$	[Note5-2,3,4]
Operating temperature (Ambient temperature)	Topr2		$-10 \sim +70$	$^{\circ}$	[Note 5-5]

[Note 5-1] CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

[Note 5-2] This rating applies to all parts of the module and should not be exceeded.

[Note 5-3] Maximum wet-bulb temperature is less than 39°C. Condensation of dew must be avoided as electrical current leaks will occur, causing a degradation of performance specifications.

[Note 5-4] The operating temperature only guarantees operation of the circuit. For contrast, speed response, and other factors related to display quality, judgment is done using the ambient temperature $Ta = +25^{\circ}C$.

[Note 5-5] Ambient temperature when the backlight is lit (reference value).

6. Electrical characteristics

6-1. TFT-LCD panel driving section

Table 6-1

TT _o	-05	2
Tа	-20	\cup

able o 1.							
P	arameter	Symbol	MIN	TYP	MAX	Unit	Remarks
+3.3V	Supply voltage	Vcc	+3.0	+3.3	+3.6	V	[Note 6-1]
power suply	Current dissipation	Icc		130	160	mA	[Note 6-2]
Permissive in	put ripple	$V_{ ext{RF}}$		_	100	MVp-p	Vcc=+3.3V
Input Low vo.	ltage	$V_{\rm IL}$	0	_	$0.3 \mathrm{Vcc}$	V	
Input High vo	Input High voltage		0.7Vcc	_	+5.5	V	[Note 6-3]
Input leak cu	rrent (Low)	I_{OL}	—	_	10	$\mu \mathbf{A}$	V _I =0V
		·					[Note 6-3]
Input leak cu	rrent (High)	I_{OH1}	_	-	10	μΑ	$V_{I}=3.3\sim5.0V$
							[Note 6-4]
		$ m I_{OH2}$	_		100	μΑ	$V_{I}=3.3\sim5.0V$
							[Note 6-5]

[Note 6-1]

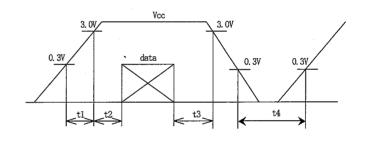
Vcc turn-on/off conditions.

 $t1 \le 20ms$

 $0 < t2 \le 50 \text{ms}$

 $0 \le t3 \le 1s$

1s<t4



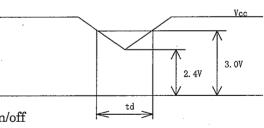
Vcc-dip conditions

1)
$$2.4V < Vcc < 3.0V$$

td ≤ 20msec

2) At Vcc < 2.4V

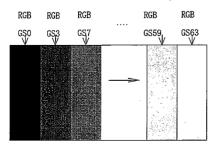
Vcc-dip conditions should also follow the Vcc turn-on/off conditions.



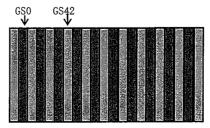
[Note 6-2] Vcc = 3.3V, V/Q = "H"

Typical current situation: Timing; Typical, 64-gray-bar pattern.

Max current situation: Timing; Typical, Vertical stripe pattern alternating 0 gray scale (GS0) with 42 gray scale (GS42) every 1 dot.



Typical current situation



Max current situation

[Note 6-3] $CK,R0\sim R5,G0\sim G5,B0\sim B5,Hsync,Vsync,ENAB,R/L,U/D,V/Q$

[Note 6-4] $CK,R0\sim R5,G0\sim G5,B0\sim B5,Hsync,Vsync,R/L,U/D$

[Note 6-5] ENAB, V/Q

6-2. Backlight driving section

The backlight system is an edge-lighting type with single CCFT (Cold Cathode Fluorescent Tube). The characteristics of Lamp are shown in the following table.

Table 6-2

Parameter		Symbol	MIN	TYP	MAX	Unit	Remarks		
lamp voltage		VL	620	620 690 760		Vrms	I L = 5 mArms		
lamp curi			4.5	5.0	5.5	mArms	ordinary state		
lamp pow			_	3.5	_	W	ratings		
lamp freq	uency	f L	30	_	60	kHz			
kick-off	Ta=+25℃	VS		_	1350	Vrms	A shield is		
voltage Ta=-10°C			_	_	1470		connected to GND.		

Inverter: HIU-288 [Output condenser: 22pF] TOSHIBA HARISON LIGHTING co. Ltd.)

[Caution]

Use the inverter providing symmetrical sine-wave in positive/negative polarity with no spike. A shield(module's metal shield) should be connected to GND of inverter circuit.

7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.7

7-1. Timing characteristics

Table 7-1

Paran	neter	Symbol	MIN	TYP	MAX	Unit	Remarks
Clock	frequency	1/Tc	. —	25.18	28.33	MHz	V/Q = H
		•	_	6.3	7.0		V/Q = L
	Duty ratio	TH/T	40	50	60	%	
Data	Setup time	Tds	5		_	$\mathbf{n}\mathbf{s}$	
	Hold time	Tdh	10			ns	
Horizontal sync.	Cycle	TH	30.0	31.8	_	μs	V/Q = H
signal			770	800	900	clock	
		TH	50.0	63.6		μs	V/Q = L
			360	400	450	clock	
	Pulse width	THp	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	V/Q = H
signal			251	262	280		V/Q = L
	Pulse width	TVp	2	_	34	line	
Horizontal display	period	THd	320	320	320	clock	
Hsync-Clock phase	difference	THc	10		Tc-10	ns	
Hsync-Vsync phase	e difference	TVh	0	_	TH-THp	μs	
Vertical display sta	art position	TVs	34	34	34	line	V/Q = H
	_		7	7	7		V/Q = L

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined by the rising edge of ENAB signal.

Table 7-2

Para	symbol	Min.	Typ.	Max.	Unit	Remark	
Enable signal	Setup time	Tes	5		Tc-10	ns	
	Pulse width	Tep	2	320	TH-10	clock	-
Hsync-Enable si	gnal phase	The	44		TH-664	clock	V/Q = H
difference	2			TH-340		V/Q = L	

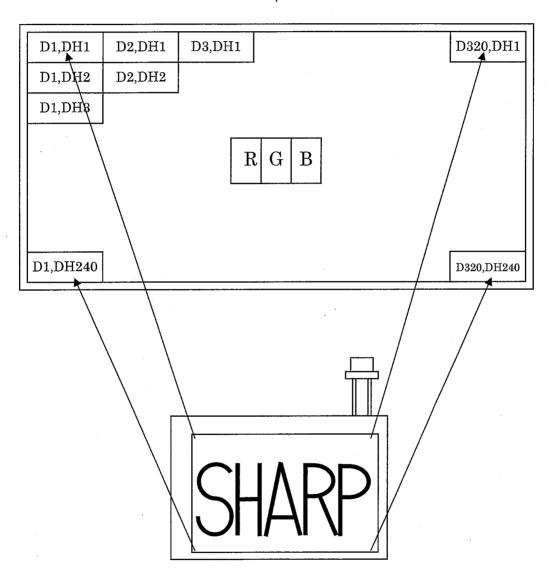
Note) When ENAB is fixed "Low", the display starts from the following data as shown in Fig.7. V/Q = "H" C104 (clock) V/Q = "L" C52 (clock)

7-3. Vertical display position

The Vertical display start position (TVs) is fixed 34 line (V/Q = H) and 7 line (V/Q = L). ENAB signal has no relation to the vertical display position.

7-4. Input Data Signals and Display Position on the screen





Display position of input data (H,V)

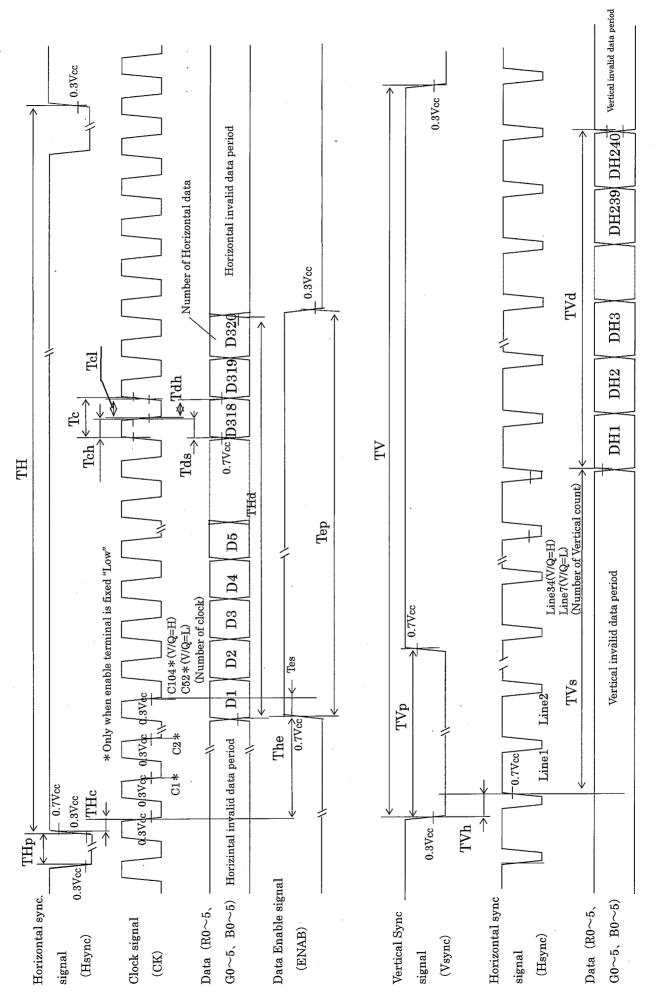


Fig 7. Input signal waveforms

(8) Input Signals, Basic Display Color and Gray Scale of Each Color

	8) Input Signals, Basic Display Color and Gray Scale of Each Color																			
	Colors &						Da	ta sig	nal				·							
	Gray scale	Gray	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	B1	B2	ВЗ	B4	B5
		Scale																		
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
В	Green	_	0	0	0	0_	0	0	1	1	1	1	1	1	0	0	0	0	0	0
asic	Cyan		0	0	0	0	0	0	1	1	1 .	1	1	1	1	1	1	1	1	1
Basic color	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		1	11	1	1	1	1	0	0	0	0	0	0	1	1	1	1	_1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	_1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
,	Black	GS0	0	0	0	0	0	0	. 0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	_1	0	0	0_	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of red	Darker	GS2	0	1	0	0	0	0	0	0	. 0	0	0	0	0	0	0	0	0	0
Sca	仓	Ψ.			1	,					1							l		
ıle o:	Û	\downarrow				/					1	<u> </u>						<u>ا</u>		
f red	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û.	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G:	仓	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
ray (Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Gray Scale	仓	V			. 1	, .					1						V	V		
e of	Û	↓			1						1	/						<u>ا</u>		
of green	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
n n	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
iray	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Gray Scale of bleu	仓	\			1	,		:			1	,					Ņ	l		
le of	Û	\			1						1							<u> </u>		
ble	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
-	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	11	1
	Bleu	GS63	0	0	0	0	0.	0	0	0	0	0	0	0	1	1	1	1	1	1
		lovel volt			TT: 1		770]+													

0:Low level voltage 1:High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9. Optical characteristics

Table 9-1						Ta=25 C,	VCC = +3	.3V
Paramete	r	Symbol	Condition	Min	Тур	Max	Unit	Remarks
		θ 21, θ 22		60	65	_	° (degree)	[Note 9-1]
Viewing angle range		θ 11	$CR \ge 5$	60	65	_	° (degree)	
		θ 12		35	40		° (degree)	4
Contrast 1	ratio	CRmax	Optimal	250	350			[Note 9-2]
			viewing angle					
Response	Rise	τr	$\theta = 0^{\circ}$		8	20	ms	[Note 9-3]
time	Fall	τd			21	40	ms	
White chromaticity		х		0.263	0.313	0.363		
l T		у	IL=5mArms	0.279	0.329	0.379		[Note 9-4]
Luminano	:e	Y		350	500	_	cd/m ²	
lamp life t	time +25°C	-	continuation	40,000	50,000	_	hour	[Note 9-5]

Lamp test in the module is made with the following inverter:.

HIU-288 [Output condenser: 22pF] TOSHIBA HARISON LIGHTING co. Ltd.

* The optical specifications are measured 30 minute after turing lamp on and in a dark room or equivalent condition, according to the method shown in Fig.9-1 below.

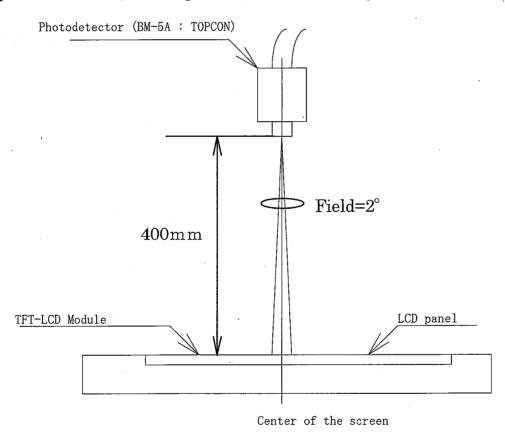
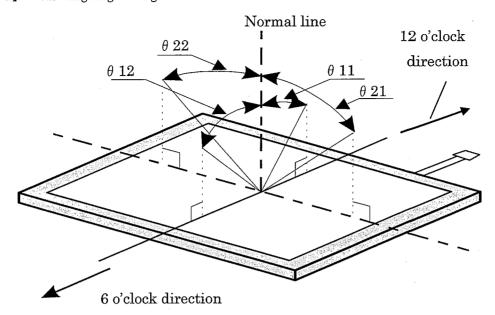


Fig.9-1 Optical characteristics measurement method

[Note 9-1] Viewing angle range is defined as follows.



The best viewing angle of this module is slightly leaned to 12 o'clock from normal line.

Where $\theta_{11} > \theta_{max}$, gray scale is reversed partially.

Where $\theta_{11} < \theta_{max}$, or in θ_{12} direction, gray scale isn't reversed.

[Note 9-2] Contrast ratio is defined as follows:

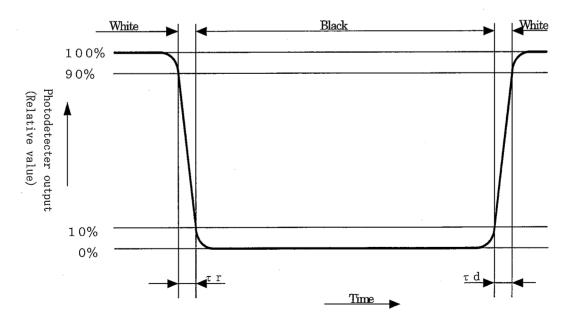
Luminance (brightness) with all pixels white

Contrast ratio(CR)= -

Luminance (brightness) with all pixels black

* Measurement point: Center of the active area

[Note 9-3] Response time is obtained by measuring the transition time of photo detector output, when input signals are applied so as to make the area "black" to and from "white".



[Note 9-4] This parameter should be measured at the center of the screen and 30 minutes after turn-on.

Inverter frequency: 49kHz

The characteristics are measured when the driver circuit is not powered.

[Note 9-5] The data for lamp is for your reference, because lamp is consumable component.

a) Lamp life time is defined by either 1 or 2 below. (Continuous turning on at Ta=25°C, IL=5mA rms)

- 1 When a brightness of lamp surface become 50% of the initial value under the standard condition.
- ② When a kick-off voltage in Ta= -10° C exceeded maximum value(1470 Vrms) at the output of inverter circuitry.
- b) In case of operating under lower temp. environment, the lamp exhaustion is accelerated and the brightness becomes lower.
 (Continuous operating for around 1 month under lower temp. condition may reduce the

brightness to half of the original brightness.)

10. Handling instructions

10-1. Mounting of modules

① The TFT-LCD module is designed to be mounted on equipment using the mounting tabs in the four corners of the module at the rear side. On mounting the module, as the nominal diameter 3.0mm tapping screw (fastening torque is 0.25 through 0.30 N⋅m) is recommended, be sure to fix the module on the same plane, taking care not to wrap or twist the module. The depth of tapping screw embedded into LCD module must be less than 5mm.

The pressing module, (ex. touching switch etc.) causes disordered image. So taking care for pressure not to conduct directly to LCD module.

- 2 Please power off the module before you connect or disconnect input connector.
- 3 Connect the metallic shielding cases of the module and the ground pattern of the inverter circuit firmly. If that connection is not perfect, there may be a possibility that the following problems happen.
 - a) The noise from the backlight unit will increase.
 - b) The output from inverter circuit will be unstable.
 - c) In some cases, a part of module will heat up.

10-2. Precautions in mounting

- ① Polarizer which is made of soft material and susceptible to flaw must be handled carefully.
- 2 Method of removing dust from polarizer.
 - Blow off dust with N2 blower for which static electricity preventive measure has been taken.
 - Since the polarizer is easily damaged, wiping should be avoided. If the panel has stain or finger grease, we recommend to use adhesive tape to softly remove them from the panel. Inevitable, wipe off by cleaning cloth for a lens with carefully, breathing on it.
- ③ When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth. For stubborn dirties, wipe the part, breathing on it.
- 4 Wipe off water drop or finger grease immediately. Long contact with water may cause discoloration or spots.
- (5) TFT LCD module uses glass which breaks or cracks easily if dropped or bumped on hard surface. handle with care.
- 6 Since CMOS LSIs are incorporated in this module, take care of electrostatic and earth human body while handling.

10-3. Caution of product design

The notes and cautions below should be followed when product is designed with this module.

- ① The LCD module should be protected against water salt-water by the waterproof cover.
- 2 Take enough shielding countermeasure not to interfere to peripheral electric device.

10-4. Others

- ① Do not expose the module to direct sunlight or intensive ultraviolet rays for many hours; liquid crystal is deteriorated by ultraviolet rays.
- ② If stored at the temperature lower than rated temperature, the liquid crystal may freeze and it may cause LCD panel damage. If storage temperature exceeds the specified rating, the molecular orientation of the liquid crystal may change to that of a liquid, and they may not revert to their original state. Store the module in normal room temperature.
- ③ The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump lead line around.
- ④ If LCD panel breaks, there may be a possibility that the liquid crystal escapes from the panel. If the liquid crystal drip into the eyes or mouth washes it out immediately.
- (5) The caution to other ordinal electronic component should be followed also.

11. Shipping requirements

- a) Number of layers of cartons in pile: 12 layers max.
- b) Maximum number of product contained: 20 units per 1 carton.
- c) Carton size : $442 \text{mm}(\overline{\text{W}}) \times 390 \text{mm}(\text{H}) \times 170 \text{mm}(\text{D})$
- d) Total mass (for 20 units): Approximately 5.5 kg
- e) Carton stock environment:

① Temperature

0°C to 40°C

2 Humidity

60%RH or less

3 Atmosphere

Harmful gases such as acid and alkali which corrode electric

components and wires must not be detected.

4 Storage period

Approximately 3 months

⑤ Opening of package

To prevent TFT-LCD module from being damaged by electrostatic,

adjust the humidity to 50%RH or higher and provide an appropriate measure for electrostatic earthing before

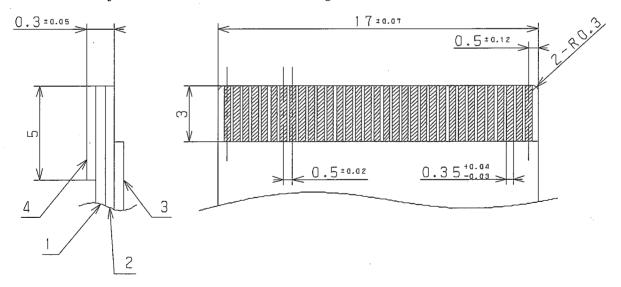
opening the package.

12. Others

- a) Adjusting volumes have been set optimally before shipment. Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described here may not be satisfied
- b) Disassembled the module, it may cause malfunction.
- c) Be careful since image retention may occur when a fixed pattern is displayed for a long time.
- d) Input/Output connector for TFT-LCD panel driving part

Used connector: FH12-33S-0.5SH(55) (HIROSE ELECTRIC CO., LTD.)

- 1) Fit FPC: Refer to the following figure
- Keeping power of terminal: 0.9N/pin or more
 Every terminal is pulled out 25±3mm/minute
- 3) Endurance of insert/pull out: Less than double of the initial resistance value [The electrical resistance value of the contact changed by the repeated insertion/pulling out by 20 times to and from the matching FPC]



No.	Parameter	Material
1	Base material	Polyimide or the equivalent [25um thickness]
2	Copper leaf	Copper leaf [35um thickness] gold leaf[2um or more thickness]
3	Coverlay	Polyimide or the equivalent
4	Reinforced sheet	Polyester, polyimide or the equivalent [188um thickness]

Fit FPC for Input/Output connector: 0.5mm pitch

13. Reliability Test Conditions for TFT LCD Module

Table 13-1

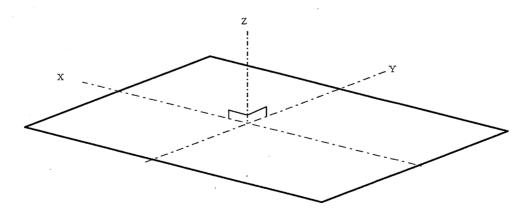
No.	Test items	Test conditions
1	High temperature storage test	Leaves the module at Ta = +80°C for 240h [Note 1]
2	Low temperature storage test	Leaves the module at Ta=-30°C for 240h [Note 1]
3	High temperature and high humidity operating test	Operates the module at Tp=+40°C, 95%RH for 240h (No condensation) [Note 1]
4	High temperature operating test	Operates the module at Tp=+70°C for 240h [Note 1]
5	Low temperature operating test	Operates the module at Ta=-10°C for 240h [Note 1] (The lifetime of a CCFT is not included)
6	Strength against ESD	$\pm 200 \mathrm{V} \cdot 200 \mathrm{pF}(0\Omega)$ 1 time for each terminals
7	Shock test (non-operating)	490m/s2 · 6ms, $\pm X$; $\pm Y$; $\pm Z$ 3 times for each direction (JIS C0041, A-7 Condition C)
8	Vibration test (non-operating)	Frequency: 5~57Hz, Stroke: 0.15mm Frequency: 58~500Hz, Acceleration: 9.8m/s² Sweep cycle: 11 minutes X,Y,Z 1 hours for each directions (total 3 hours) [Note 2] (JIS D1601)
9	Thermal shock test	Ta= -30 °C~+80°C , 200 cycles (0.5h) (0.5h)

[Result evaluation criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

[Note 1] Ta=Ambient temperature, Tp= Panel surface temperature

[Note 2] X,Y,Z directions are shown as follows:



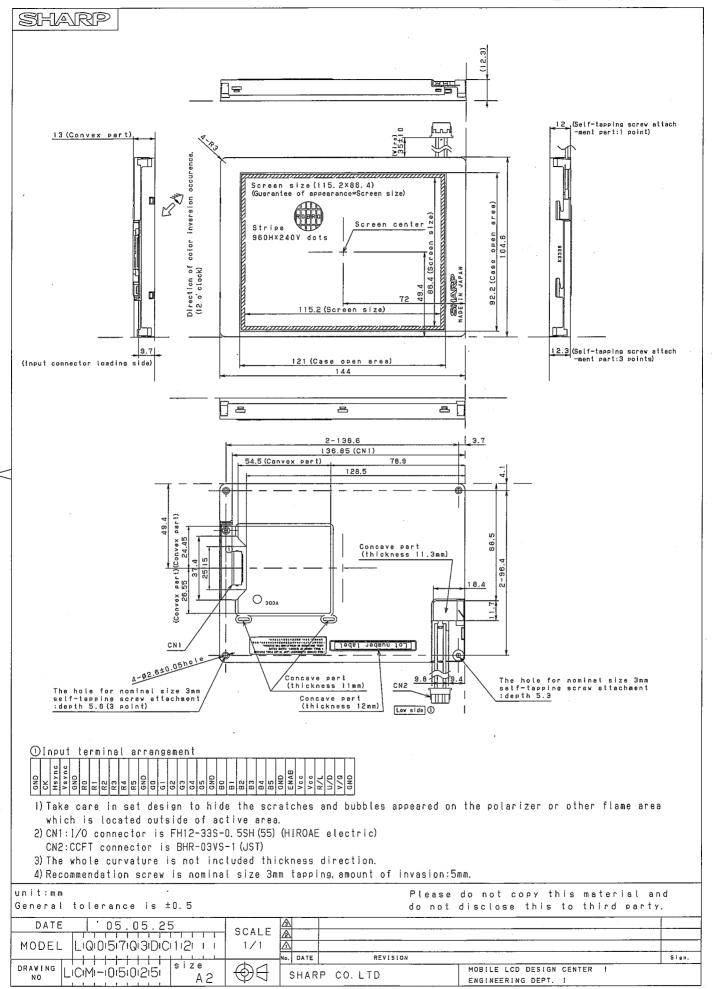


Fig. 1 DIMENSIONS OF THE MODULE

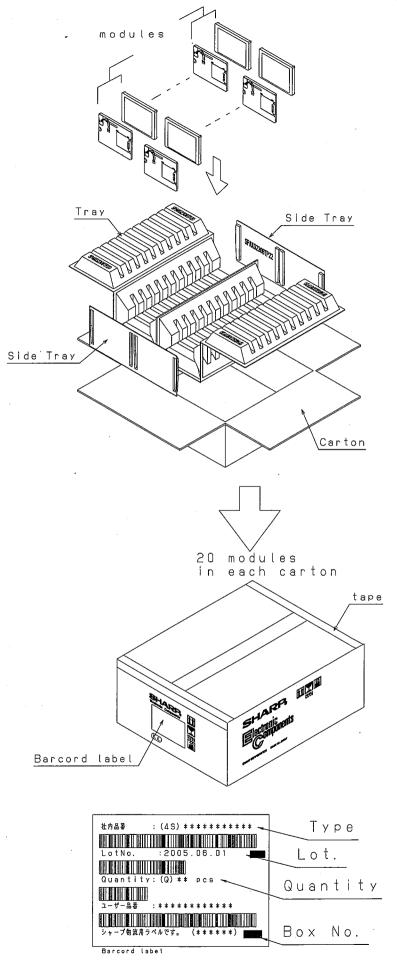


Fig. 2-PACKING FORM