

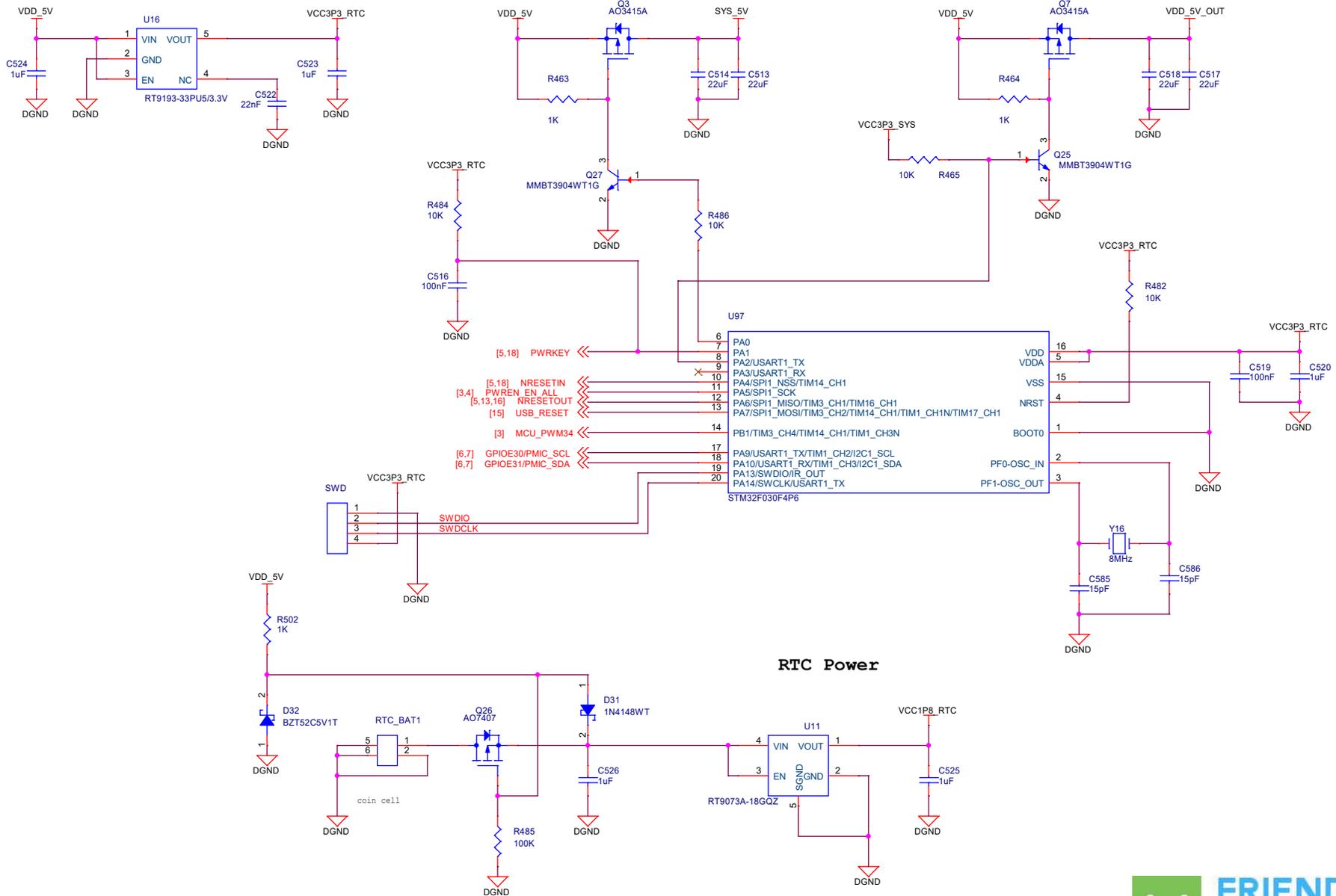
NanoPC T2/T3

2020/01/13:
R463 -> 1K, R486 -> 270K, R465 -> 270K, R464 -> 1K, R512 -> NC, C541 -> 10K
for T2(S5P4418), R509 -> 100K/1K

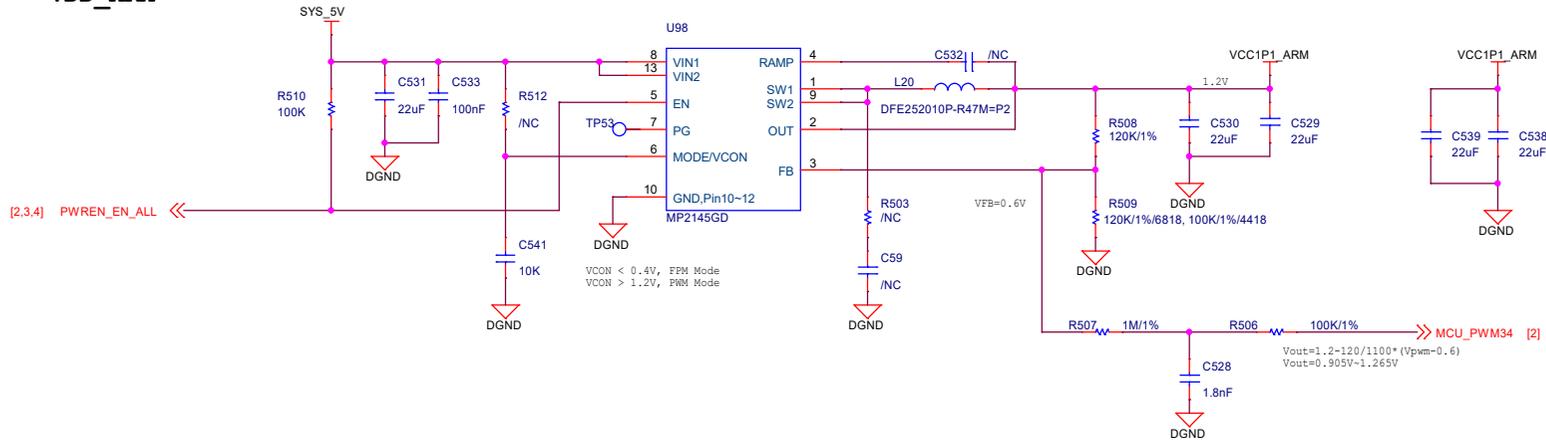
2022/12/07:
R486 ->10K, R465 ->10K



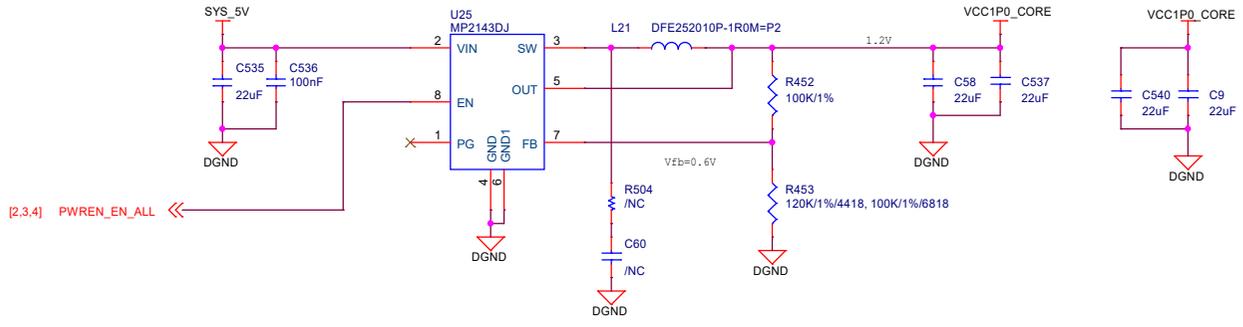
NanoPC-T2/T3		
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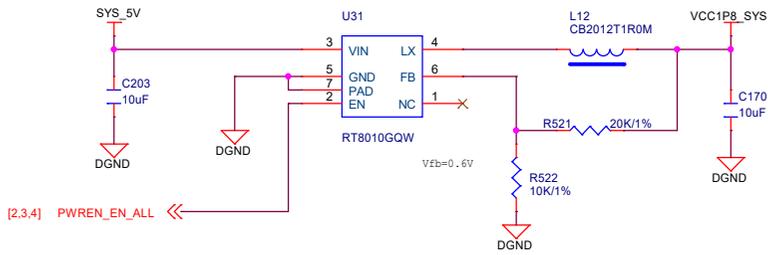
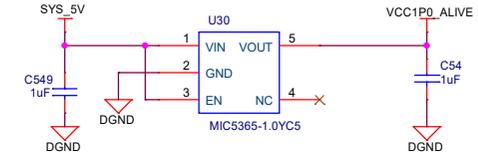
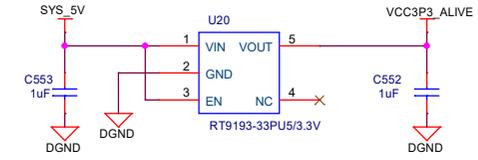
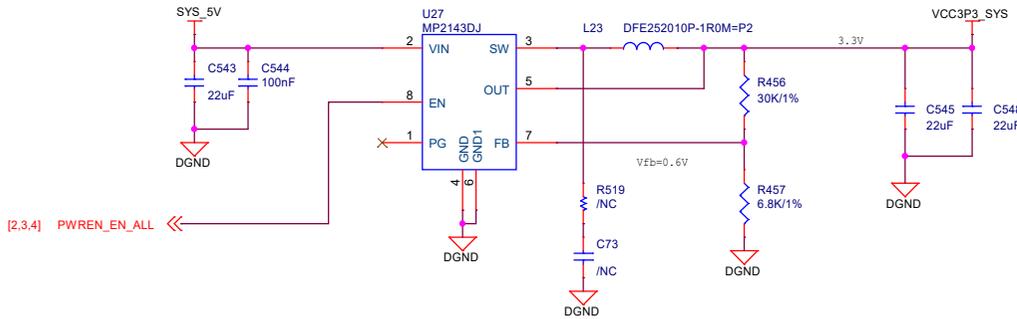
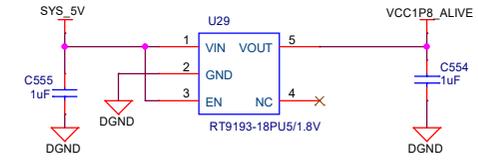
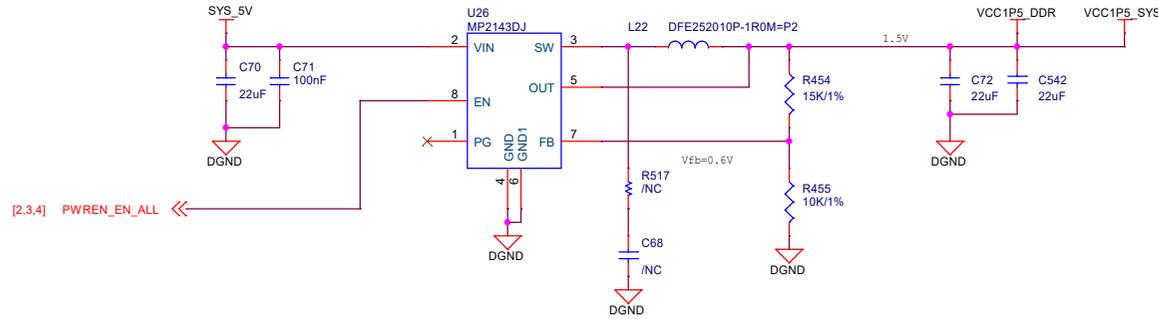


VDD_ARM

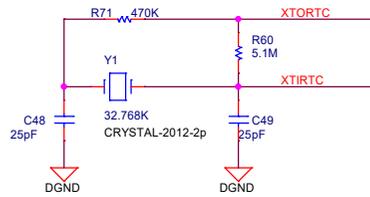
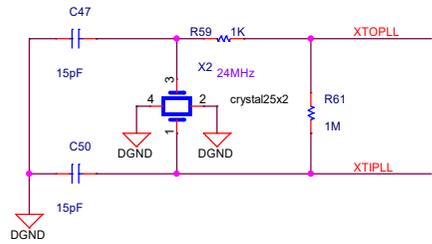
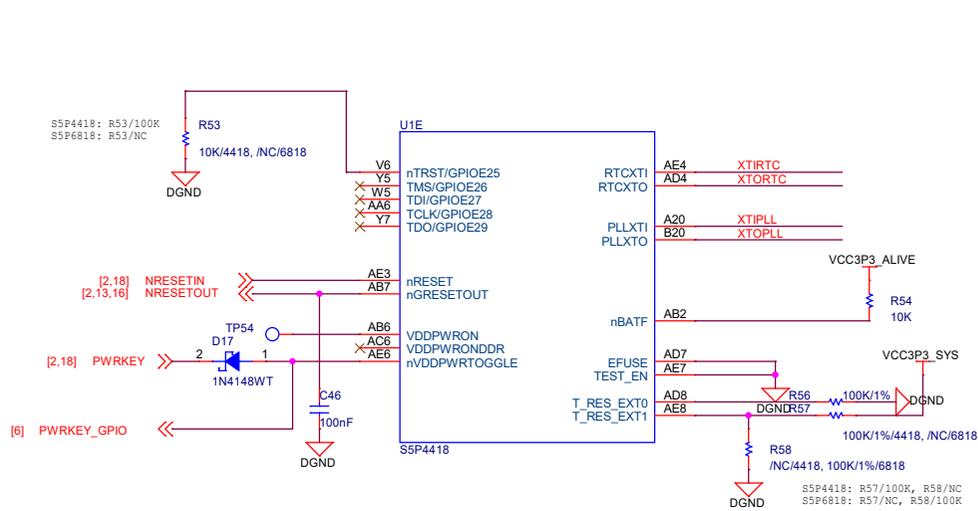


VDD_CORE

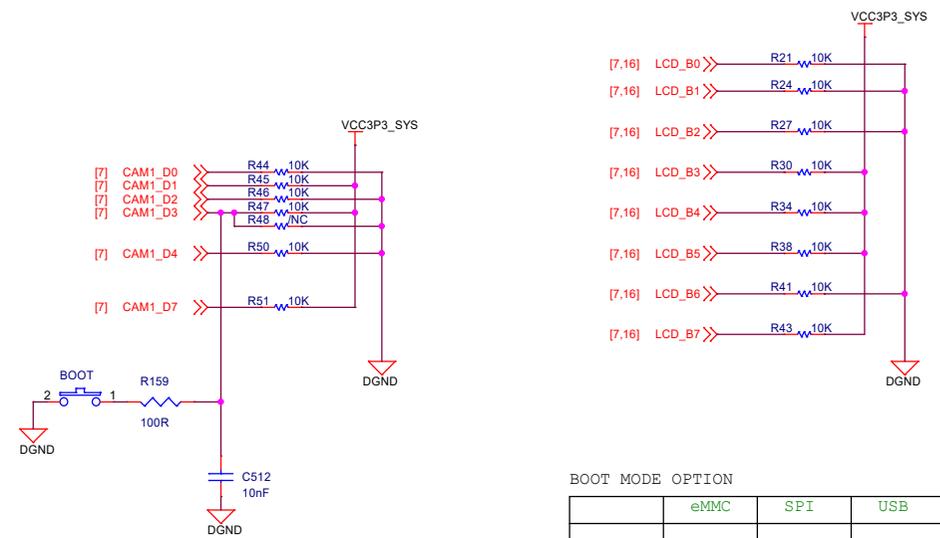




System Reset, Clocks



Boot Mode Config



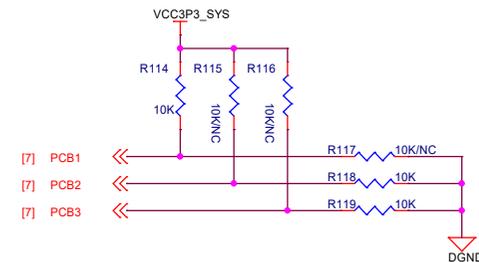
BOOT MODE OPTION

	eMMC	SPI	USB	NAND
SD0	HIGH	LOW	LOW	HIGH
SD1	LOW	LOW	HIGH	HIGH
SD2	HIGH	HIGH	HIGH	HIGH
SD4	LOW	HIGH		
SD5	LOW	LOW		

Boot media port select (SPI, eMMC)

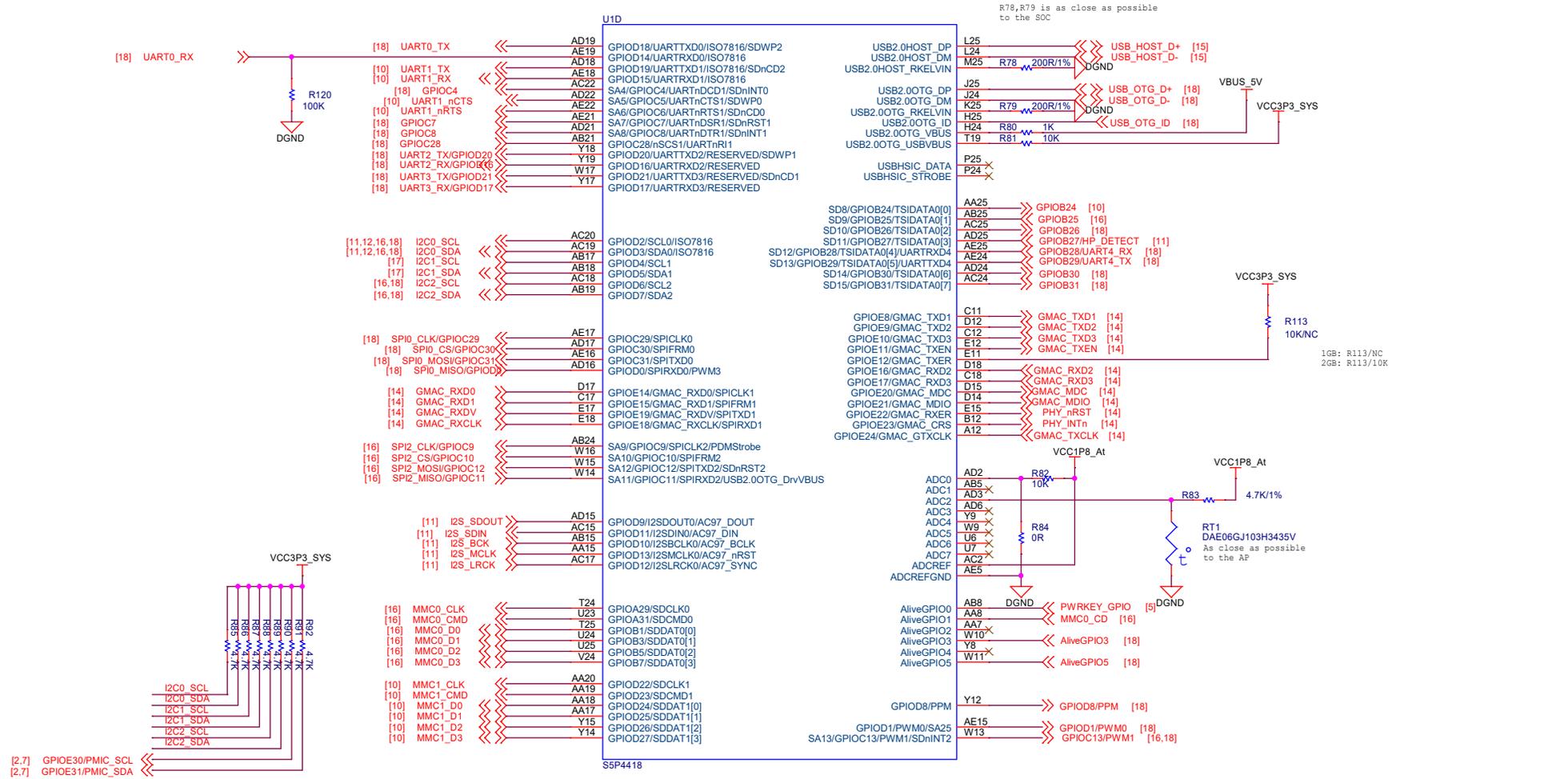
	CH0	CH1	CH2
SD3	LOW	HIGH	LOW
CAM1_D3	LOW	LOW	HIGH

PCB Version



NanoPC-T2/T3

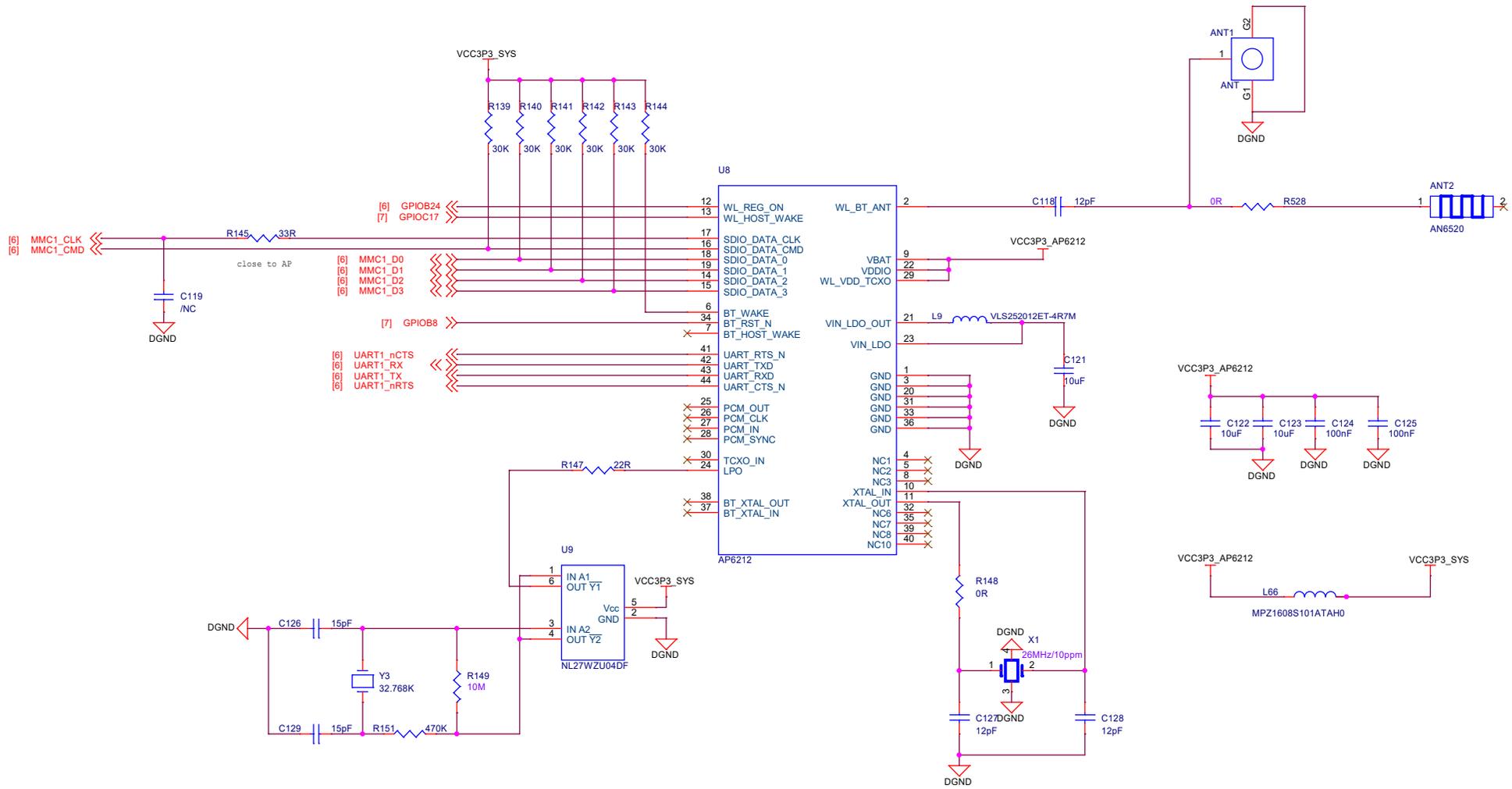
AP Peripherals



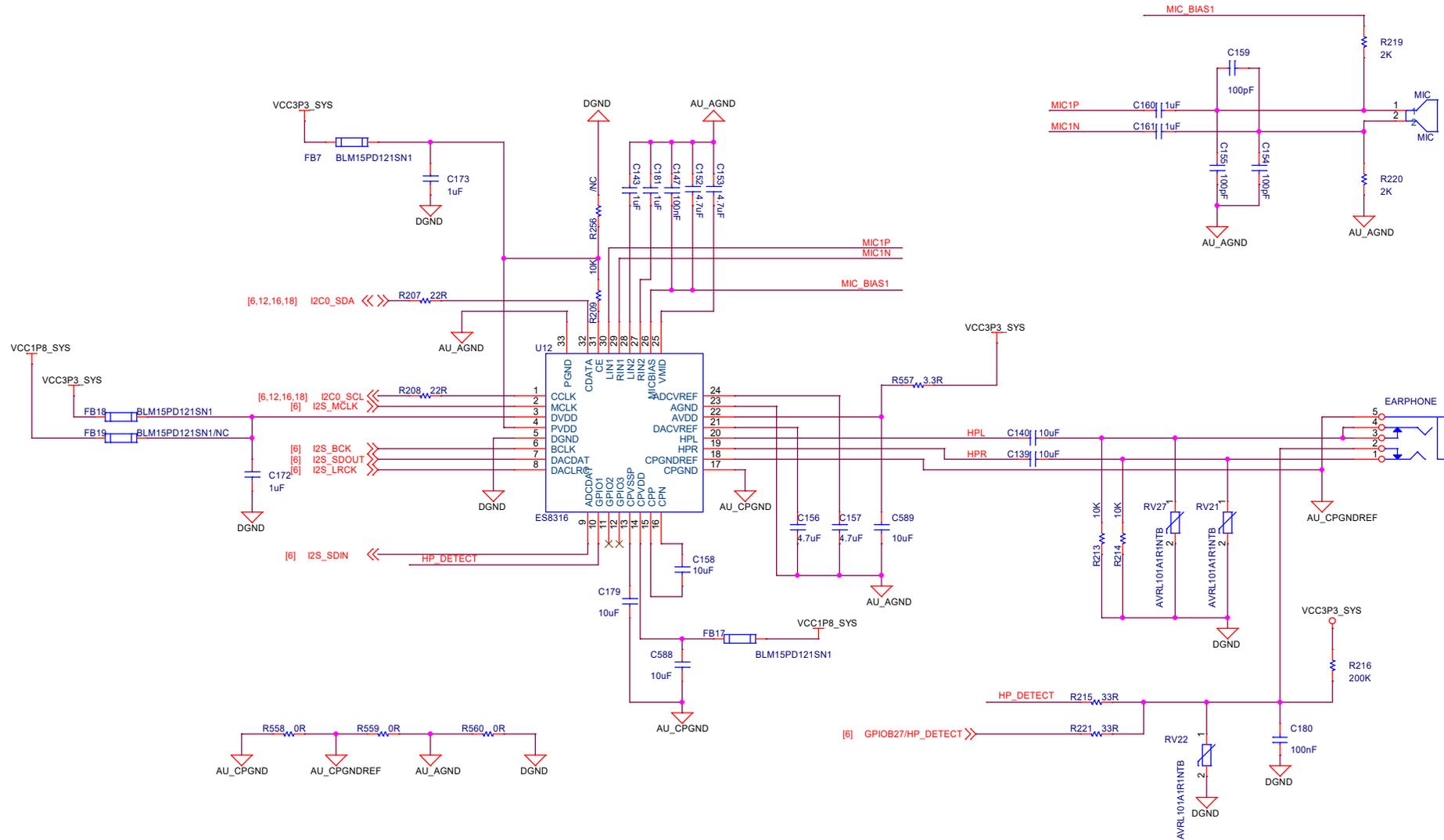
I2C CH0 : Camera
 I2C CH1 : HDMI EDID
 I2C CH2 : Touch
 PMIC_I2C : PMIC



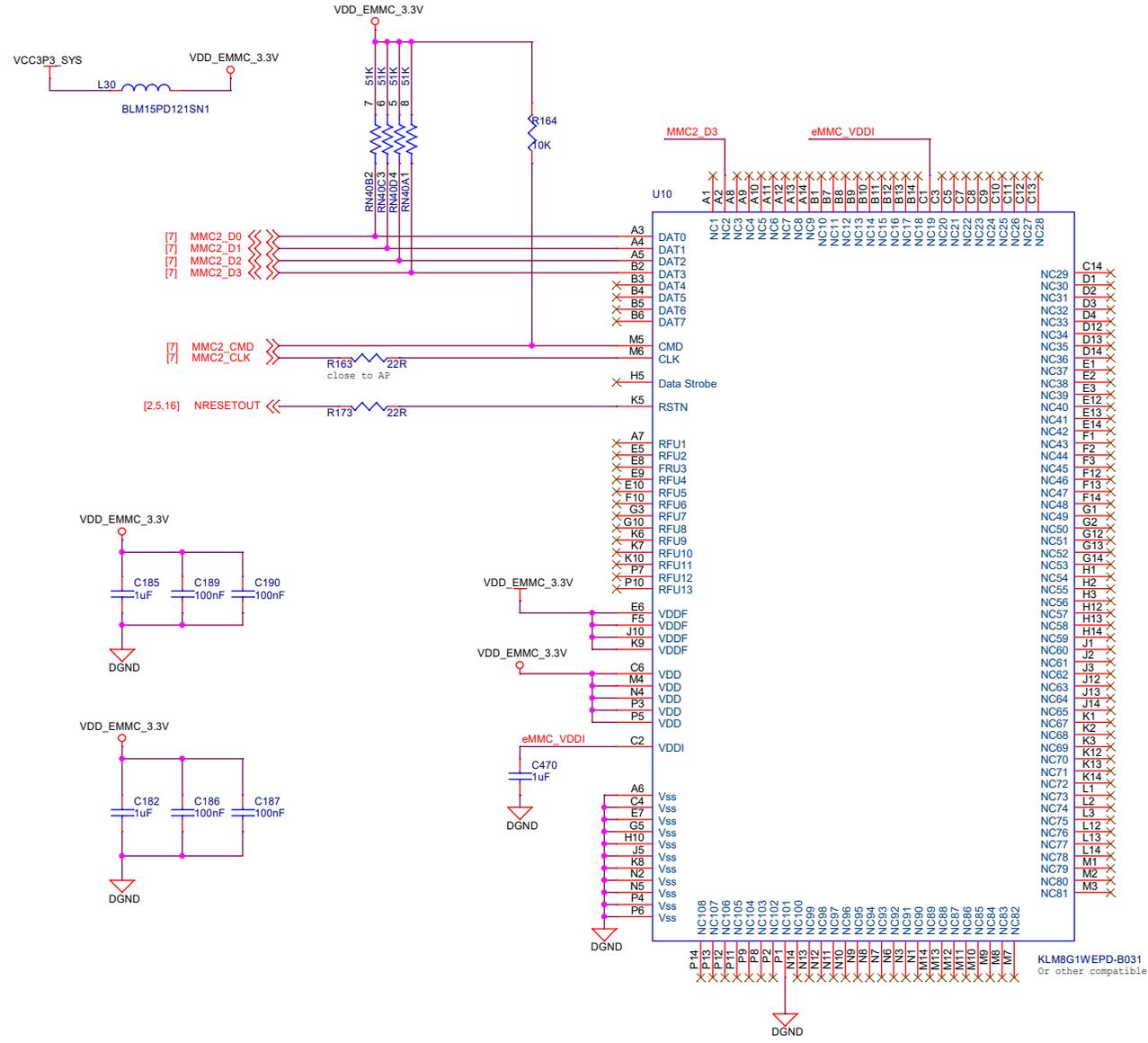
Wi-Fi/Bluetooth



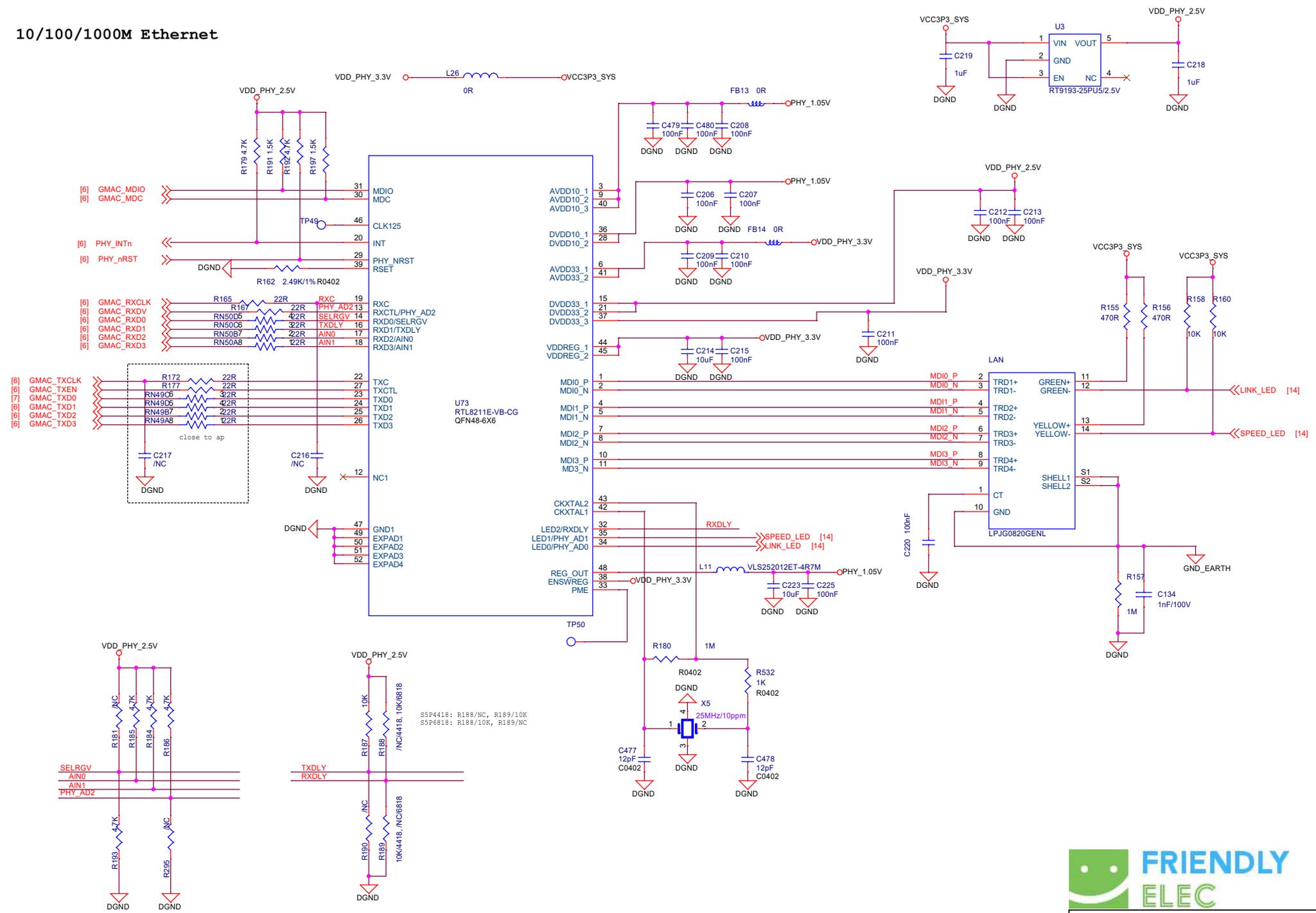
Audio



eMMC



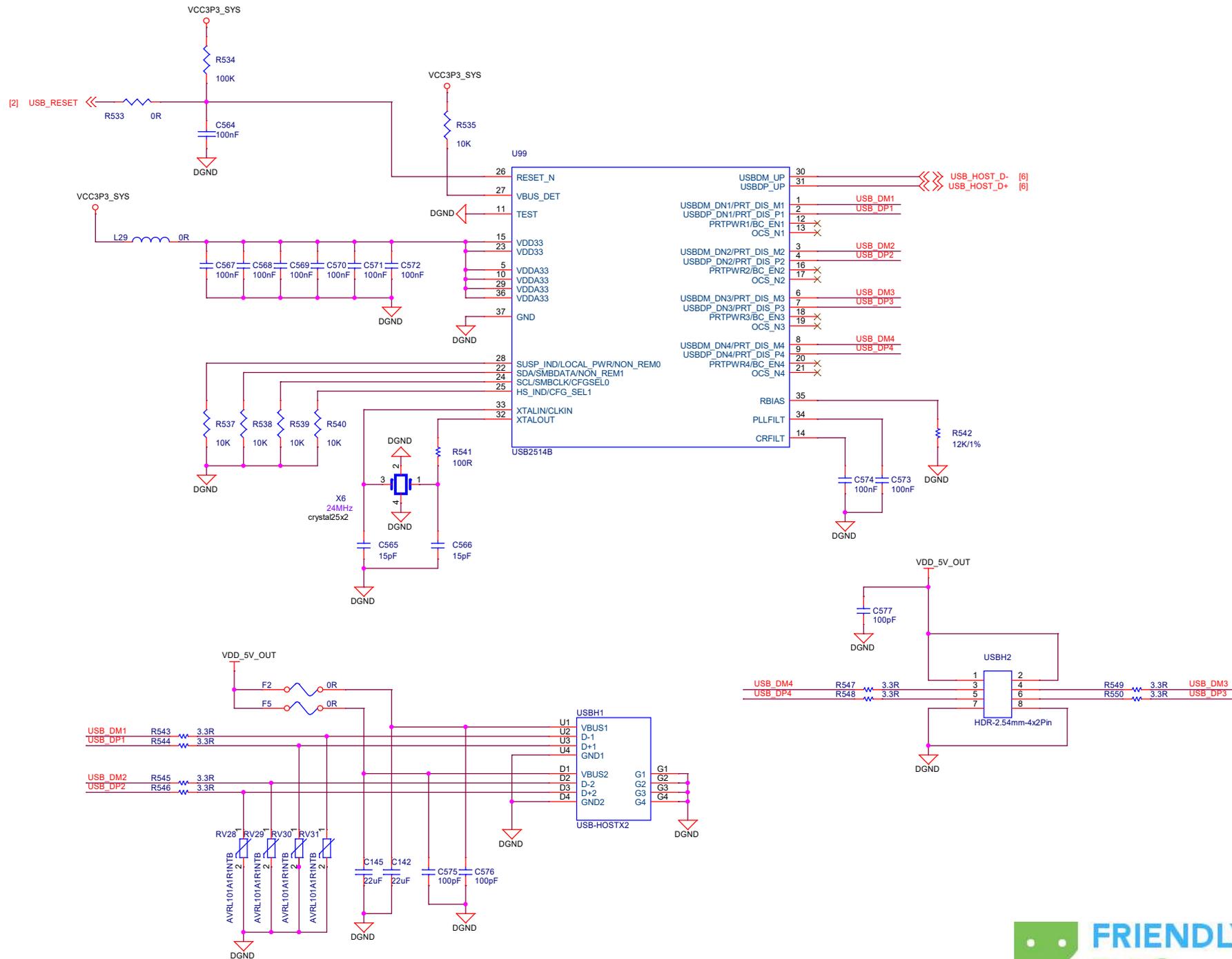
10/100/1000M Ethernet



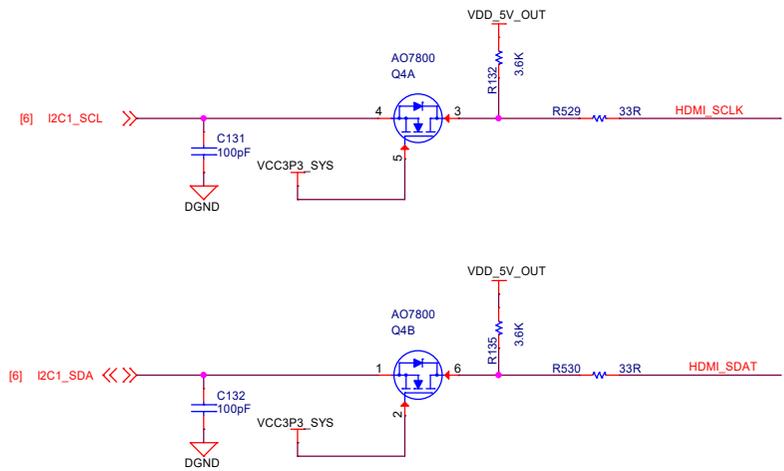
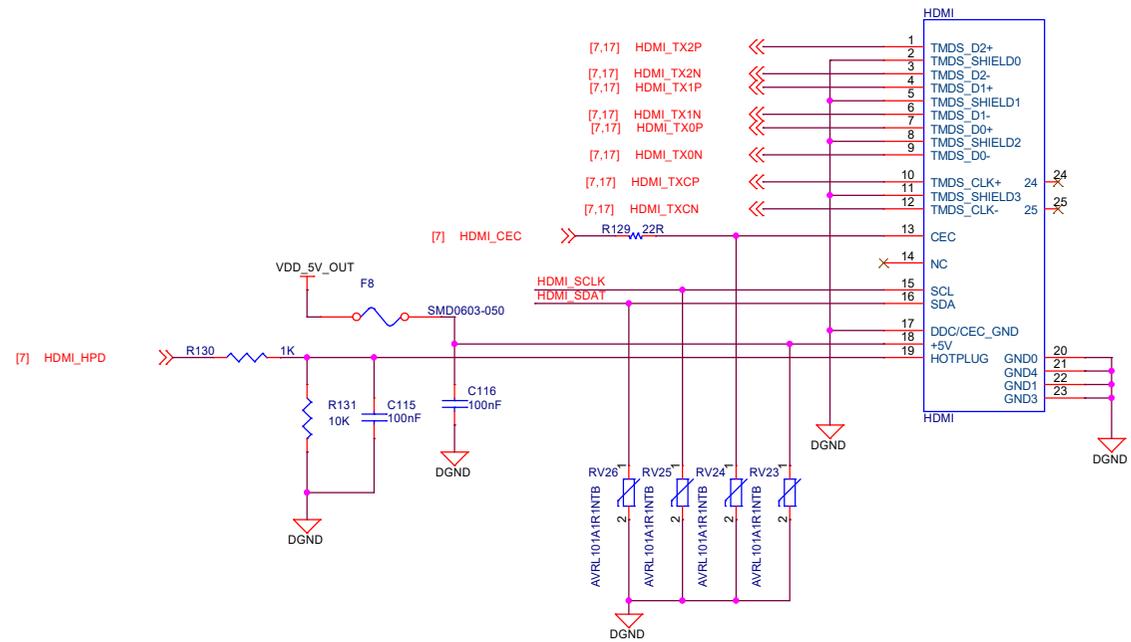
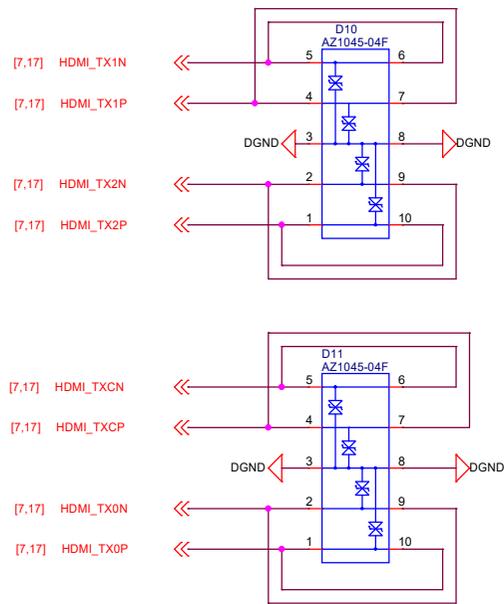
S5P4418: R188/NC, R189/10K
 S5P6818: R188/10K, R189/NC



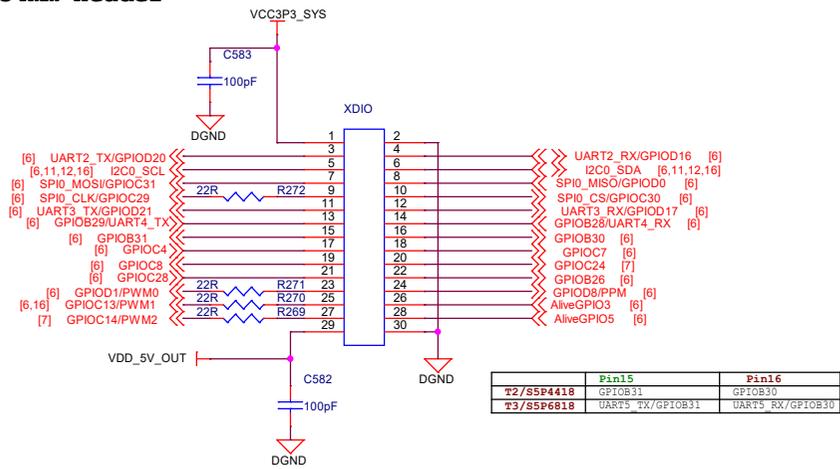
NanoPC-T2/T3		
Size A3	Document Number 14.LAN	Rev 1711
Date: Wednesday, January 10, 2018	Sheet 14	of 18



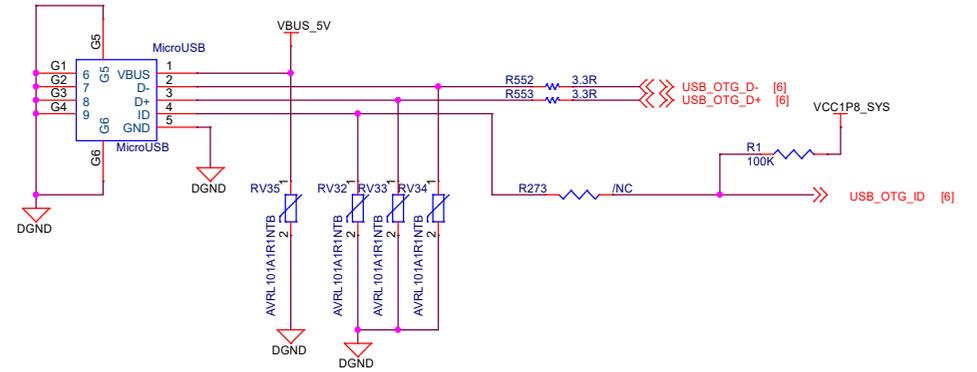
HDMI



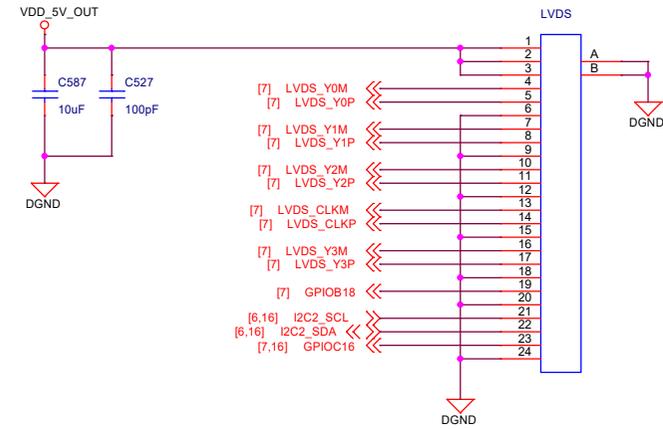
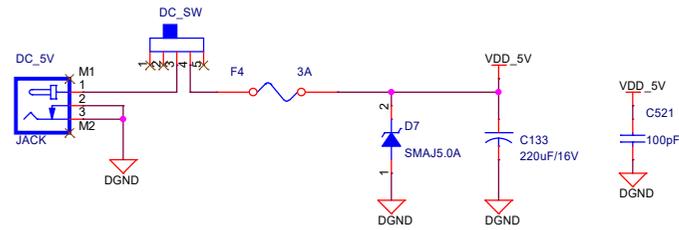
2.54mm Header



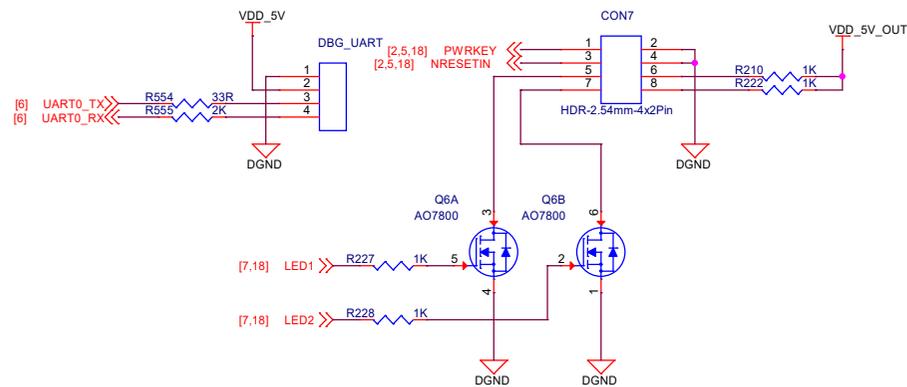
MicroUSB



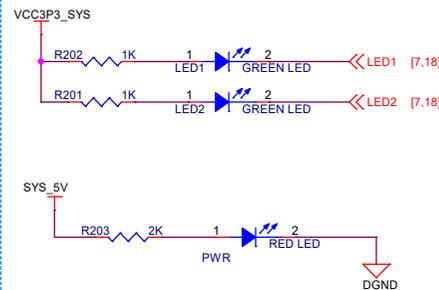
5V Power IN



Debug UART



LEDs



Buttons

