

Surface Mount Glass Passivated Rectifier


DO-214AA (SMB)

| PRIMARY CHARACTERISTICS | |
|-------------------------|----------------|
| $I_{F(AV)}$ | 1.5 A |
| V_{RRM} | 50 V to 1000 V |
| I_{FSM} | 50 A |
| I_R | 1.0 μ A |
| V_F | 1.15 V |
| T_J max. | 150 °C |

FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Low forward voltage drop
- Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC


RoHS
COMPLIANT

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer, automotive and telecommunication.

MECHANICAL DATA

Case: DO-214AA (SMB)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test, HE3 suffix for high reliability grade (AEC Q101 qualified), meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

| MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise noted) | | | | | | | | | |
|--|----------------|---------------|-----|-----|-----|-----|-----|------|------|
| PARAMETER | SYMBOL | S2A | S2B | S2D | S2G | S2J | S2K | S2M | UNIT |
| Device marking code | | SA | SB | SD | SG | SJ | SK | SM | |
| Maximum repetitive peak reverse voltage | V_{RRM} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum RMS voltage | V_{RMS} | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V |
| Maximum DC blocking voltage | V_{DC} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum average forward rectified current at $T_L = 100$ °C | $I_{F(AV)}$ | 1.5 | | | | | | | A |
| Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load | I_{FSM} | 50 | | | | | | | A |
| Operating and storage temperature range | T_J, T_{STG} | - 55 to + 150 | | | | | | | °C |



| ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted) | | | | | | | | | | |
|--|---|----------|-----|-----|-----|------------|-----|-----|-----|---------------|
| PARAMETER | TEST CONDITIONS | SYMBOL | S2A | S2B | S2D | S2G | S2J | S2K | S2M | UNIT |
| Maximum instantaneous forward voltage | 1.5 A | V_F | | | | 1.15 | | | | V |
| Maximum DC reverse current at rated DC blocking voltage | $T_A = 25\text{ }^\circ\text{C}$ $T_A = 125\text{ }^\circ\text{C}$ | I_R | | | | 1.0 125 | | | | μA |
| Typical reverse recovery time | $I_F = 0.5\text{ A}$, $I_R = 1.0\text{ A}$, $I_{rr} = 0.25\text{ A}$ | t_{rr} | | | | 2.0 | | | | μs |
| Typical junction capacitance | 4.0 V, 1 MHz | C_J | | | | 16 | | | | pF |

| THERMAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted) | | | | | | | | | | |
|---|------------------------------------|-----|-----|-----|----------|-----|-----|-----|--------------------|--|
| PARAMETER | SYMBOL | S2A | S2B | S2D | S2G | S2J | S2K | S2M | UNIT | |
| Typical thermal resistance ⁽¹⁾ | $R_{\theta JA}$ $R_{\theta JL}$ | | | | 53 16 | | | | $^\circ\text{C/W}$ | |

Note:

(1) Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.3 x 0.3" (8.0 x 8.0 mm) copper pad areas

| ORDERING INFORMATION (Example) | | | | |
|---------------------------------------|-----------------|-----------------------|---------------|------------------------------------|
| PREFERRED P/N | UNIT WEIGHT (g) | REFERRED PACKAGE CODE | BASE QUANTITY | DELIVERY MODE |
| S2J-E3/52T | 0.096 | 52T | 750 | 7" diameter plastic tape and reel |
| S2J-E3/5BT | 0.096 | 5BT | 3200 | 13" diameter plastic tape and reel |
| S2JHE3/52T ⁽¹⁾ | 0.096 | 52T | 750 | 7" diameter plastic tape and reel |
| S2JHE3/5BT ⁽¹⁾ | 0.096 | 5BT | 3200 | 13" diameter plastic tape and reel |

Note:

(1) Automotive grade AEC Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

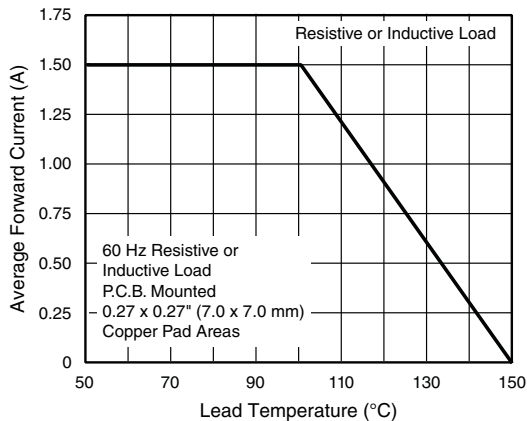


Figure 1. Forward Current Derating Curve

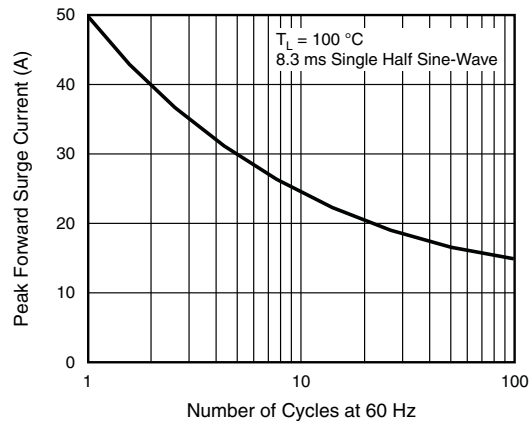


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

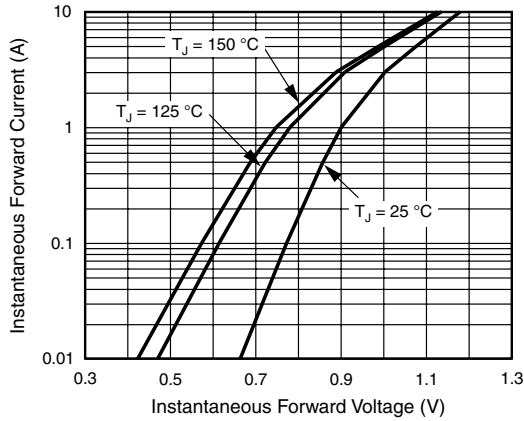


Figure 3. Typical Instantaneous Forward Characteristics

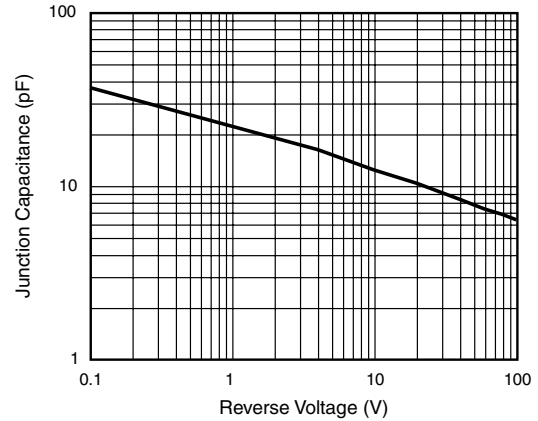


Figure 5. Typical Junction Capacitance

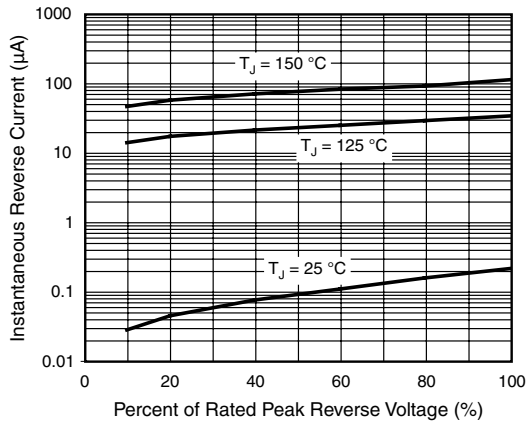


Figure 4. Typical Reverse Characteristics

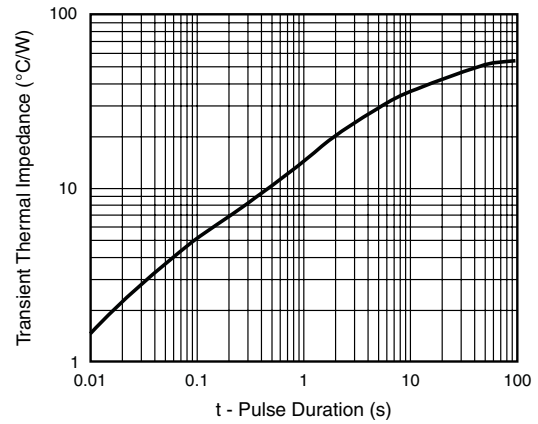
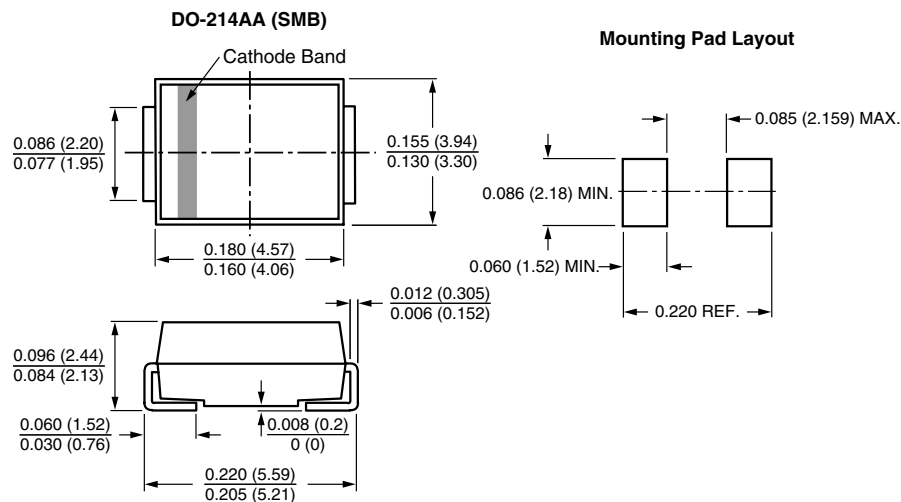


Figure 6. Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.