

Advance Information

**CONTROL IC FOR LINE-ISOLATED FREE
 RUNNING FLYBACK CONVERTER**

The bipolar integrated circuit TDA4601 drives, regulates and monitors the switching transistor in a power supply based on the ringing choke flyback principle.

Due to the wide regulating range and the high voltage stability during large load changes, SMPS for Hi-Fi equipment and active loudspeakers can be realized as well as applications in TV receivers and video recorders.

The TDA4601 is available in a 9-pin plastic medium power SIP package. The operating temperature range is -15°C to $+85^{\circ}\text{C}$.

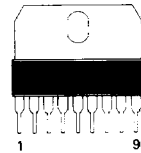
- Wide Operational Range
- High Voltage Stability Even at High Load Changes
- Direct Control of Switching Transistor
- Low Start-Up Current
- Linear Foldback of the Overload Characteristic
- Base Drive Proportional to the Current Through the Power Switching Transistor
- Standby Mode 3.5 W into the External Load
- Inhibit Capability (TTL Compatible)
- Undervoltage Lockout

For Application Details See ANE002

TDA4601

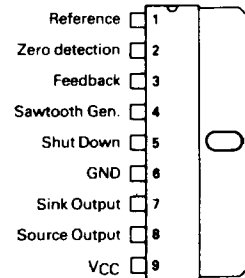
**FLYBACK CONVERTER
 CONTROL CIRCUIT**

**SILICON MONOLITHIC
 INTEGRATED CIRCUIT**

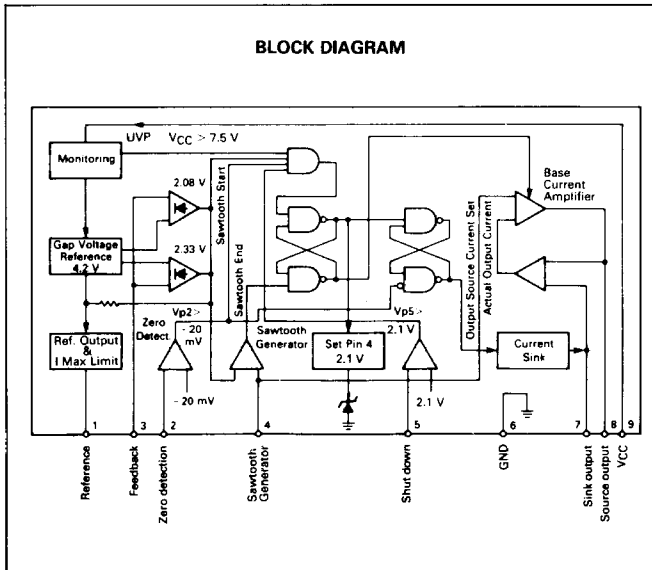


PLASTIC
 MEDIUM POWER
 PACKAGE
 CASE 762

PIN ASSIGNMENTS



BLOCK DIAGRAM



ORDERING INFORMATION

Device	Temperature Range	Package
TDA4601	-15°C to $+85^{\circ}\text{C}$	Plastic SIP

This document contains information on a new product. Specifications and information herein are subject to change without notice.

TDA4601

3

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	V_g	20	V
Sink Output Voltage	V_7 V_7-V_8	0 to V_g ± 6.0	V
Reference Output	I_1	-10 to +1.0	mA
Zero Crossing	I_2	-3.0 to +3.0	mA
Control Amplifier	I_3	-3.0 to 0	mA
Collector Current	I_4	-2.0 to +5.0	mA
Trigger Input	I_5	-2.0 to +3.0	mA
Sink Output	I_7	-1.5	A
Junction Temperature	T_J	+150	°C
Storage Temperature	T_{stg}	-40 to +125	°C
Thermal Resistance (Junction to Air)	θ_{JA}	70	°C/W
Thermal Resistance (Junction to Case)	θ_{JC}	15	°C/W

ELECTRICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$ unless otherwise stated)

Range of Operation	Symbol	Fig. No.	Min	Typ	Max	Unit
Supply Voltage	V_g		—	15	18	V
Ambient Temperature	T_A		-15	—	85	°C

START OPERATION $T_A = 25^\circ\text{C}$

Current Consumption (V_1 Not Yet Switched) $V_g = 3.0\text{ V}$ $V_g = 5.0\text{ V}$ $V_g = 10\text{ V}$	I_g	1	— — —	— 1.5 2.0	0.5 2.0 3.2	mA
Turn-On Point for V_1	V_g	1	11.3	11.8	12.3	V
V_4 Before Start-Up ($V_g < 11.8\text{ V}$)	V_4	1	6.0	6.7	—	V

REGULATION MODE $V_g = 15\text{ V}$ $T_A = 25^\circ\text{C}$

Current Consumption $V_{reg} = -10\text{ V}$ $V_{reg} = 0$	I_g	1	110 55	135 85	160 110	mA	
Reference Voltage $I_1 < 0.1\text{ mA}$ $I_1 = 5.0\text{ mA}$	V_1	1	4.0 4.0	4.2 4.2	4.5 4.4	V	
Reference Voltage Temperature Coefficient	TC_1	1	—	100	—	ppm/°C	
$V_{Pin 4}$ Low Static Voltage	V_4	1	1.8	2.08	2.5	V	
$V_{Pin 4}$ Regulation Peak Voltage $I_{pin 3} = 5.0\ \mu\text{A}$ $I_{pin 3} = 1.3\text{ mA}$	$V_4\text{ peak}$	1	4.0 —	4.2 2.4	4.5 3.0	V	
$V_{Pin 3}$ Full Fold Back $I_{pin 3} = 1.3\text{ mA}$ Fold Back $I_{pin 3} = 0.5\text{ mA}$ Overload Decision $I_{pin 3} = 1.0\ \mu\text{A}$ $V_{Pin 3}$ Regulation $I_{pin 3}$ Regulation $I_{pin 3}$ Leakage at $V_{Pin 3} = 1.5\text{ V}$	V_3	1	— — — —	3.7 2.5 2.4 2.11	4.0 3.0 2.9 —	V	
	I_3	1	— —	1.0 0.4	— —	μA	
$V_{Pin 7}$ Peak High $V_R = 0\text{ V}$ (Full Fold Back) $V_R = -10\text{ V}$ (Regulation) $V_R = -15\text{ V}$ (Standby)	$V_7\text{ peak}$	1	— — —	3.5 4.0 5.0	— — —	V	
	$V_{Pin 7}$ Peak Low $V_R = 0\text{ V}$ $V_R = -10\text{ V}$ $V_R = -15\text{ V}$	$V_7\text{ peak}$	1	— — —	1.4 1.45 1.57	— — —	V

TDA4601

ELECTRICAL CHARACTERISTICS (continued) ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Range of Operation	Symbol	Fig. No.	Min	Typ	Max	Unit
REGULATION MODE (continued) $V_g = 15\text{ V}$ $T_A = 25^\circ\text{C}$						
$I_{\text{Pin 7 Sink Peak}}$ $V_R = -15\text{ V}$	$I_{7\text{ peak}}$	1	—	+0.7	—	A
$I_{\text{Pin 8 Source Peak}}$ $V_R = -15\text{ V}$	$I_{8\text{ peak}}$	1	—	-0.8	—	A
$V_{\text{Pin 2}}$ $I_{\text{Pin 2}} = -3.0\text{ mA}$ $= -0.3\text{ mA}$ $+3.0\text{ mA}$ $+0.3\text{ mA}$	V_2	1	—	-0.3 -0.2 +0.7 +0.8	—	V
PROTECTIVE OPERATION $V_g = 15\text{ V}$ $T_A = 25^\circ\text{C}$						
Current Consumption ($V_5 < 1.8\text{ V}$)	I_g	1	14	20	26	mA
Turn-Off Voltage ($V_5 < 1.8\text{ V}$)	V_7 V_4	1 1	1.3 1.8	1.5 2.1	1.8 2.5	V
External Trigger Input Enable Voltage ($V_{\text{reg}} = 0\text{ V}$) Disabled Voltage ($V_{\text{reg}} = 0\text{ V}$)	V_5	1	— 2.0	2.2 2.2	2.4 —	V
Supply Voltage Disabling V_8 and V_1	V_9	1	6.7	7.4	7.8	V
$V_{\text{Pin 5 Zener Voltage (Pin 5 Open)}}$	V_5	1	6.5	7.3	7.8	V
$I_{\text{Pin 5}}$ $V_{\text{Pin 5}} = 3.0\text{ V}$ $V_{\text{Pin 5}} = 0\text{ V}$	I_5	1	— —	1.4 -11	— —	μA
Turn-On Time (Secondary Voltages)	t_{on}	2	—	350	450	ms
Voltage Change When $S_3 = \text{Closed}$ ($\Delta P_3 = 19\text{ W}$) When $S_2 = \text{Closed}$ ($\Delta P_2 = 15\text{ W}$)	ΔV_2	2	— —	100 500	500 1000	mV
Standby Operation (Minimum Secondary Power: 3.0 Watts) When $S_1 = \text{Open}$	ΔV_2	2	—	20	30	V
Switching Frequency During Standby Mode	f	2	70	75	—	kHz
Primary Power Consumption During Standby Mode The heatsink must be optimized, taking the maximum data (T_J , θ_{JC} , T_A) into consideration	P_{prim}	2	—	10	15	VA

CIRCUIT DESCRIPTION

The TDA4601 regulates, controls and protects the switching transistor in flyback converter power supplies at starting-up, normal, and overload operation.

A. Start-Up Sequence

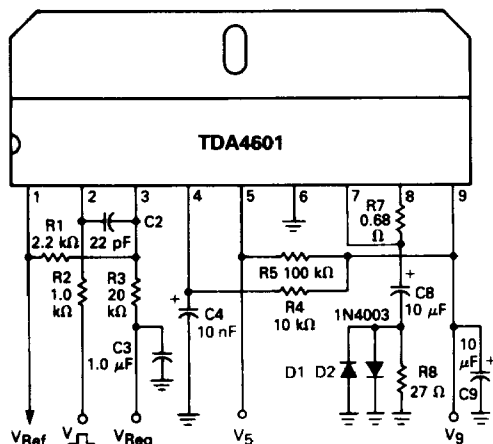
During start-up there are three consecutive operations:

1. An internal reference voltage is created. It supplies the voltage regulator and enables the supply to the coupling electrolytic capacitor and the switching transistor. For a supply voltage (V_g) of 12 V, the current is less than 3.2 mA.

2. Activation of the internal reference voltage $V_1 = 4.0\text{ V}$. This voltage is suddenly available when V_g reaches 12 V and enables all parts of the IC to be supplied from the control logic including thermal and overload protection.
3. Activation of the control logic. As soon as the reference voltage is available, the control is switched on through an additional stabilization circuit.

This start-up sequence is necessary for smoothly driving the switching transistor through the coupling electrolytic capacitor.

FIGURE 1 — TEST CONFIGURATION



B. Normal Operation

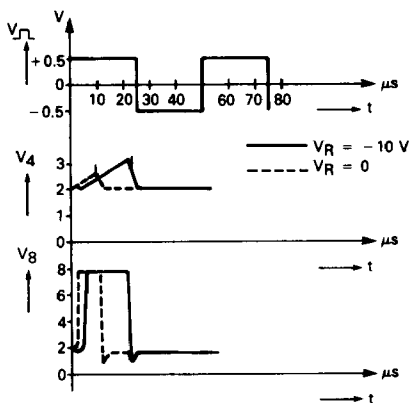
Zero crossing detection is sensed on Pin 2 and linked to the control logic.

The signal picked up on the feedback winding is applied, after filtering, to Pin 3 (used for input regulation and for overload protection). The regulating section works with an input voltage of about 2.0 V for normal regulation and a current of about 1.4 mA for foldback operation. Together with the collector current simulation Pin 4, the overload recognition defines the operating region of the regulating amplifier depending on the internal reference voltage. The simulation of the collector current is generated by an external RC network at Pin 4 and an internally set voltage level.

For a constant line voltage and for a given output power on the load (t on fixed) less than the maximum output power, a decrease of C4 produces an increase of the current sent to the base of the power switching transistor. So the foldback point is reached earlier. The regulation range starts from a 2.0 Vdc level which is the bottom of a sawtooth waveform; the maximum is limited at 4.0 V (reference voltage).

A secondary load of 19 W produces a switching frequency of about 50 kHz at an almost constant duty cycle (approximately 3). Furthermore, when the switchmode power supply delivers approximately 3.0 W, the switching frequency jumps to about 70 kHz at a duty cycle of approximately 11. At the same time, the collector peak current falls below 1.0 A.

FIGURE 2 — TEST DIAGRAM: NORMAL OPERATION



The comparison of the output level of the regulating amplifier, the overload detection and the collector current simulation drives the control logic. An additional steering control and blocking possibility is offered thru Pin 5. When the voltage applied on Pin 5 falls below 2.2 V then the source output (Pin 8) is blocked.

The control logic is set according to the start-up circuit, the zero crossing detection and the trigger enabling. This logic drives the base current amplifier and the base current shutdown. The base current amplifier drives the source output (Pin 8) proportionally to the sawtooth voltage (Pin 4). A current feedback is performed by an external shunt inserted between Pin 8 and the base of the switching power transistor. This resistor determines the maximum amplitude of the base current drive.

C. Protective Features

The base current shut-down, released by the control logic, clamps the sink output (Pin 7) at 1.6 V, turning off the switching transistor. This feature will be released if the voltage on Pin 9 is less than 7.4 V, or if the applied voltage on Pin 5 is less than 2.2 V. In case of a short circuit of the secondary windings, the TDA4601 continuously monitors the fault condition.

In standby operation the circuit is set to a high duty cycle. The total power consumption of the power supply is held below 6.0 to 10 W.

FIGURE 3 — FREQUENCY versus OUTPUT POWER

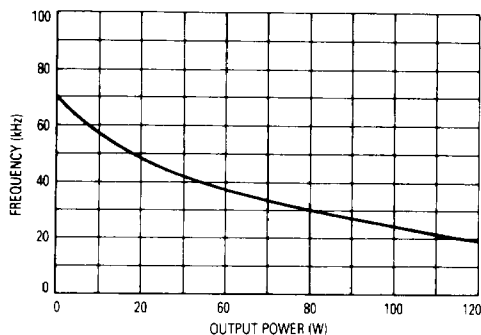
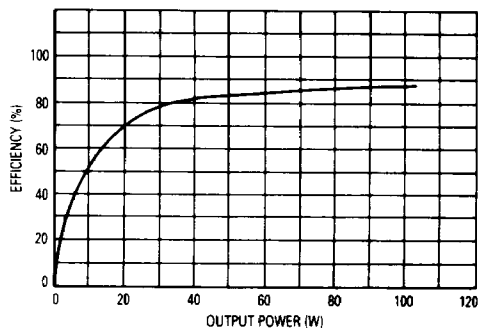
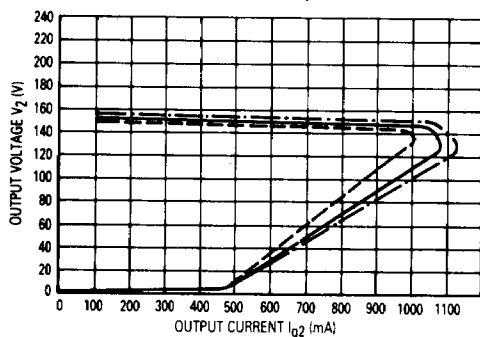
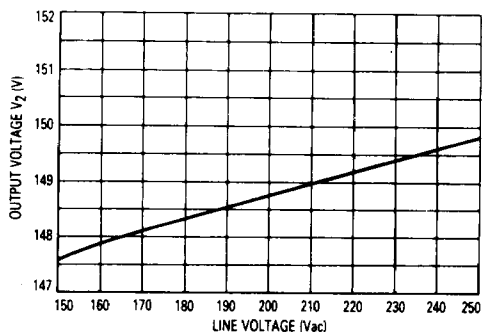


FIGURE 4 — EFFICIENCY versus OUTPUT POWER

FIGURE 5 — OUTPUT VOLTAGE (V_2) versus OUTPUT CURRENT (I_{q2})FIGURE 6 — OUTPUT VOLTAGE (V_2) versus LINE VOLTAGE

TEST CIRCUIT AND TYPICAL APPLICATION (See Figure 7)

This application circuit shown in Figure 2 represents a blocking converter for color TV sets with 30 W to 120 W of output power and line voltages from 160 to 270 V.

In spite of regulation on the primary side, good voltage stability of the various secondary voltages is achieved even with large load changes.

For line voltage isolation and transformation to the desired secondary voltages, a transformer with ferrite core is used.

SPECIAL FEATURES OF THE FLYBACK CONVERTER POWER SUPPLY USING THE TDA4601

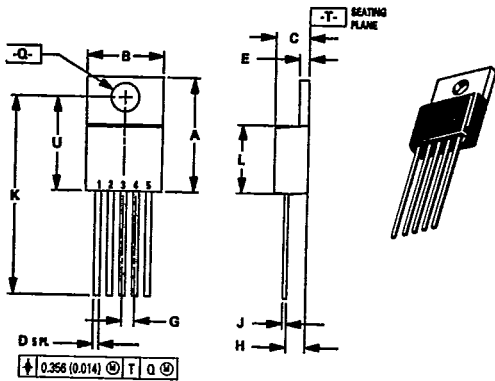
- Direct driving of the power switching transistor
- Low starting current, defined starting behavior also at slowly rising line voltage
- Short circuit proof and open-loop resistant circuit. In both cases a power of only 6.0 to 10 W is consumed. Linear foldback characteristic at overload.
- Automatic restart after elimination of the overload.
- Efficiency of more than 80% at an output power of 40 to 100 W.
- Frequency of oscillation between 20 kHz (100 W) and 70 kHz (without load).
- Simple RF1 suppression
- Good regulation of load current and line voltage variations. At a line voltage variation between 170 and 240 V the output voltage of 150 V will change approximately 2.0 V.

PACKAGE OUTLINE DIMENSIONS (continued)

**T SUFFIX
PLASTIC PACKAGE
CASE 314D-02**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	14.529	15.570	0.572	0.613
B	9.908	10.541	0.390	0.415
C	4.318	4.572	0.170	0.180
D	0.635	0.965	0.025	0.038
E	1.169	1.397	0.046	0.055
G	1.702 BSC		0.067 BSC	
H	2.109	2.717	0.083	0.107
J	0.381	0.635	0.015	0.025
K	25.907	26.670	1.016	1.050
L	8.052	9.398	0.317	0.370
Q	3.556	3.937	0.140	0.155
U	11.893	12.827	0.468	0.505

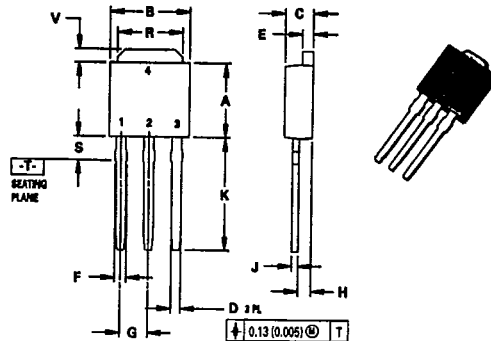
NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.



**DT-1 SUFFIX
PLASTIC PACKAGE
CASE 369-03**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	5.97	6.22	0.235	0.245
B	6.35	6.73	0.250	0.265
C	2.19	2.38	0.086	0.094
D	0.69	0.88	0.027	0.035
E	0.84	0.94	0.033	0.037
F	0.77	1.14	0.030	0.045
G	2.29 BSC		0.090 BSC	
H	0.97	1.06	0.038	0.042
J	0.46	0.58	0.018	0.023
K	8.89	9.65	0.350	0.380
R	5.21	5.46	0.205	0.215
S	1.91	2.28	0.075	0.090
V	0.89	1.27	0.035	0.050

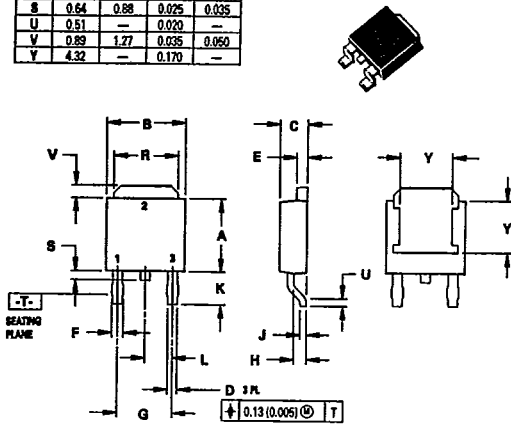
NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.



**DT SUFFIX
PLASTIC PACKAGE
CASE 369A-03
DPAK**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	5.97	6.22	0.235	0.245
B	6.35	6.73	0.250	0.265
C	2.19	2.38	0.086	0.094
D	0.69	0.88	0.027	0.035
E	0.84	0.94	0.033	0.037
F	0.77	1.14	0.030	0.045
G	4.58 BSC		0.180 BSC	
H	0.97	1.06	0.038	0.042
J	0.46	0.58	0.018	0.023
K	2.60	2.89	0.102	0.114
L	2.29 BSC		0.090 BSC	
R	5.21	5.46	0.205	0.215
S	0.64	0.88	0.025	0.035
U	0.51	—	0.020	—
V	0.89	1.27	0.035	0.050
Y	4.32	—	0.170	—

NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

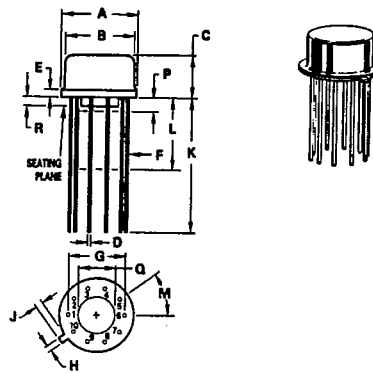


**H, G SUFFIX
METAL PACKAGE
CASE 603-04
R_{θJA} = 160°C/W
(TO-100)**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	8.51	9.29	0.335	0.370
B	7.25	8.51	0.285	0.335
C	4.19	4.70	0.165	0.185
D	0.407	0.533	0.016	0.021
E	—	1.02	—	0.040
F	0.406	0.483	0.016	0.019
G	5.84 BSC		0.230 BSC	
H	0.712	0.864	0.028	0.034
J	0.737	1.14	0.029	0.045
K	12.70	—	0.500	—
L	6.35	12.70	0.250	0.500
M	36° BSC		36° BSC	
P	—	1.27	—	0.050
Q	3.56	4.06	0.140	0.160
R	0.254	1.02	0.010	0.040

NOTE:
LEADS WITHIN 0.18 mm (0.007) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.

All JEDEC Dimensions and Notes Apply.

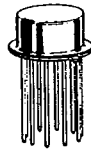
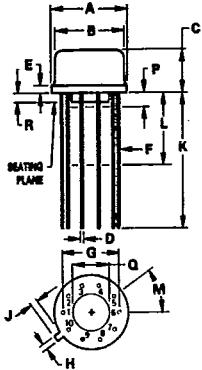


PACKAGE OUTLINE DIMENSIONS (continued)

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	8.51	9.39	0.335	0.370
B	7.75	8.51	0.305	0.335
C	4.19	6.73	0.165	0.265
D	0.407	0.533	0.016	0.021
E	—	1.02	—	0.040
F	0.406	0.483	0.016	0.019
G	5.84 BSC		0.230 BSC	
H	0.712	0.864	0.028	0.034
J	0.737	1.14	0.029	0.045
K	12.70	—	0.500	—
L	6.35	12.70	0.250	0.500
M	36° BSC		36° BSC	
P	—	1.27	—	0.050
Q	3.56	4.06	0.140	0.160
R	0.254	1.02	0.010	0.040

**G SUFFIX
METAL PACKAGE
CASE 603C-01**
R θ JA = 150°C/W (TYP)
(TO-100)

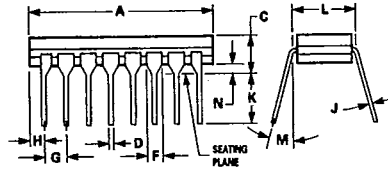
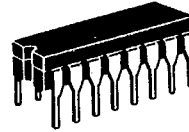
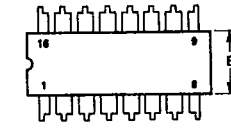
- NOTES:
1. LEADS WITHIN 0.18 mm (0.007) RADIUS OF TRUE POSITION TO DIM. "A" & "H" AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
2. LEAD DIA UNCONTROLLED BEYOND DIM "K" MIN.



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	19.05	19.94	0.750	0.785
B	6.10	7.49	0.240	0.295
C	—	5.08	—	0.200
D	0.38	0.53	0.015	0.021
F	1.40	1.78	0.055	0.070
G	2.54 BSC		0.100 BSC	
H	0.51	1.14	0.020	0.045
J	0.20	0.30	0.008	0.012
K	3.18	4.32	0.125	0.170
L	7.62 BSC		0.300 BSC	
M	—	15°	—	15°
N	0.51	1.02	0.020	0.040

**DP2, D, J, L, N SUFFIX
CERAMIC PACKAGE
CASE 620-10**
R θ JA = 100°C/W (TYP)

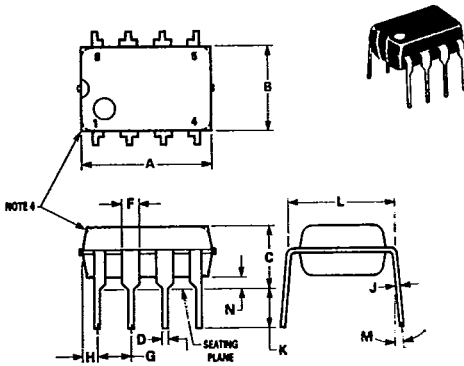
- NOTES:
1. LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
2. PACKAGE INDEX: NOTCH IN LEAD NOTCH IN CERAMIC OR INK DOT.
3. DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
4. DIM "A" AND "B" DO NOT INCLUDE GLASS RUN-OUT.
5. DIM "F" MAY NARROW TO 0.76 mm (0.030) WHERE THE LEAD ENTERS THE CERAMIC BODY.



**N, P1 SUFFIX
PLASTIC PACKAGE
CASE 626-05**
R θ JA = 100°C/W (TYP)

- NOTES:
1. LEAD POSITIONAL TOLERANCE:
 $\pm 0.13 (0.005) \text{ T A } \text{B}$
2. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
3. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
4. DIMENSIONS A AND B ARE DATUMS.
5. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

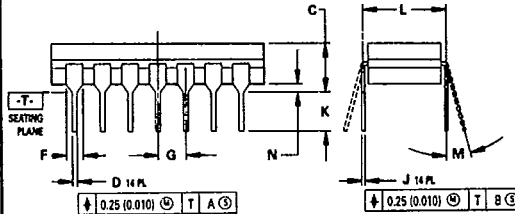
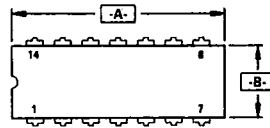
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.40	10.16	0.370	0.400
B	6.10	6.60	0.240	0.260
C	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.52	0.040	0.060
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	—	10°	—	10°
N	0.76	1.01	0.030	0.040



**L SUFFIX
CERAMIC PACKAGE
CASE 632-08**
R θ JA = 100°C/W (TYP)
(TO-116)

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
4. DIM F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC BODY.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	19.05	19.94	0.750	0.785
B	6.23	7.11	0.245	0.280
C	3.94	5.08	0.155	0.200
D	0.39	0.50	0.015	0.020
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
J	0.21	0.38	0.008	0.015
K	3.18	4.31	0.125	0.170
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.01	0.020	0.040

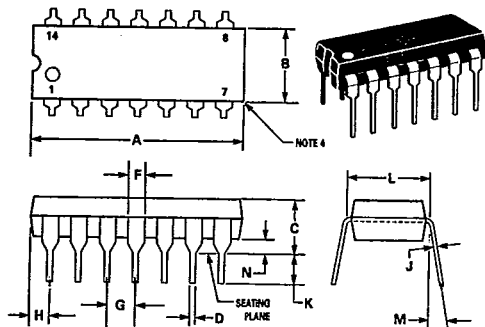


PACKAGE OUTLINE DIMENSIONS (continued)

**N, P, N-14, P2 SUFFIX
PLASTIC PACKAGE
CASE 646-06**
 $R_{\theta JA} = 100^{\circ}\text{C/W (TYP)}$

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.16	19.56	0.715	0.770
B	6.10	6.60	0.240	0.260
C	3.69	4.69	0.145	0.185
D	0.39	0.53	0.015	0.021
F	1.02	1.79	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	1.22	2.41	0.052	0.095
J	0.20	0.38	0.008	0.015
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	0°	10°	0°	10°
N	0.39	1.01	0.015	0.039

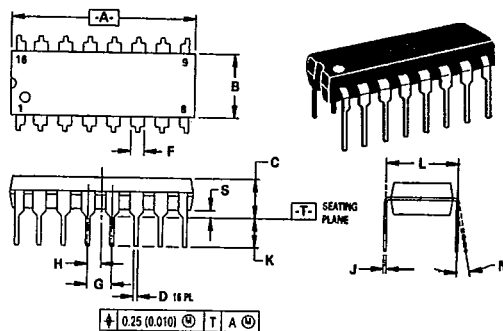
- NOTES:
- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
 - ROUNDED CORNERS OPTIONAL.



**N, P SUFFIX
PLASTIC PACKAGE
CASE 648-08**
 $R_{\theta JA} = 100^{\circ}\text{C/W (TYP)}$

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.80	19.55	0.740	0.770
B	6.35	6.85	0.250	0.270
C	3.69	4.44	0.145	0.175
D	0.39	0.53	0.015	0.021
F	1.02	1.77	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	1.27 BSC		0.050 BSC	
J	0.21	0.38	0.008	0.015
K	2.80	3.30	0.110	0.130
L	7.50	7.74	0.295	0.305
M	0°	10°	0°	10°
S	0.51	1.01	0.020	0.040

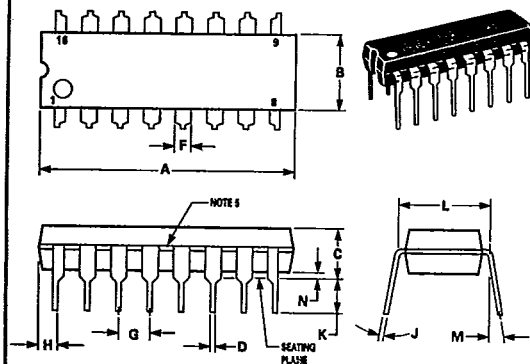
- NOTES:
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 - CONTROLLING DIMENSION: INCH.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
 - ROUNDED CORNERS OPTIONAL.



**P SUFFIX
PLASTIC PACKAGE
CASE 648C-02**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.80	21.34	0.740	0.840
B	6.10	6.50	0.240	0.260
C	3.69	4.69	0.145	0.185
D	0.39	0.53	0.015	0.021
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	0.39	2.41	0.015	0.095
J	0.20	0.38	0.008	0.015
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	0°	10°	0°	10°
N	0.39	1.01	0.015	0.040

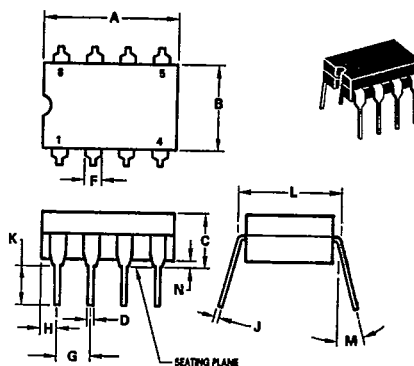
- NOTES:
- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
 - ROUNDED CORNERS OPTIONAL.
 - EXTERNAL LEAD CONNECTION, BETWEEN 4 AND 5, 12 AND 13 AS SHOWN.



**J-8, J, JG, U, Z SUFFIX
CERAMIC PACKAGE
CASE 693-02**
 $R_{\theta JA} = 100^{\circ}\text{C/W (TYP)}$

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.91	10.92	0.390	0.430
B	6.22	6.99	0.245	0.275
C	4.32	5.08	0.170	0.200
D	0.41	0.51	0.016	0.020
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
H	1.14	1.65	0.045	0.065
J	0.20	0.30	0.008	0.012
K	3.18	4.06	0.125	0.160
L	7.37	7.87	0.290	0.310
M	—	15°	—	15°
N	0.51	1.02	0.020	0.040

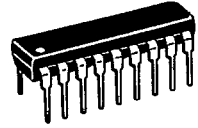
- NOTES:
- LEADS WITHIN 0.13 mm (0.005) RAD OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.



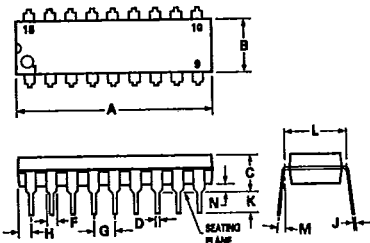
PACKAGE OUTLINE DIMENSIONS (continued)

**A, B, N, P SUFFIX
PLASTIC PACKAGE
CASE 707-02**
R_{θJA} = 100°C/W (TYP)

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	22.22	23.24	0.875	0.915
B	6.10	6.60	0.240	0.260
C	3.58	4.57	0.140	0.180
D	0.26	0.56	0.014	0.022
F	1.27	1.78	0.050	0.070
G	2.54 BSC		0.100 BSC	
H	1.02	1.52	0.040	0.060
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.02	0.020	0.040



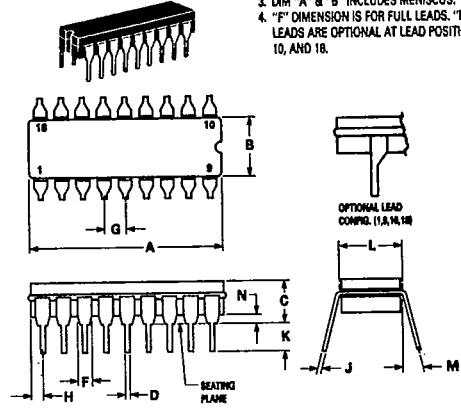
- NOTES:
1. POSITIONAL TOLERANCE OF LEADS (D), SHALL BE WITHIN 0.25mm(0.010) AT MAXIMUM MATERIAL CONDITION, IN RELATION TO SEATING PLANE AND EACH OTHER.
 2. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIMENSION B DOES NOT INCLUDE MOLD FLASH.



**J, L SUFFIX
CERAMIC PACKAGE
CASE 726-04**
R_{θJA} = 100°C/W (TYP)

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	22.35	23.11	0.880	0.910
B	6.10	7.49	0.240	0.295
C	5.08		0.200	
D	0.38	0.53	0.015	0.021
F	1.40	1.78	0.055	0.070
G	2.54 BSC		0.100 BSC	
H	0.51	1.14	0.020	0.045
J	0.20	0.30	0.008	0.012
K	3.18	4.32	0.125	0.170
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.02	0.020	0.040

- NOTES:
1. LEADS, TRUE POSITIONED WITHIN 0.25 mm (0.010) DIA. AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
 2. DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIM "A" & "B" INCLUDES MENISCUS.
 4. "F" DIMENSION IS FOR FULL LEADS. "HALF" LEADS ARE OPTIONAL AT LEAD POSITIONS 1, 9, 10, AND 18.

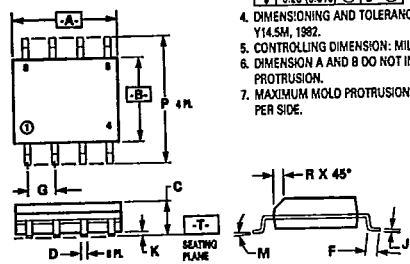


**D SUFFIX
CASE 751-03
PLASTIC PACKAGE
SO-8, SOP-8**
R_{θJA} = 190°C/W (SO-8)
R_{θJA} = 160°C/W (SOP-8)

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.198
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.008	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019



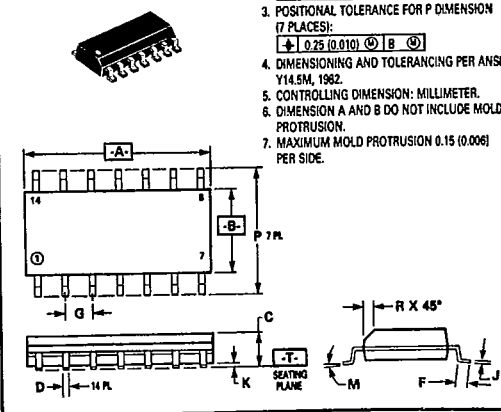
- NOTES:
1. DIMENSIONS A AND B ARE DATUMS AND T IS A DATUM SURFACE.
 2. POSITIONAL TOLERANCE FOR D DIMENSION (8 PLACES):
±0.25 (0.010) (M) T B (L) A (M)
 3. POSITIONAL TOLERANCE FOR P DIMENSION (4 PLACES):
±0.25 (0.010) (M) B (M)
 4. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 5. CONTROLLING DIMENSION: MILLIMETER.
 6. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 7. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.



**D SUFFIX
PLASTIC PACKAGE
CASE 751A-02
SO-14**
R_{θJA} = 145°C/W (TYP)

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	8.55	8.75	0.337	0.344
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.008	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

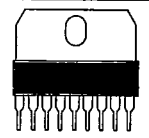
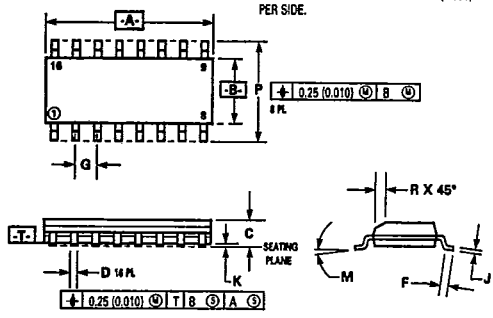
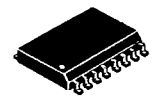
- NOTES:
1. DIMENSIONS A AND B ARE DATUMS AND T IS A DATUM SURFACE.
 2. POSITIONAL TOLERANCE FOR D DIMENSION (14 PLACES):
±0.25 (0.010) (M) T B (L) A (M)
 3. POSITIONAL TOLERANCE FOR P DIMENSION (7 PLACES):
±0.25 (0.010) (M) B (M)
 4. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 5. CONTROLLING DIMENSION: MILLIMETER.
 6. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 7. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.



**DW SUFFIX
PLASTIC PACKAGE
CASE 751G-01
SO-16L**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.15	10.45	0.400	0.411
B	7.40	7.60	0.292	0.299
C	2.35	2.65	0.093	0.104
D	0.35	0.49	0.014	0.019
F	0.50	0.90	0.020	0.035
G	1.27 BSC		0.050 BSC	
J	0.25	0.32	0.010	0.012
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	10.05	10.65	0.395	0.415
	0.25	0.75	0.010	0.029

- NOTES:
1. DIMENSIONS A AND B ARE DATUMS AND T IS A DATUM SURFACE.
 2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 3. CONTROLLING DIMENSION: MILLIMETER.
 4. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 5. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.



**CASE 762-01
PLASTIC MEDIUM
POWER PACKAGE
SIP-9**

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	22.40	23.00	0.873	0.907
B	6.40	6.80	0.252	0.269
C	3.45	3.65	0.135	0.143
D	0.40	0.55	0.015	0.021
E	0.35	0.60	0.009	0.024
F	1.40	1.60	0.055	0.062
G	2.54 BSC		0.100 BSC	
H	1.81	1.71	0.069	0.067
J	0.380	0.400	0.014	0.015
K	3.35	4.20	0.130	0.165
M	30° BSC		30° BSC	
N	2.50	2.70	0.099	0.106
Q	3.15	3.45	0.124	0.135
R	13.80	13.90	0.539	0.547
S	1.65	1.95	0.065	0.076
U	22.00	22.20	0.866	0.874
V	0.55	0.75	0.021	0.029
W	2.89 BSC		0.113 BSC	
X	0.85	0.75	0.033	0.029
Y	2.70	2.90	0.106	0.110

- NOTES:
1. DIMENSIONS A, AND C ARE DATUMS. AND -T- IS A DATUM PLANE.
 2. POSITIONAL TOLERANCE FOR LEAD DIMENSION D: $\pm 0.25 (0.010) \text{ (M)} | -T- | A \text{ (M)}$
 3. POSITIONAL TOLERANCE FOR LEAD DIMENSION J: $\pm 0.25 (0.010) \text{ (M)} | -T- | C \text{ (M)}$
 4. POSITIONAL TOLERANCE FOR LEAD DIMENSION Q: $\pm 0.25 (0.010) \text{ (M)} | -T- | A \text{ (M)}$
 5. DIMENSIONING AND TOLERANCING PER ANSI Y14.5, 1982.
 6. CONTROLLING DIMENSION: MILLIMETER.

