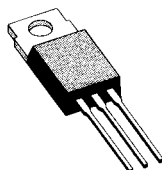


LOW-DROP VOLTAGE REGULATOR

- $V_O = 5V \pm 4\%$ ($I_O = 5mA$)
- $I_{OS} \geq 500mA$
- $V_I - V_O \leq 0.6V$ ($I_O = 500mA$)
- V_I (surge) = $\pm 80V$
- THERMAL AND SHORT-CIRCUIT PROTECTION



TO220
(Plastic Package)

ORDER CODE : TEA7605SP

DESCRIPTION

TEA7605 is a low-drop 5V regulator well suited to supplying stabilized voltage to μ Ps in harsh industrial environment.

Special care was taken to keep :

- Lowest possible quiescent current ($250\mu A$).
- Lowest possible output capacitor ($1\mu F$).

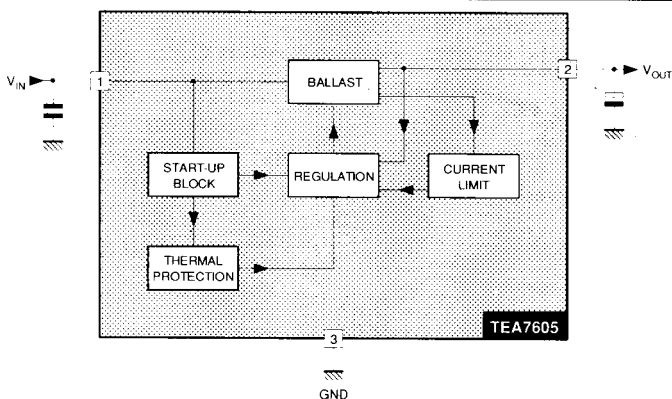
PIN CONNECTIONS

- 1 = V_I
2 = V_O
3 = GND



89DSTEA7605-01

BLOCK DIAGRAM



91DSTEA7605-02

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_I	Input Voltage - Continuous - $\tau = 300$ ms	30	V
		80	V
$V_{I(R)}$	Reverse Input Voltage - Continuous - $\tau = 120$ ms	- 18 - 80	V V
T_J	Operating Junction Temperature	- 45, +150	°C
T_{stg}	Storage Temperature	- 55, +150	°C

TAB-01

THERMAL DATA

$R_{th(j-c)}$	Junction-case Thermal Resistance	Max.	3	°C/W
$R_{th(j-a)}$	Junction-ambient Thermal Resistance	Max.	70	°C/W

TAB-02

ELECTRICAL OPERATING CHARACTERISTICS

$T_J = 25^\circ\text{C}$, $V_I = 14.4\text{V}$ (unless otherwise specified) Output Capacitor = $10\mu\text{F}$ (see note)

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_O	Output Voltage ($I_O = 5$ to 500mA)	4.875	5	5.125	V
V_I	Input Supply Voltage (permanent)			28	V
I_{CC}	Current Consumption $I_O = 0\text{mA}$ $I_O = 150\text{mA}$ $I_O = 500\text{mA}$		0.25 10 75	0.4 20 100	mA mA mA
K_{VI}	Line Regulation ($V_I = 6$ to 26V ; $I_O = 5\text{mA}$)		5	10	mV
K_{VO}	Load Regulation ($I_O = 5$ to 500mA)		40	60	mV
$V_I - V_O$	Drop-out Voltage $I_O = 150\text{mA}$ $I_O = 500\text{mA}$		0.18 0.4		V V
SVR	Supply Voltage Rejection ($I_O = 350\text{mA}$, $f = 120\text{Hz}$, $C_O = 1\mu\text{F}$, $V_I = 12 \pm 5\text{V}$)		60		dB
I_{OS}	Short-circuit Output Current	0.5	0.7		A

TAB-03

NOTE : Applications Hints

The output capacitor has a direct influence on output voltage stability. A $10\mu\text{F}$ capacitor will provide satisfactory results. There is no upper limit on this capacitor value.

If necessary, this value can be reduced down to $1\mu\text{F}$; however, in such case, it should be checked that output capacitor keeps sufficiently high capacitance and low equivalent series resistance in the whole temperature range.

Such low capacitor value is not recommended either, if output current is to switch abruptly from very high to very low values (for instance, 400mA to $< 1\text{mA}$).

ELECTRICAL OPERATING CHARACTERISTICS

$T_J = -45^\circ\text{C}$ to $+125^\circ\text{C}$, $V_I = 14.4\text{V}$ (unless otherwise specified) Output Capacitor = $10\mu\text{F}$

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_O	Output Voltage ($I_O = 5$ to 500mA)	4.8	5	5.2	V
$\frac{dV_O}{dt}$	Output Voltage Drift $T_J = -45$ to $+25^\circ\text{C}$ $T_J = +25$ to $+125^\circ\text{C}$	- 0.4 - 0.6			mV/°C
I_{CC}	Current Consumption $I_O = 0\text{mA}$ $I_O = 150\text{mA}$ $I_O = 500\text{mA}$			0.45 25 120	mA mA mA
K_{VI}	Line Regulation ($V_I = 6$ to 26V , $I_O = 5\text{mA}$)			20	mV
K_{VO}	Load Regulation ($I_O = 5$ to 500mA)			80	mV
$V_I - V_O$	Drop-out Voltage $I_O = 150\text{mA}$ $I_O = 500\text{mA}$		0.2	0.8	V V
I_{OS}	Short-circuit Output Current	0.4			A
I_{OM}	Maximum Output Current	0.5			A

TAB-04

APPLICATION DIAGRAM

